

US Citizen
Secret Security Clearance

Professional Summary

I am a VHDL FPGA Engineer with team lead and individual contributor experience.

Education

Clarkson University *Potsdam, NY*

May 2019

Bachelor of Science in Computer Engineering

Work Experience

SRC Inc *Syracuse, NY*

October 2023 – Present

Acting Lead Firmware Engineer

- Interview team member for the Firmware Engineering department; performed over 20 interviews, evaluating technical skill and a candidate's overall fit for the company and department.
- Act in a lead role to oversee a team of 2-3 engineers; lead daily standups and assign bi-weekly Sprint tasking.
- Act as the responsible individual for Functional Overview architecture and documentation.
- Oversee Detailed Design architecture and documentation, coding and simulation, and hardware integration.
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SRC Inc *Syracuse, NY*

June 2019 – Present

Firmware Engineer

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Skills

- Functional: Mentorship, Tasking, Sprint Standups
- Languages: Python, VHDL (CocoTB Simulation, Hardware Integration)
- Standards:
- Software: \LaTeX , Git, Perforce, MS Visio, Linux
- Hardware: