24 Pin flat ribbon connector for V.D.U. (S1) :-

pin	function	pin	function
1	A0	24	D0
2	A1	23	D1
3	A2	22	D2
4	A3	21	D3
5	A4	20	D4
6	A5	19	D5
7	A6	18	D6
8	A7	17	D7
9	A8	16	R/W
10	A9	15	E Clk.
11	Reset-	14	8000-9FFF Select-
12	A12	13	IRQ-

(Note: A '-' following a function indicates active low.)

P.I.A. Connector on V.D.U. board:-

```
1 - Strobe        Keyboard inputs to port A.
2 - bit 0           ( active high strobe )
3 - bit 1
4 - bit 2
5 - bit 3
6 - bit 4
7 - bit 5
8 - bit 6
9 - bit 7         ( leave disconnected or tie to +5 v.)
18 - +5
19 - -12v
20 - GND
```

Teletext board address

9000 — 93BF   Screen memory Ram

93C0 — 93FB   Teletext scratch RAM

93FC — 93FF   PIA

NOTES
1) All unmarked capacitors 10nF Ceramic
2) —⊢ denotes Molex connectors

2u2
33u

PLL
⏀024
1K0
470R
2K2
68p
62p
⏀044
÷

TT CHARACTER GEN
SAA5050

3K9
3K9
3K9
3K9

TT TIMING
SAA5020
Hex Non-Inv Buffer
4050

RAM
2114
RAM
2114

OCTAL BUS Tx/Rx
74LS245

MICRO SOCKET

4 x 2 MULTIPLEX
74LS157
74LS157
74LS157
74LS157

4 x 2 NAND
74LS00

74LS283

DUAL SYNCRO U/D COUNTER
⏀4520

4 x 2 EOR
74LS86

2 x DF/F
4013

4 Bit Full Adder
74LS283

3 x 3 NAND
74LS10

PIA
6821

÷8
CD4022

2 x DF/F
74LS74

1 x 8 NAND
74LS30

74LS283

82R
150R
47K
47K
BC547
2K2
4K7
12K
68R
2K2
BC558
BC558

82R
68R
5K6
2K2
82R
BC558
BC558

2u2
BC558
5K6
68R
22K
33u
2K2
BC558
22u
22K
82R
BC558
5K6
68
2K2
BC558
82R

÷8

TELETEXT V.D.U. Top

NOTES:-
1- All holes not containing a component lead are feed thus and require a length of wire to be inserted and soldered on both sides.

2- None of the socket pads require top soldering.

6802 POLYWOG

TELETEXT VDU CIRCUIT DIAGRAM