Andrey Kurenkov

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EDUCATION

Georgia Institute of Technology, Atlanta GA August 2011 – May 2015

- Dual major: B.S. in Electrical Engineering, B.S. in Computer Science | Research Option
- GPA: **3.88** | CS GPA: **4.0**
- GRE**:** quantitative **170/170 (98th percentile),** verbal **168/170 (98th percentile),** writing **5.0/6.0 (93rd percentile)**

EXPERIENCE

Oracle, Santa Clara CA

Software Engineer, ZFS Storage Appliance Observability Group June 2015 – Present

- Implementing an analytics service that collects and aggregates data from a scalable number of storage appliances
- Working with Docker, Flask, and multiple messaging frameworks with an in-development microservice framework

Georgia Institute of Technology, College of Computing/School of ECE, Atlanta GA

Research Assistant, Socially Intelligent Machines Lab August 2013 – May 2015

- Published as lead author ("An Evaluation of GUI and Kinesthetic Teaching Methods for Constrained-Keyframe Skills", IROS 2015); wrote software, ran a user study with a humanoid robot, and completed and revised the paper.
- Improved C++ object segmentation code to enable future research, integrated ROS DMP functionality with existing Java code for simpler robot control, and contributed to the lab's software in multiple additional ways.

Teaching Assistant, Intro to Object Oriented Programming and Intro to Artificial Intelligence May 2012 – May 2015

- Held weekly office hours, taught recitations, implemented projects, and graded tests to assist teaching AI and OOP concepts

Opportunity Research Scholars Undergraduate Research August 2012 – May 2013

- Designed and implemented a robust MapReduce simulation framework that efficiently models concurrent processes.

École Polytechnique Fédérale de Lausanne, Lausanne Switzerland May 2014 – August 2014

EPFL Summer Research Intern, Microelectronic System Lab

- Modeled the lab's memristor technology using VerilogA, in order to simulate their behavior in new research initiatives.
- Developed simulations in ADE-L and Matlab to evaluate memristor applications in logic calculation and machine learning.
- Designed a novel CMOS circuit implementation of an abstract neuron model, and evaluated its performance with ADE-L.

Carnegie Mellon University, Pittsburgh PA June 2013 – August 2013

Robotics Institute Summer Scholars Research Intern, Personal Robotics Lab

- Implemented a planning-based task execution framework with extensive data logging for smarter robot behavior.
- Researched, designed, and implemented a machine learning approach for error avoidance during task execution.

TEAM PROJECTS

GT Solar Racing Car Team, *Software Lead, Electrical subteam member* August 2011 – May 2015

- Supervised and directed a group that developed high quality telemetry and control software with TI's C2000 Picollo chips.
- Collaborated with a partner on motor control software as well as others for electrical prototyping, testing, and debugging.

Other: FIRST Robotics (Aug 2009 – May 2011), **RoboJackets** (Aug 2011-May 2012), **IEEE (**Aug 2011 – May 2013)

SKILLS

Experienced at working independently and within larger teams. Self-motivated to learn new skills and meet time-constraints.

Programming: C, Java, Python, Matlab, Octave, Android, ROS, Eclipse, Vim, svn, git, Linux, Latex
Electronics: PIC24, TIC2000, Arduino, soldering, common tools (oscilloscopes, multimeters, power supplies)
Learning: Udacity Data Science Nanodegree, Coursera (Machine Learning and Programming Languages), Mandarin

AWARDS

Georgia Tech's President's Undergraduate Research Award Spring 2014, Fall 2014
IEEE PES Scholarship Plus Recipient 2012