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B. Naroska

TRIGGER WORDS

The trigger word is the 10th (contrary to J.C.N. 23a) half word after the bank length in the bank "TRIG", number 1.

It tells you the T1 condition of the trigger.

At the moment the bits have the following meaning (bits 1 - 8 are T1 POSTPONE, bits 9 - 16 T1 ACCEPT)

BIT	OCTAL(NORD-DISPL)	HEX(IBM-DISPL)	CONTENT	
1	1	1	2TOF•LG>1GeV	} T1 POSTPONE
2	2	2	TAGG	
5	20	10	2TOF•≥4TOF COLL	
9	400	100	LUMI	} T1 ACCEPT
10	1000	200	LG>4GeV	
11	2000	400	TAGG•LG>2GeV	

(TAGG is the "OR" of the 2 tagging leadglass arrays, LUMI the "AND")

All combinations of these bits are also possible, eg bit 9 automatically implies bit 2.

To find out what caused the trigger (especially if it was caused by T2), you may also use the "TRIGGER ACTION & LOGICS CONDITIONS" word, which is read out for each event. It is half word 22 after the banklength in the bank "HEAD" number 0.

BIT	CONTENT
1	T1 ACTUAL ACCEPT
2	" " "
3	" " "
4	T1 LOGICS CONDITION ACCEPT
5	T1 " " POSTPONE
6	T2 " " ACCEPT
7	T2 " " POSTPONE
8	T3 " " ACCEPT

T2 LOGICS CONDITION is true for

2TOF•LG>1GeV • 1 track

TAGG • 1 track

2TOF • ≥4TOF • 2 fast tracks
COLL

