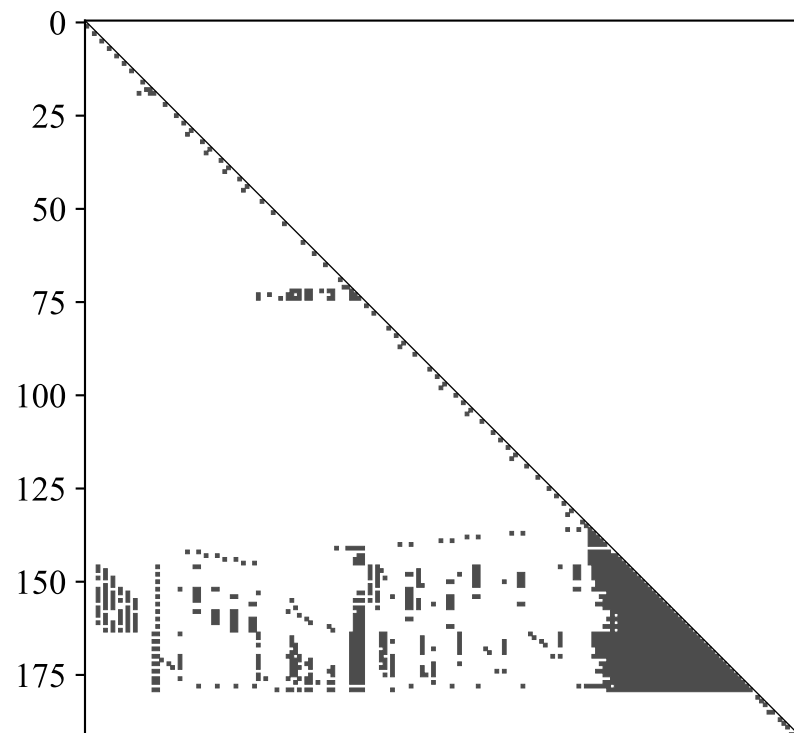
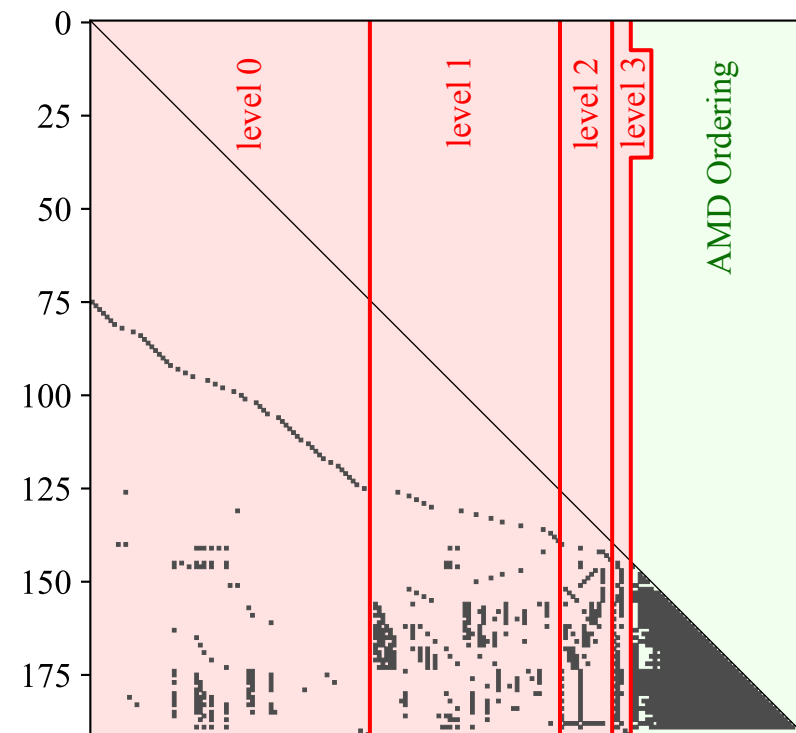


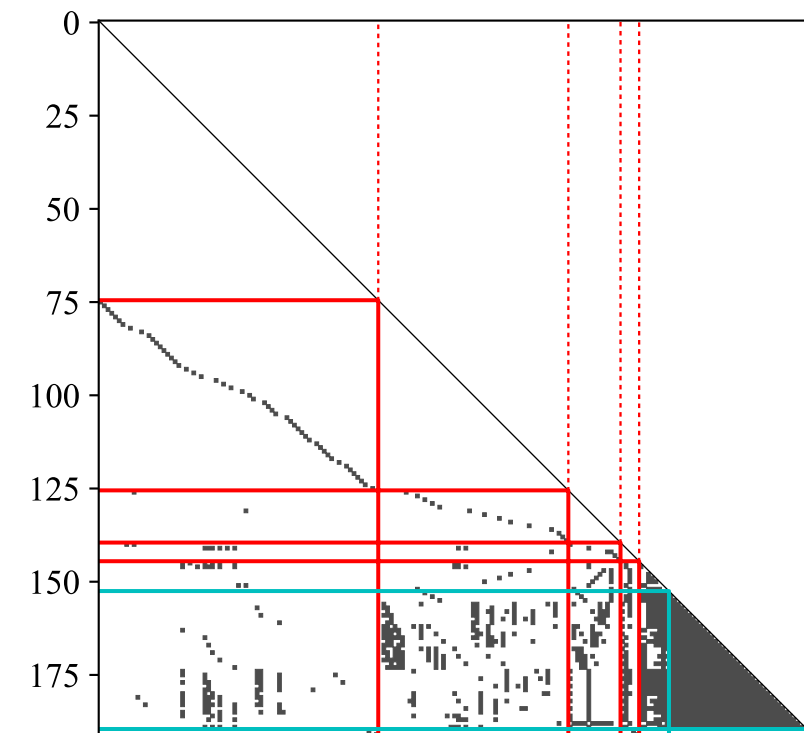
a) LDL decomp. with AMD ordering



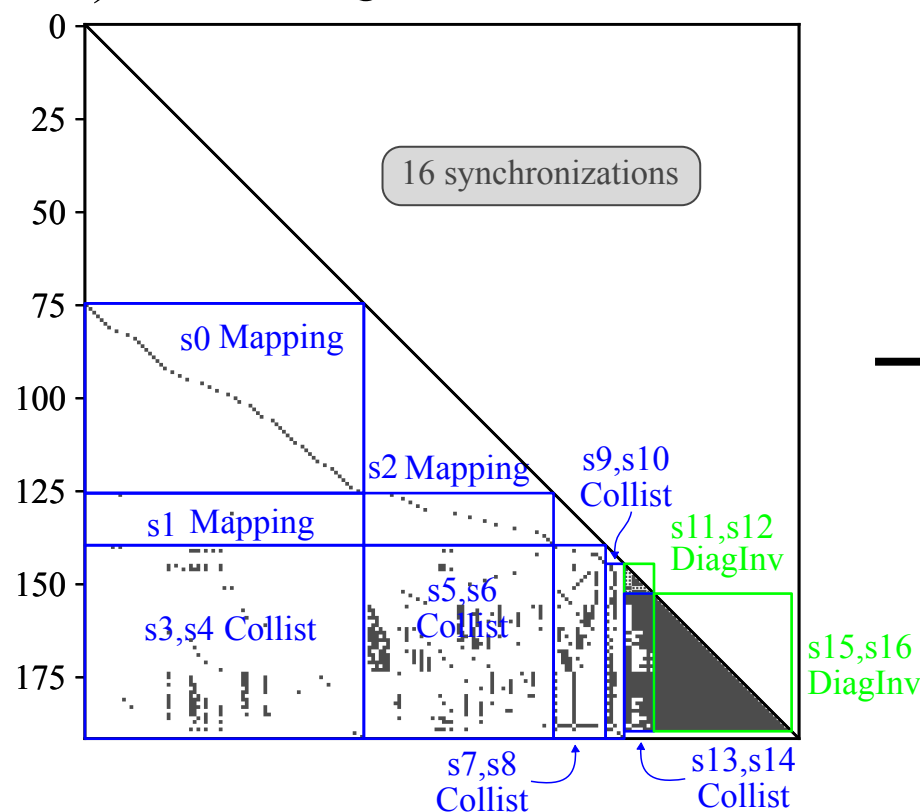
b) partial level scheduling (ASAP)



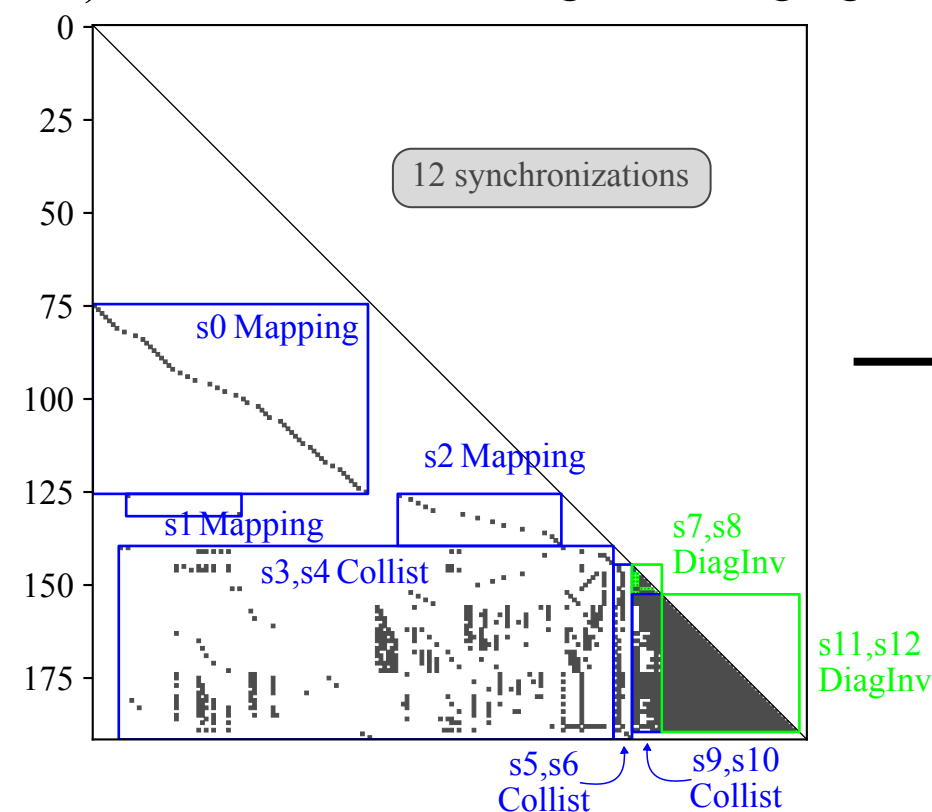
c) graph-based tiling



d) kernel assignment



e) ALAP tile scheduling and merging



f) intra-kernel workload parallelization

