

DSP HW3 Report

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Enviroment:

Mac OSX

How to Compile:

make all

Compile mydisambig

How to execute:

make map

Generate ZhuYin-Big5.map

make run

Generate result2/1.txt – 10.txt

What I have done:

1. Compile SRILM on Mac OSX
2. Use TA's code generate segmental data
3. Use SRILM generate bigram.lm
4. My code:

map.py:

Use python3 to convert Big5-Zhuyin.map to Zhuyin-Big5.map.

mydisambig.cpp:

My disambig c++ implementation.

Makefile:

Makefile parameters: MACHINE_TYPE, SRIPATH, LM.

5. Use SRILM disambig generatre result1/1.txt – 10.txt