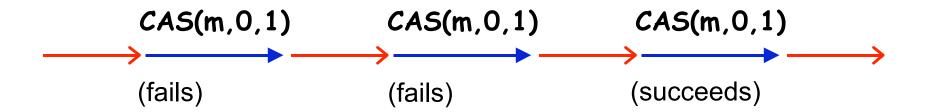
Exploiting Purity for Atomicity

Busy Acquire

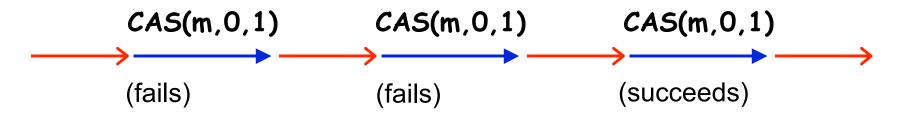
```
atomic void busy_acquire() {
  while (true) {
    if (CAS(m,0,1)) break;
                        if (m == 0) {
                         m = 1; return true;
                        } else {
                         return false;
```

Busy Acquire

```
atomic void busy_acquire() {
   while (true) {
     if (CAS(m,0,1)) break;
   }
}
```



Non-Serial Execution:



Serial Execution:

$$\xrightarrow{CAS(m,0,1)}$$

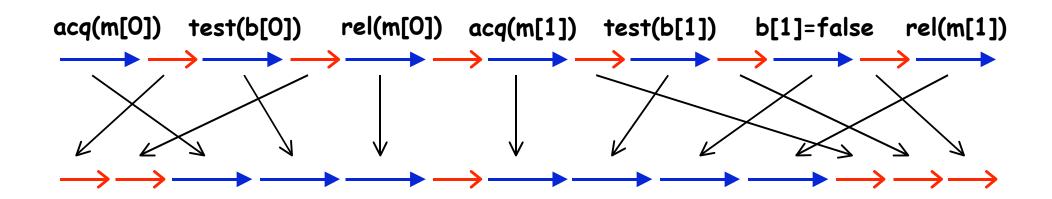
$$\xrightarrow{}$$
(succeeds)

Atomic but not reducible

alloc

```
boolean b[MAX]; // b[i] == true iff block i is free
Lock m[MAX];
atomic int alloc() {
  int i = 0;
  while (i < MAX) {
    acquire(m[i]);
    if (b[i]) {
      b[i] = false;
      release(m[i]);
      return i;
    release(m[i]);
    i++;
  return -1;
```

alloc



alloc is not Atomic

 There are non-serial executions with no equivalent serial executions

```
m[0] = m[1] = 0; b[0] = b[1] = false;
t = alloc(); || free(0); free(1);
                                 void free(int i) {
                                  acquire(m[i]);
                                   b[i] = true;
                                  release(m[i]);
```

```
m[0] = m[1] = 0; b[0] = b[1] = false;
t = alloc(); || free(0); free(1);
```

Non-Serial Execution:

$$\frac{\text{loop for b[0]} \quad \text{free(0)} \quad \text{free(1)} \quad \text{loop for b[1]}}{} + = 1$$

Serial Executions:
 loop for b[0] loop for b[1] free(0) free(1)

Extending Atomicity

- Atomicity doesn't always hold for methods that are "intuitively atomic"
 - serializable but not reducible (busy_acquire)
 - not serializable (alloc)
- Examples
 - initialization
 - resource allocation
 - wait/notify

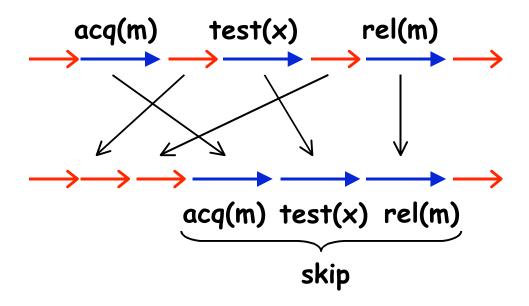
- caches
- commit/retry transactions

Pure Code Blocks

- Pure block: pure { E }
 - $-\mathbf{E}$ is reducible in normally terminating executions
 - If E terminates normally, it does not update state visible outside of E
- Example

```
while (true) {
   pure {
     acquire(mx);
     if (x == 0) { x = 1; release(mx); break; }
     release(mx);
   }
}
```

Purity and Abstraction



- Abstract execution semantics:
 - treat normal execution of pure blocks as the skip statement

Abstraction

- Abstract semantics that admits more behaviors
 - pure blocks can be skipped
 - hides "irrelevant" details (ie, failed loop iters)

 Program must still be (sequentially) correct in abstract semantics

Abstract semantics make reduction possible

Busy Acquire

```
atomic void busy_acquire() {
   while (true) {
     pure { if (CAS(m,0,1)) break; }
   }
}
```

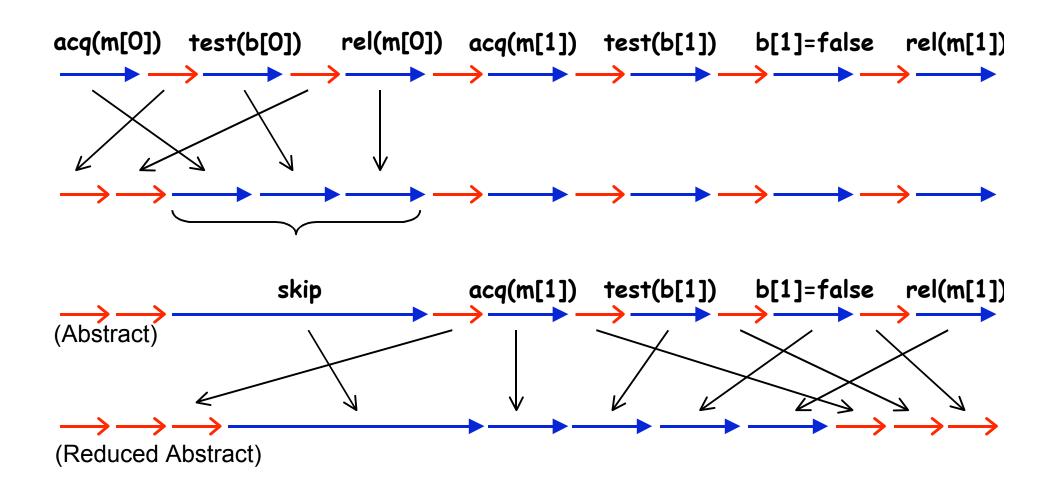
Abstract Execution of Busy Acquire

```
atomic void busy_acquire() {
      while (true) {
        pure { if (CAS(m,0,1)) break; }
                          CAS(m,0,1)
         CAS(m,0,1)
                                          CAS(m,0,1)
(Concrete)
             skip
                              skip
                                          CAS(m,0,1)
(Abstract)
(Reduced Abstract)
```

alloc

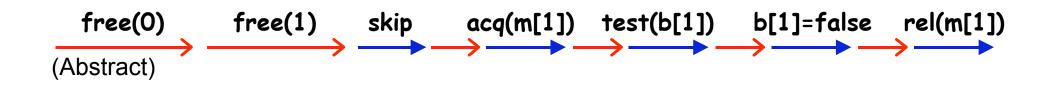
```
atomic int alloc() {
  int i = 0;
  while (i < MAX) {
    pure {
      acquire(m[i]);
      if (b[i]) {
        b[i] = false;
        release(m[i]);
        return i;
      release(m[i]);
    i++;
  return -1;
```

Abstract Execution of alloc



Abstraction

Abstract semantics admits more executions



- Can still reason about important properties
 - "alloc returns either the index of a freshly allocated block or -1"
 - cannot guarantee "alloc returns smallest possible index"
 - but what does this really mean anyway???

Type Checking

```
atomic void deposit(int n) {
   acquire(this);
   int j = bal;
bal = j + n;
B
                                    ((R;B);B);L =
   release(this);
                                    R;L =
atomic void depositLoop() {
 while (true) {
  deposit(10);
  A
  (A)^* = C \Rightarrow ERROR
```

alloc

```
boolean b[MAX];
Lock m[MAX];
atomic int alloc() {
  int i = 0;
  while (i < MAX) {
    acquire(m[i]);
    if (b[i]) {
      b[i] = false;
      release(m[i]);
      return i;
    release(m[i]);
    i++;
  return -1;
```

Type Checking with Purity

```
atomic int alloc() {
  int i = 0;
  while (i < MAX) {
    pure {
       acquire(m[i]);
       if (b[i]) {
        b[i] = false;
         release(m[i]);
         return i;
       release(m[i]);
    i++;
  return -1;
}
```

```
atomic void init() {
  if (x != null) return;
  acquire(l);
  if (x == null) x = new();
  release(l);
}
```

```
atomic void init() {
  if (x != null) return;
                              _ conflicting accesses
  acquire(1);
  if (x == null)(x) = new();
  release(1);
       test(x)
                         test(x)
                                 x=new()
                 acq(1)
                                            rel(1)
                                                (Concrete)
```

```
atomic void init() {
  if (x != null) return;
  acquire(l);
  if (x == null) x = new();
  release(l);
}

test(x) acq(l) test(x) x=new() rel(l)
  (Concrete)
```

```
atomic void init() {
  pure { if (x != null) return; } B \uparrow A acquire(1); if (x == null) x = new(); A \uparrow A
   release(1);
     test(x)
                 acq(I)
                            test(x) x=new()
                                                      rel(I)
                                                            (Concrete)
      skip
                  acq(I)
                             test(x) x=new()
                                                      rel(I)
                                                             (Abstract)
                                                    (Reduced Abstract)
```

Modifying local variables in pure blocks

- Partition variables into global and local variables
- Allow modification of local variables

```
pure { acq(m); rel(m); }

\cong skip

local x; pure { acq(m); x = z; rel(m); }

global z; \cong pure \{ x = z; \}

\cong x = z0;

local x1, x2; pure { acq(m); x1 = z; x2 = z; rel(m); }

global z; \cong pure \{ x1 = z; x2 = z; \}

\cong x1 = z0; x2 = z0
```

```
atomic void apply_f() {
  int x, fx;
  while (true) {
     acq(m);
     x = z
     rel(m);
     fx = f(x);
     acq(m);
     if (x == z) \{ z = fx; rel(m); break; \}
     rel(m);
```

```
atomic void apply_f() {
   int x, fx;
   while (true) {
     pure {
        acq(m);
        x = z;
        rel(m);
     fx = f(x);
     pure {
        acq(m);
        if (x == z) \{ z = fx; rel(m); break; \}
        rel(m);
                                                                                  28
```

```
atomic void apply_f() {
  int x, fx;
  while (true) {
     pure {
        acq(m);
        x = z;
        rel(m);
     fx = f(x);
     pure {
        acq(m);
        if (x == z) \{ z = fx; rel(m); break; \}
        rel(m);
```

- The pure blocks allow us to prove apply_f abstractly atomic
- We can prove on the abstraction that z is updated to f(z) atomically

```
atomic void apply_f() {
                                                atomic void apply_f() {
  int x, fx;
                                                   int x, fx;
  while (true) {
                                                   while (true) {
     pure {
        acq(m);
                                                      skip;
        x = z;
        rel(m);
     fx = f(x);
                                                      fx = f(x);
     pure {
                                                      if (*)
        acq(m);
                                                         // normal execution
        if (x == z) \{ z = fx; rel(m); break; \}
                                                         skip;
        rel(m);
                                                      else
                                                         // exceptional execution
                                                         acq(m); assume(x==z);
                                                         z =fx; rel(m); break;
```

Lock-free synchronization

- Load-linked: x = LL(z)
 - loads the value of z into x
- Store-conditional: f = SC(z,v)
 - if no SC has happened since the last LL by this thread
 - store the value of v into z and set f to true
 - otherwise
 - set f to false

Scenarios

$$x = LL(z)$$
 \Rightarrow
 $f = SC(z,v)$
Success

$$x = LL(z)$$

$$f' = SC(z,v')$$

$$f = SC(z,v)$$
Failure

$$x = LL(z)$$

$$f' = SC(z,v')$$

$$f = SC(z,v)$$
Failure

Lock-free atomic increment

```
atomic void increment() {
  int x;
  while (true) {
    x = LL(z);
    x = x + 1;
    if (SC(z,x)) break;
  }
}
```

Modeling LL-SC

- Global variable zSet initialized to { }
 - contains ids of threads who have performed the operation LL(z) since the last SC(z,v)

```
x = LL(z) \cong x = z; zSet = zSet \cup \{ tid \};

f = SC(z,v) \cong if (tid \in zSet)

\{ z = v; zSet = \{ \}; f = true; \}

else

\{ f = false; \}
```

Modeling LL-SC

- Global variable zSet initialized to { }
 - contains the id of the unique thread that has performed the operation LL(z) since the last SC(z,v) and whose SC(z,v') is destined to succeed

```
x = LL(z) \simeq
LL-Success(x,z) { assume(zSet = {}); zSet = { tid }; x = z; }
              else
      f = SC(z, v)^{x = z;}
              if (tid \in zSet)
                {z = v; zSet = {}; f = true; }
              else
                 { f = false: }
```

Modeling LL-SC

- Global variable zSet initialized to { }
 - contains the id of the unique thread that has performed the operation LL(z) since the last SC(z,v) and whose SC(z,v') is destined to succeed

```
x = LL(z) \cong

if (*)

LL-Success(x,z);

else

x = z;

f = SC(z,v) \cong

if (tid \in zSet)
```

- LL-Success(x,z) is a right mover
- SC(z,v) is a left
 mover
 provided stores to z

... provided stores to z performed only through SC [Wang-Stoller 2005]

 ${z = v; zSet = {}; f = true; }$

Lock-free atomic increment

```
atomic void increment() {
atomic void increment() {
  int x;
                                     int x;
                                     while (true) {
  while (true) {
                                        if (*)
     x = LL(z);
                                           LL-Success(x,z);
     x = x + 1;
     if (SC(z,x)) break;
                                        else
                                           x = z;
                                        x = x + 1;
                                        if (SC(z,x)) break;
```

Lock-free atomic increment

```
atomic void increment() { atomic void increment() {
  int x;
                                int x;
  while (true) {
                                while (true) {
     pure {
                                   pure {
        if (*)
                                      if (*)
          LL-Success(x,z);
                                        LL-Success(x,z)
                                        x = x + 1;
        else
                                                            B↑(R;B;L)
                                        // SC succeeds
          x = z;
        x = x + 1;
                                        SC(z,x); break;
        if (SC(z,x)) break;
                                     else
                                        x = z; x = x + 1;
                                        // SC fails
```

Atomicity and Purity Effect System

- Enforces properties for abstract semantics
 - pure blocks are reducible and side-effect free
 - atomic blocks are reducible
- Leverages other analyses
 - race-freedom
 - control-flow
 - side-effect

Summary

- Atomicity
 - enables sequential analysis
 - common in practice
- Purity enables reasoning about atomicity at an abstract level
 - matches programmer intuition
 - more effective checkers