Lab 6: Simple Calculator (Pre-Lab)

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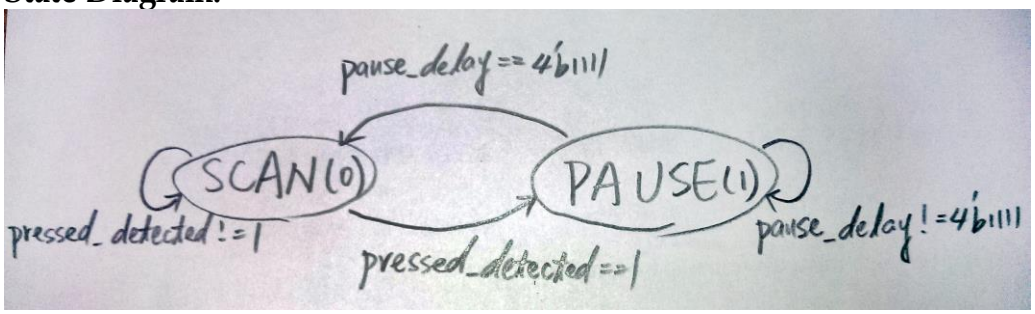
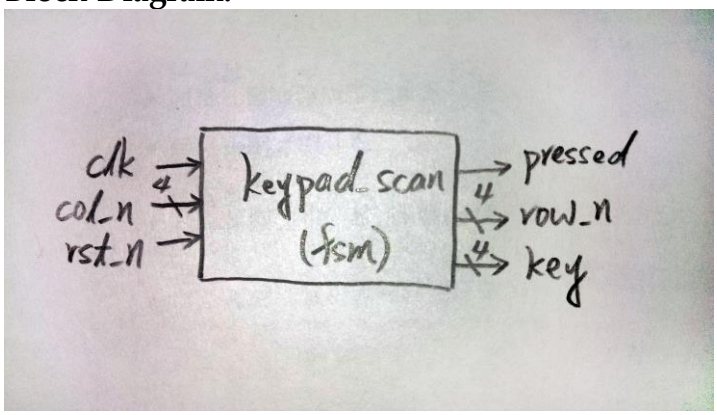
Design Specification

- ✓ **I/Os:**
  Inputs: clk, rst_n, [3:0] col_n
  Outputs: pressed, [3:0] row_n, [3:0] key
- ✓ **State Diagram:**



- ✓ **Block Diagram:**



Simulation