Controlpath logic table

regwrite	1	1	1	1	1	1	1	0	1	0	0	0
ALUsrc	0	0	0	0	0	0	1	1	1	0	0	0
ALOp	000	001	010	010	011	100	1111	1111	100	000	101	000
memtoreg	00	00	00	00	00	00	01	00	00	00	00	10
memread	0	0	0	0	0	0	1	0	0	0	0	0
memwrite	0	0	0	0	0	0	0	1	0	0	0	0
nextctrl	00	00	00	00	00	00	01	01	00	00	00	10
ractrl	0	0	0	0	0	0	0	0	0	П	0	_
sitctri	00	00	10	11	00	00	00	00	00	00	00	00
beqctrl	0	0	0	0 0	0	0	0	0	0	0	1	0
jrctri	0	0	0	0	0	0	0	0	0	1	0	_
jalctri	0	0	0	0	0	0	0	0	0	0	1	,
jctrl	0	0	0	0	0	0	0	0	0	0	1	
						$\mathbb{R}$		П	П	$_{ m JR}$	ſ	_
						$_{ m SL}$						