Controlpath logic table

regwrite	1	1	1	1	1	1	1	0	1	0	0	C
ALUsrc	0	0	0	0	0	0				0	0	0
$_{ m ALUop}$	000	001	010	010	011	100	1111	1111	100	000	101	000
memtoreg	00	00	00	00	00	00	01	00	00	00	00	10
memread	0	0	0	0	0	0	1	0	0	0	0	0
memwrite	0	0	0	0	0	0	0	1	0	0	0	0
nextctrl	00	00	00	00	00	00	01	01	00	00	00	10
ractrl	0	0	0	0	0	0	0	0	0	1	0	0
sltctrl	00	00	10	11	00		00	00	00	00	00	00
beqctrl	0	0	0	0	0	0 0 0 0	0	0	0	0	П	0
jrctrl	0	0	0	0	0	0	0	0	0	1	0	0
jalctrl	0	0	0	0	0	0	0	0	0	0	П	_
jetrl	0	0	0	0	0	0	0	0	0	0	П	0
Ъе								Н	Ι	$_{ m JR}$	ſ	<u> </u>
instruction	add	nand	slt_0	slt -1	$_{\rm sl}$	$_{ m SL}$	lw	$^{ m SW}$	addi	jr JI	bed	[8]