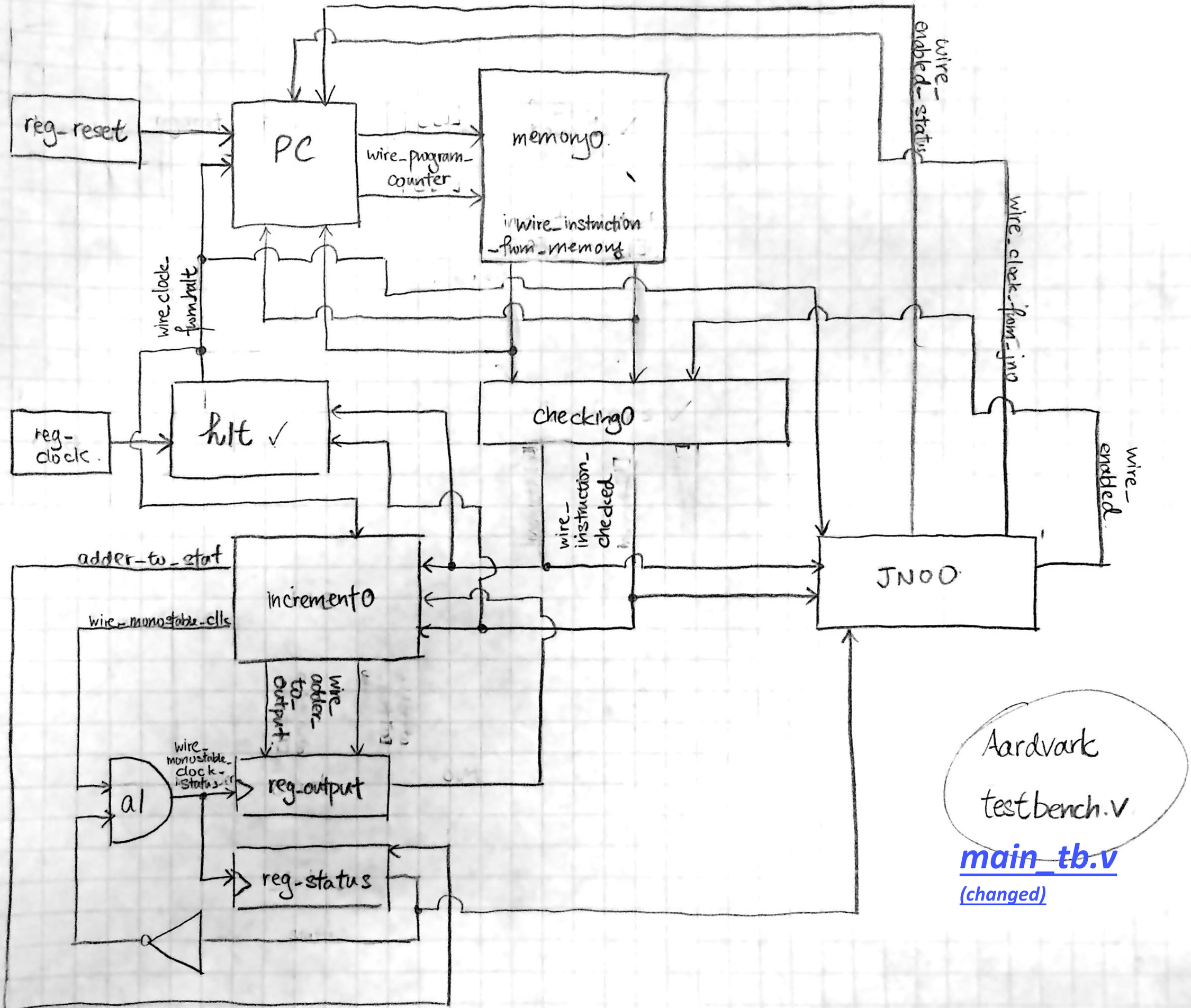
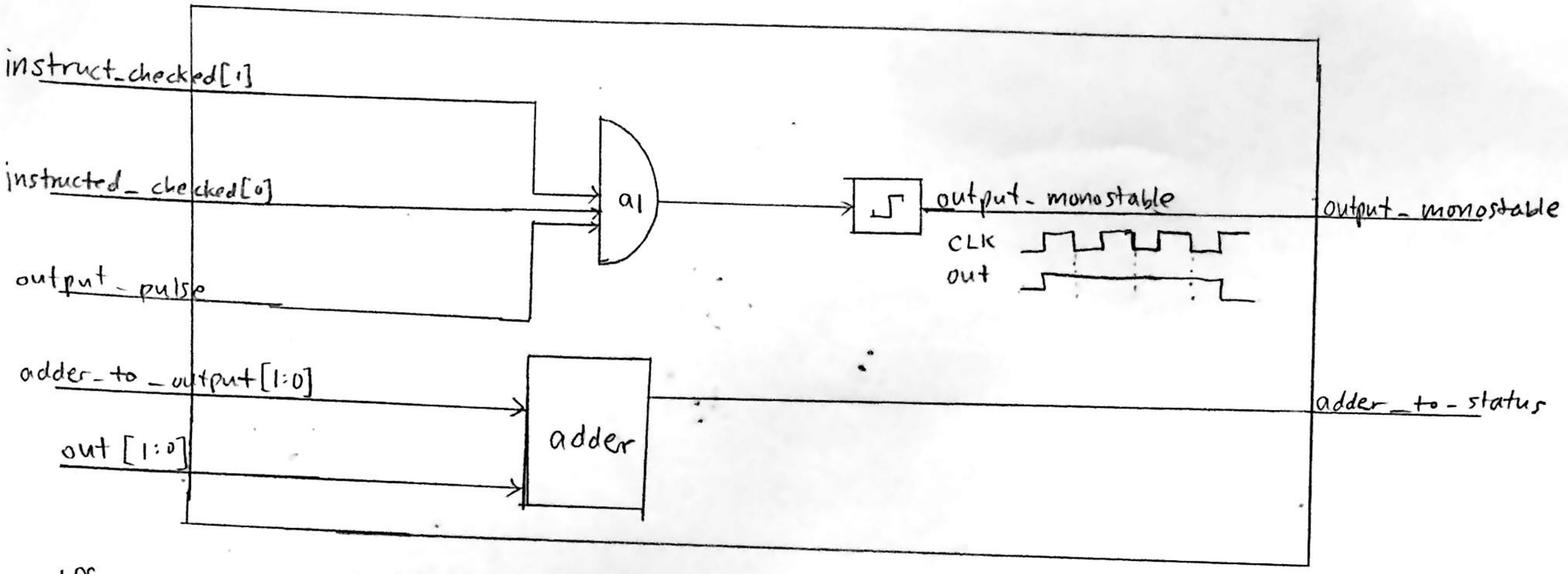
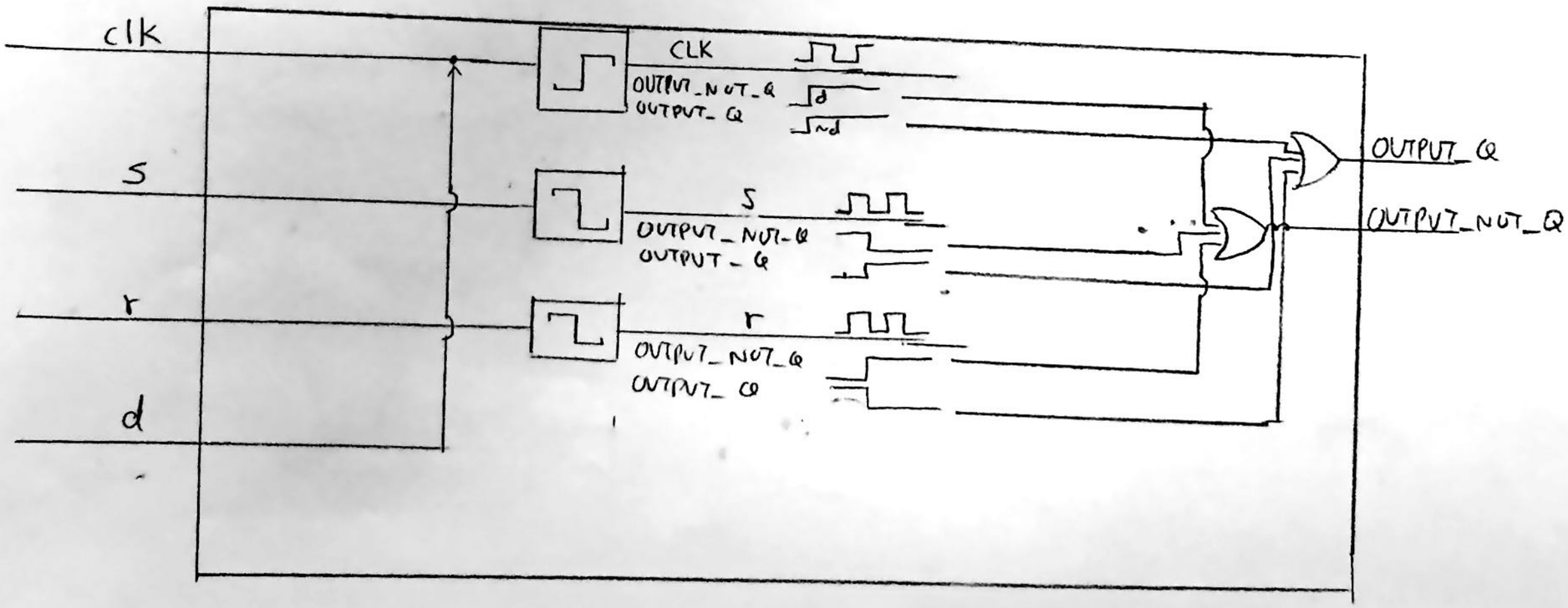

reg_reset
PC
wire_program_counter
memory0
in wire_instruction_from_memory
wire_enabled_status
wire_clock_from_jno
wire_enabled_
wire clock_from_halt
reg_clock
halt ✓
checking0
wire_instruction_checked
JNO0
adder_to_start
increment0
wire_monostable_clks
wire_adder_to_output
wire_monostable_clock_status
a1
reg_output
reg_status
Aardvark
testbench.v
main_tb.v
(changed)

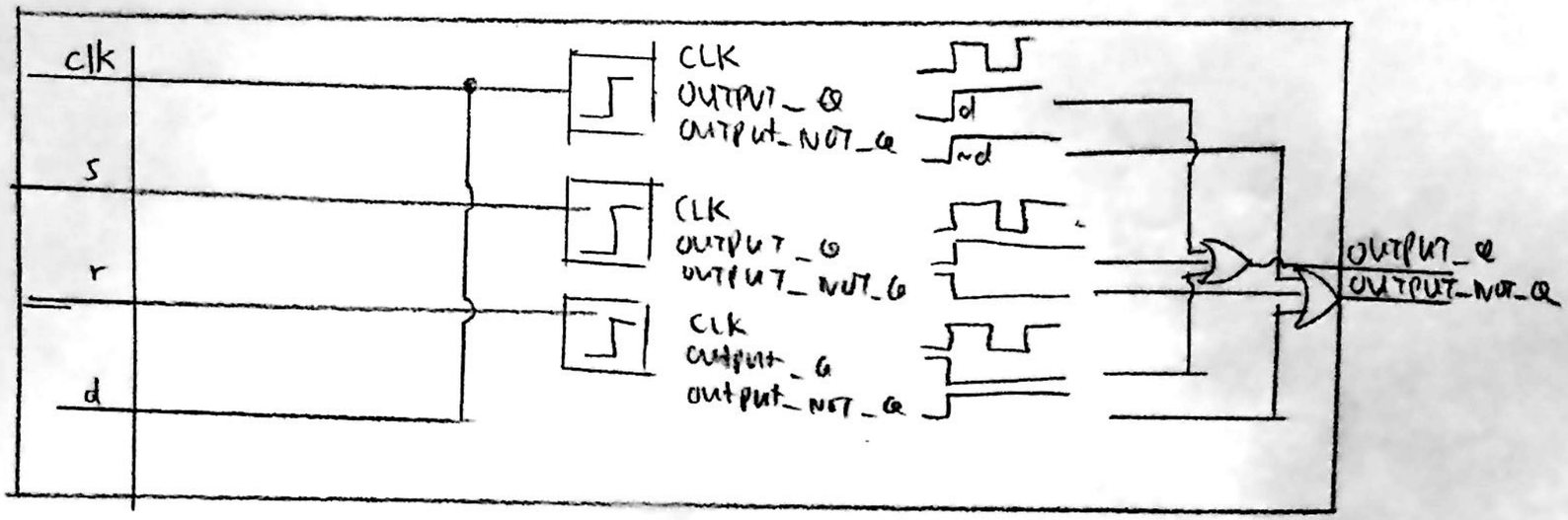
Increment.v



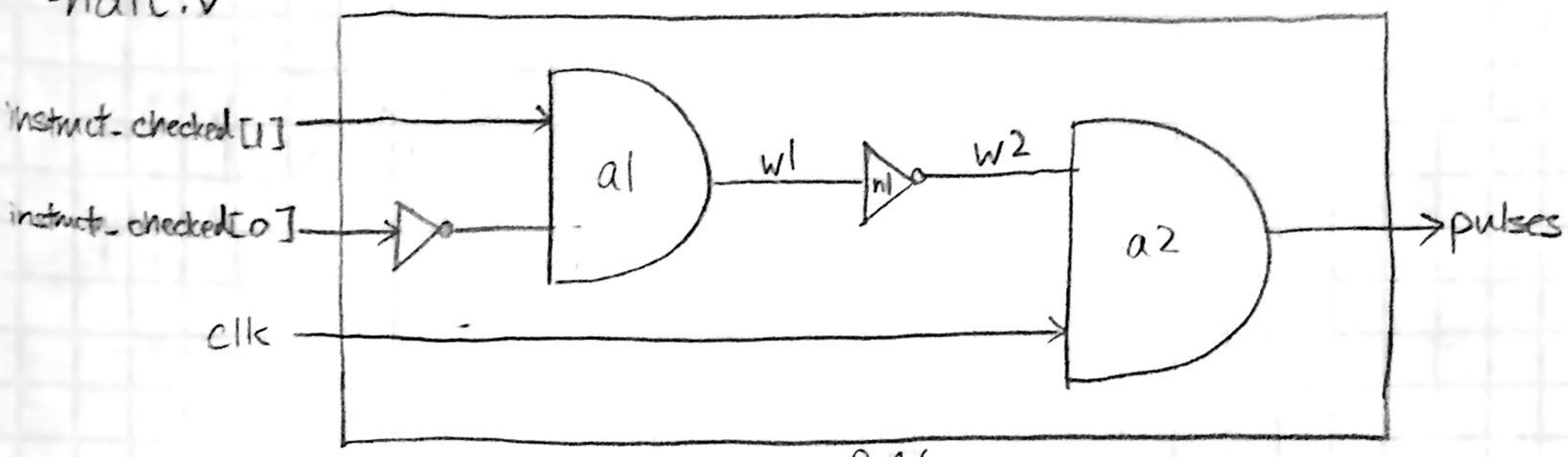
dff_reset_negedge.v



dff_reset_posedge.v

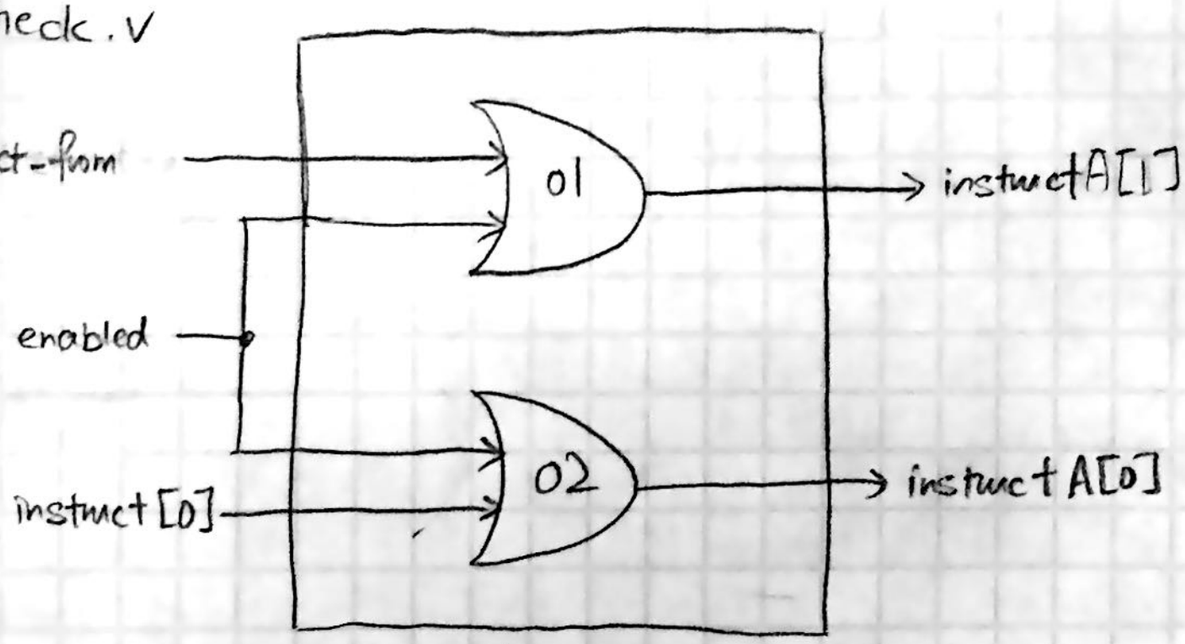


halt.v



name : halt

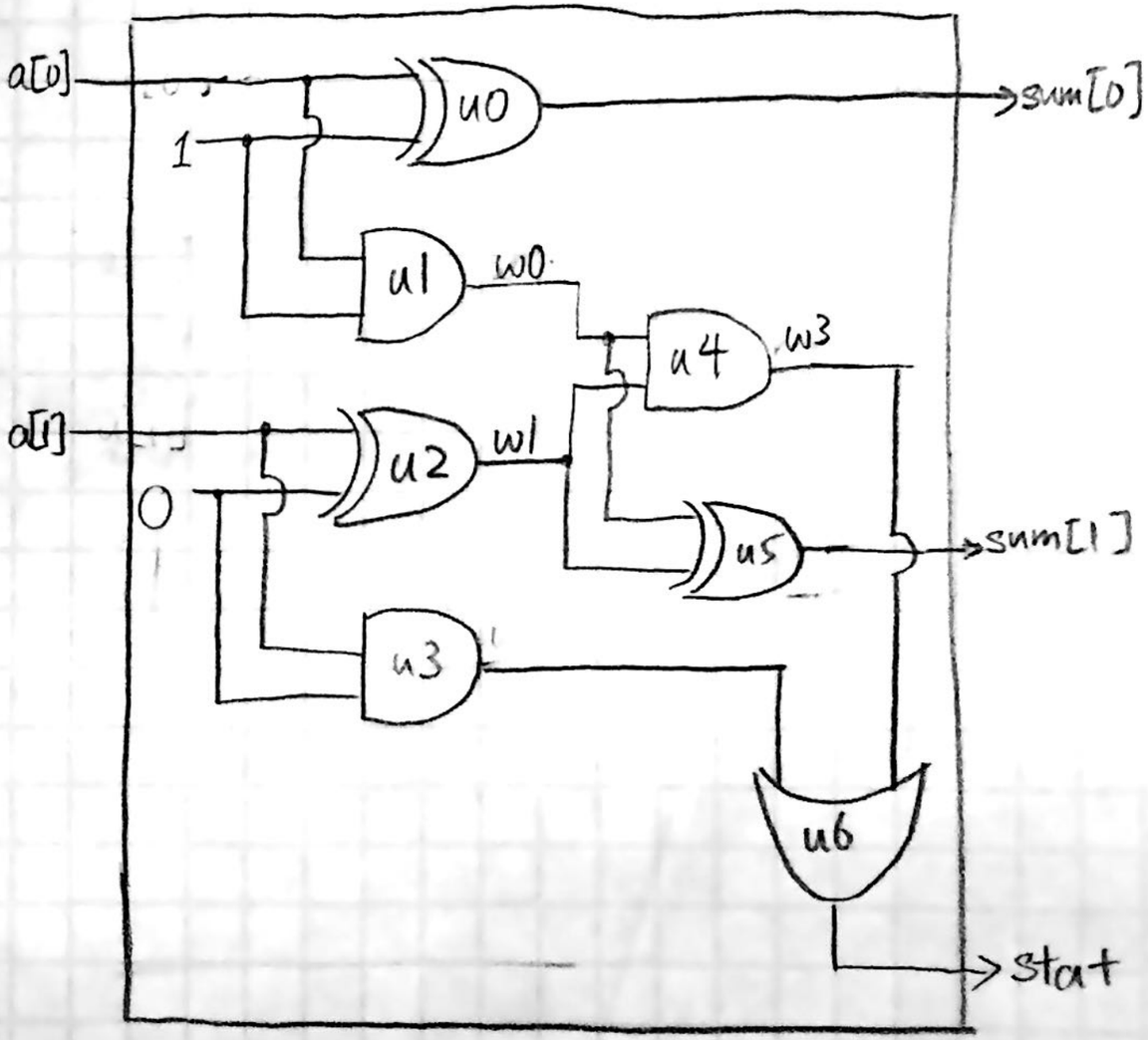
check.v

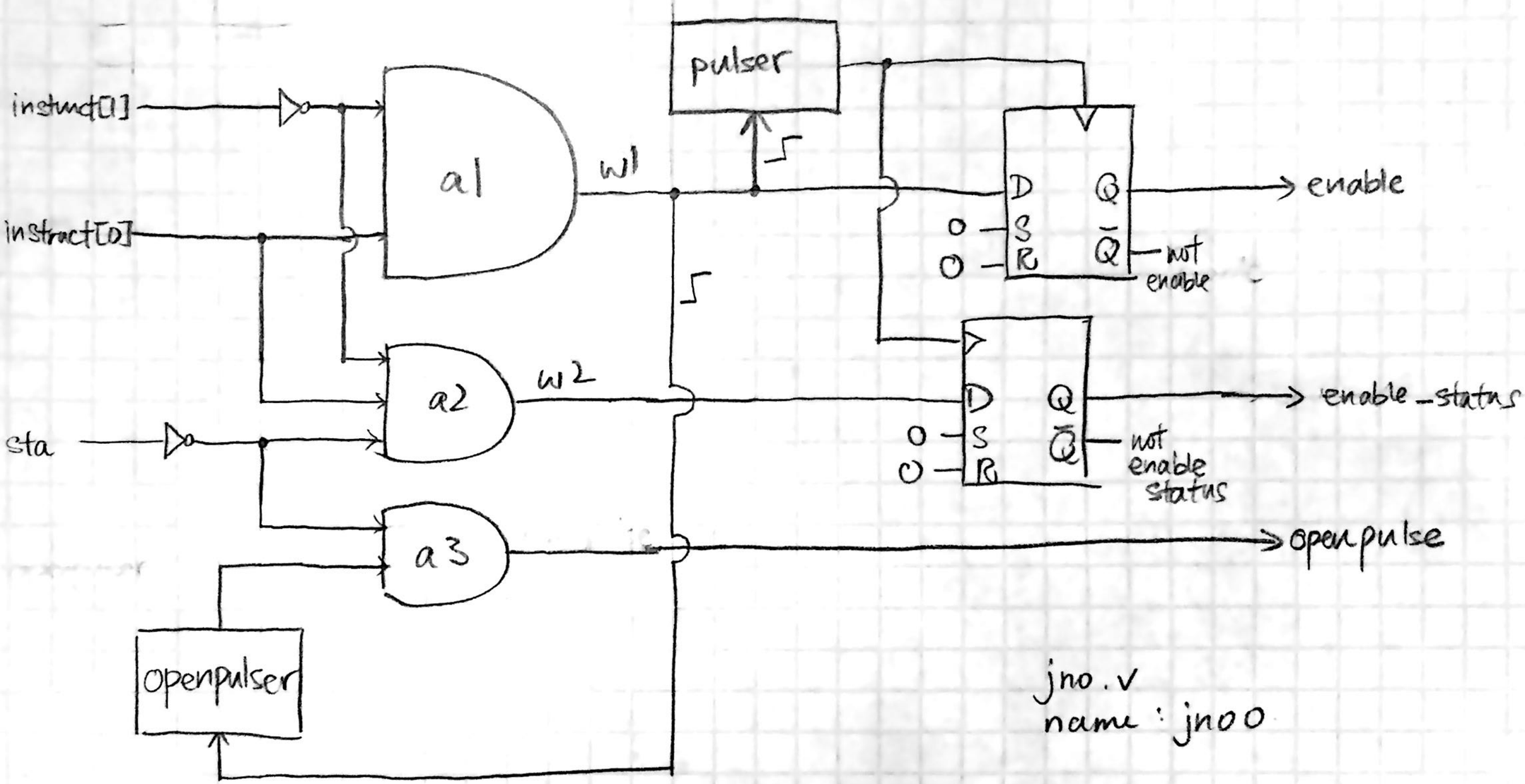


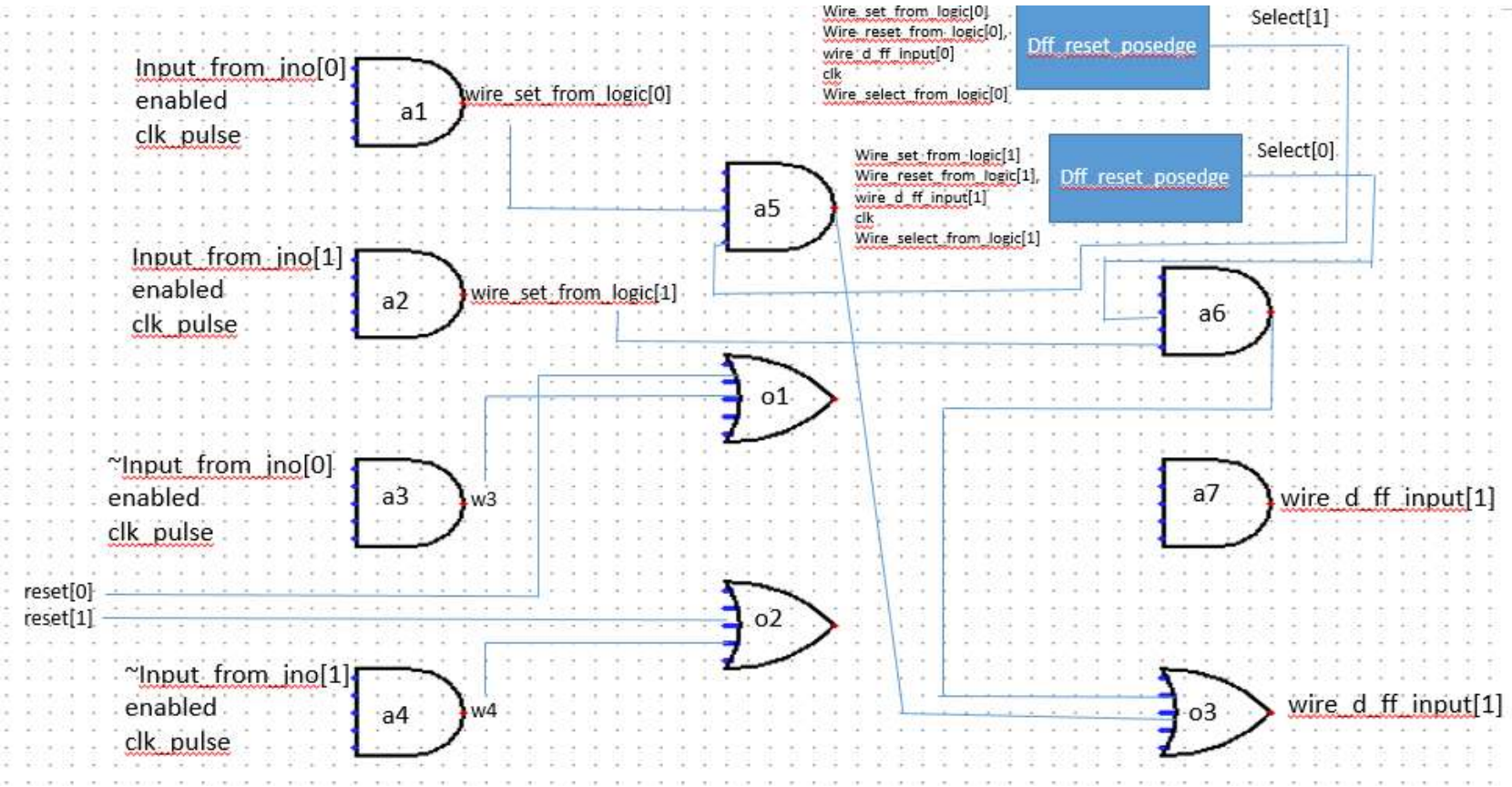
name = checkers

fulladder.v

name : adder1 (used in increment.v)





jno.v
name : jno0

Programcounter.v