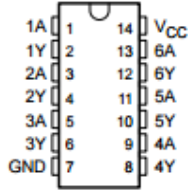
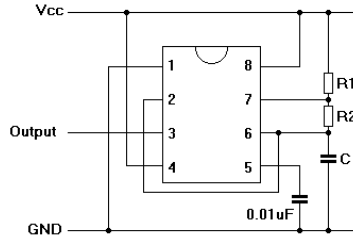
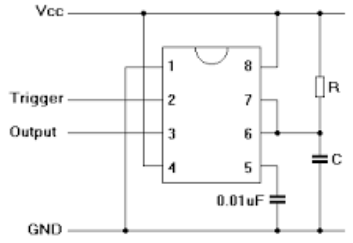
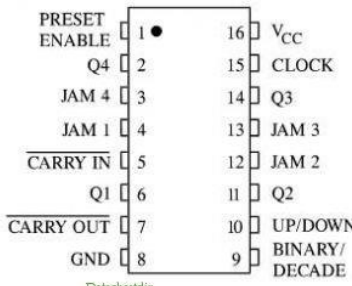
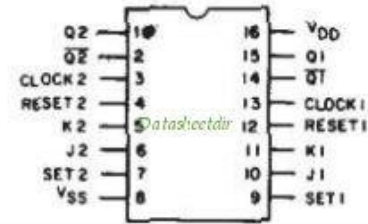
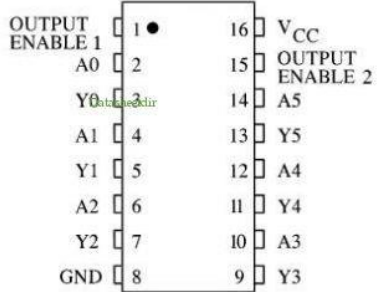
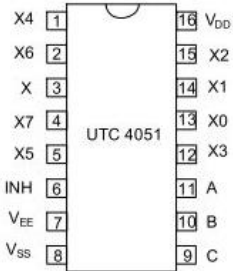
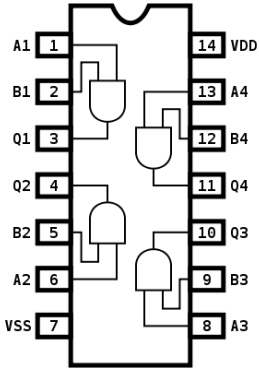
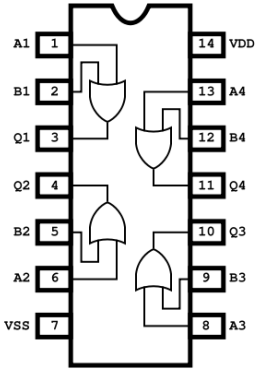


chip	Pins
7414	
555AS	<p>Astable Mode</p>  <p>CSGNetwork.Com 2/12/92</p>
555MS	<p>Monostable Mode</p>  <p>CSGNetwork.Com 2/12/92</p>
4029	 <p>Datasheetdir</p>
4027	

4503	 <p>Pinout diagram for 4503:</p> <ul style="list-style-type: none"> Pin 1: OUTPUT ENABLE 1 Pin 2: A0 Pin 3: Y0 Pin 4: A1 Pin 5: Y1 Pin 6: A2 Pin 7: Y2 Pin 8: GND Pin 9: Y3 Pin 10: A3 Pin 11: Y4 Pin 12: A4 Pin 13: Y5 Pin 14: A5 Pin 15: OUTPUT ENABLE 2 Pin 16: V_{CC}
4051	 <p>Pinout diagram for 4051:</p> <ul style="list-style-type: none"> Pin 1: X4 Pin 2: X6 Pin 3: X Pin 4: X7 Pin 5: X5 Pin 6: INH Pin 7: V_{EE} Pin 8: V_{SS} Pin 9: C Pin 10: B Pin 11: A Pin 12: X3 Pin 13: X0 Pin 14: X1 Pin 15: X2 Pin 16: V_{DD}
4081	 <p>Pinout diagram for 4081:</p> <ul style="list-style-type: none"> Pin 1: A1 Pin 2: B1 Pin 3: Q1 Pin 4: Q2 Pin 5: B2 Pin 6: A2 Pin 7: VSS Pin 8: A3 Pin 9: B3 Pin 10: Q3 Pin 11: Q4 Pin 12: B4 Pin 13: A4 Pin 14: VDD
4071	 <p>Pinout diagram for 4071:</p> <ul style="list-style-type: none"> Pin 1: A1 Pin 2: B1 Pin 3: Q1 Pin 4: Q2 Pin 5: B2 Pin 6: A2 Pin 7: VSS Pin 8: A3 Pin 9: B3 Pin 10: Q3 Pin 11: Q4 Pin 12: B4 Pin 13: A4 Pin 14: VDD

4035	
4069	<p>Figure 4069A</p>
74244	
2114	

4030	<p>(TOP VIEW)</p>	
4543	<p>7293262</p>	
4072	<p>(TOP VIEW)</p>	