



FIGURE 9-16 Circuit for keyboard entry of three-digit number into storage registers.

the ring counter to the 100 state (X goes HIGH). The positive transition at X is fed to the *CLK* inputs of flip-flops Q_8 to Q_{11} , so that the encoder outputs are transferred to these FFs. That is, $Q_{11} = 0$, $Q_{10} = 0$, $Q_9 = 1$, and $Q_8 = 1$. Note that flip-flops Q_0 to Q_7 are not affected because their *CLK* inputs have not received a positive transition.