

The schematic diagram illustrates the power supply circuit for the STM32F103C8T6 microcontroller. Key components and connections include:

- Microcontroller Pins:** U5 (1117-3.3) and U6 (1117-3.3) represent the microcontroller's power pins. U5 has pins VI, VO, and GND. U6 has pins VI, VO, and GND.
- Decoupling Capacitors:** C7 (10uF), C8 (22uF), C9 (10uF), and C10 (22uF) are connected to the 5V and 3V3 pins. C11 through C20 (100nF) are connected to the 3V3 pin. C21 (100uF/16V) and C22 through C26 (100nF) are connected to the 5V pin.
- Power Sources:** +5V, +3V3, +3V3A, and +5V_PRG (J5) are the main power sources.
- LEDs:** LED1 and LED2 are connected to the D1 and D2 pins, respectively, through resistors R20 and R21 (1k).
- Grounding:** All components are connected to GND.

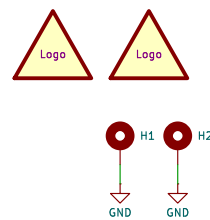
Pinout diagram of the EPM3128ATC100 (U3) showing connections to various components:

- Power Supply:**
 - +3V3 (Pin 3)
 - GND (Pin 11, 26, 33, 43, 53, 59, 65, 74, 78, 95, 98, 38, 86)
- Control Signals:**
 - TDI (Pin 4)
 - YM_A0 (Pin 9)
 - YM_A1 (Pin 10)
 - YM_CS (Pin 12)
 - YM_SMP2 (Pin 13)
 - YM_SMP1 (Pin 14)
 - TMS (Pin 16)
 - YM_DATA (Pin 17)
 - YM_DCLK (Pin 19)
 - CLK14 (Pin 20)
 - A0 (Pin 21)
 - A1 (Pin 22)
 - A2 (Pin 23)
 - A3 (Pin 25)
 - TORQGE (Pin 29)
 - A7 (Pin 30)
 - A6 (Pin 31)
 - A5 (Pin 32)
 - A4 (Pin 35)
 - A9 (Pin 36)
 - RS1 (Pin 40)
 - IORQ (Pin 41)
 - M1 (Pin 44)
 - A8 (Pin 45)
- General Purpose I/O (GPIO):**
 - Pin 1: I/O
 - Pin 2: I/O
 - Pin 5: I/O
 - Pin 6: I/O
 - Pin 7: I/O
 - Pin 8: I/O
 - Pin 11: I/O
 - Pin 12: I/O
 - Pin 13: I/O
 - Pin 14: I/O
 - Pin 15: I/O
 - Pin 16: I/O
 - Pin 17: I/O
 - Pin 18: I/O
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 - Pin 37: I/O
 - Pin 38: I/O
 - Pin 39: I/O
 - Pin 40: I/O
 - Pin 41: I/O
 - Pin 42: I/O
 - Pin 43: I/O
 - Pin 44: I/O
 - Pin 45: I/O
 - Pin 46: I/O
 - Pin 47: I/O
 - Pin 48: I/O
 - Pin 49: I/O
- Other Signals:**
 - TCK (Pin 62)
 - LED2 (Pin 69)
 - LED1 (Pin 72)
 - TDO (Pin 73)
 - DAC_STD (Pin 87)
 - CLK2B8 (Pin 89)
 - CFG4 (Pin 93)
 - CFG3 (Pin 94)
 - CFG0 (Pin 96)
 - CFG1 (Pin 97)
 - CFG2 (Pin 98)
 - DAC_DAT (Pin 99)
 - DAC_LRCK (Pin 99)
 - DAC_BCK (Pin 100)

The diagram shows a 5-pin switch (SW1) connected to a +5V supply and GND. The connections are as follows:

- Pin 1: Connected to +5V
- Pin 2: Connected to GND
- Pin 3: Connected to CFG0
- Pin 4: Connected to CFG1
- Pin 5: Connected to CFG2

The SW1 is labeled SW1 and SW_DIP_x05.

[illegible]

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