姓名:郭紘安 Email: andy8766kuo@gmail.com

實驗名稱: Exception

實驗目的:

- Observe the initialization of exception.
- Observe the implementation of system calls.
- Add a new system call.

實驗步驟:

▶ 例外處理

Step 1: 例外初始化

1.下載Linux kernel source code andy@ubuntu:~/disk\$ git clone --depth=1 https://github.com/raspberrypi/linux -b rpi-4.9.y

2.在<Linux kernel source code>/init/main.c的start_kernel函式中加入一個printk敘述。

(Linux Host 的<Linux kernel source code>/init/main.c)

```
jump_label_init();
 \star These use large bootmem allocations and must precede
 * kmem_cache_init()
setup_log_buf(0);
pidhash_init();
vfs_caches_init_early();
sort main extable();
/* Modify*/
printk("Initialize traps\n");
/*Modify*/
trap_init();
 * Set up the scheduler prior starting any interrupts (such as the
 * timer interrupt). Full topology setup happens at smp_init()
* time - but meanwhile we still have a functioning scheduler.
sched_init();
 \star Disable preemption - early bootup scheduling is extremely
 * fragile until we cpu_idle() for the first time.
preempt disable();
if (WARN(!irqs_disabled(),
           Interrupts were
                             enabled *very* early, fixing it\n"))
        local_irq_disable();
idr_init_cache();
rcu init();
/* trace_printk() and trace points may be used after this */
trace init();
```

姓名:郭紘安 Email: andy8766kuo@gmail.com

3.在<Linux kernel source code>/arch/arm/kernel/traps.c的
trap_init函式中加入一個printk敘述。(Linux Host的<Linux kernel
source code>/arch/arm/kernel/traps.c)

Step 2: 編譯核心

andy@ubuntu:~/disk\$ cd linux/
andy@ubuntu:~/disk/linux\$ make ARCH=arm bcm2709_defconfig

```
andy@ubuntu:~/disk$ cd linux/
andy@ubuntu:~/disk/linux$ make ARCH=arm bcm2709_defconfig
HOSTCC scripts/basic/fixdep
HOSTCC scripts/kconfig/conf.o
SHIPPED scripts/kconfig/zconf.tab.c
SHIPPED scripts/kconfig/zconf.lex.c
SHIPPED scripts/kconfig/zconf.hash.c
HOSTCC scripts/kconfig/zconf.tab.o
HOSTLD scripts/kconfig/conf#
# configuration written to .config
#
andy@ubuntu:~/disk/linux$
```

Add Path:

andy@ubuntu:~/disk/linux\$ PATH="/home/andy/WORK/crossgcc2/bin:\$PATH"
andy@ubuntu:~/disk/linux\$ make ARCH=arm CROSS_COMPILE=arm-linux-gnueabihfbzImage

```
AS arch/arm/boot/compressed/hyp-stub.or
SHIPPED arch/arm/boot/compressed/lib1funcs.or
AS arch/arm/boot/compressed/lib1funcs.or
SHIPPED arch/arm/boot/compressed/ashldi3.or
AS arch/arm/boot/compressed/ashldi3.or
SHIPPED arch/arm/boot/compressed/bswapsdi2.or
AS arch/arm/boot/compressed/bswapsdi2.or
LD arch/arm/boot/compressed/vmlinux
OBJCOPY arch/arm/boot/zImage
Kernel: arch/arm/boot/zImage is ready
```

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你可以在kernel source目錄>/arch/arm/boot目錄下,找到zImage。 將 zImage 複製到 microSD 卡的第一個 partition。

andy@ubuntu:~/disk/linux/arch/arm/boot\$ sudo cp -rf zImage ~/mmc1

```
0.000000] Booting Linux on physical CPU 0x0
0.000000] Linux version 4.9.80-v7+ (andy@ubuntu) (gcc version 4.9.3 (GCC) ) #1 SMP Tue May 19 01:53:07 CST
0.000000] CPU: ARMv7 Processor [410fd034] revision 4 (ARMv7), cr=10c5383d
  0.000000] CPU: div instructions available: patching division code
0.000000] CPU: PIPT / VIPT nonaliasing data cache, VIPT aliasing instruction cache
0.000000] OF: fdt:Machine model: Raspberry Pi 3 Model B Rev 1.2
  0.000000] Memory policy: Data cache writealloc
0.000000] percpu: Embedded 14 pages/cpu @b8b8b000 s25600 r8192 d23552 u57344
0.000000] Built 1 zonelists in Zone order, mobility grouping on. Total pages: 234465
0.000000] Kernel command line: coherent_pool=1M 8250.nr_uarts=1 bcm2708_fb.fbwidth=720 bcm2708_fb.fbheight
ttyAMA0,115200
  0.000000] PID hash table entries: 4096 (order: 2, 16384 bytes)
0.000000] Dentry cache hash table entries: 131072 (order: 7, 524288 bytes)
0.000000] Inode-cache hash table entries: 65536 (order: 6, 262144 bytes)
 0.000000] Initialize traps
0.000000] arm: system call handler initialization -> see assembly code
  0.000000] arm: system call handler initialization -> see assembly code
0.000000] Virtual kernel memory layout:
0.000000] Virtual kernel memory layout:
0.000000] vector : 0xffff00000 - 0xffff1000 ( 4 kB)
0.000000] fixmap : 0xffc00000 - 0xfff00000 (3072 kB)
0.000000] vmalloc : 0xba000000 - 0xff800000 (1112 MB)
0.000000] vmalloc : 0xba000000 - 0xb9c00000 ( 924 MB)
0.000000] lowmem : 0x80000000 - 0xb9c00000 ( 924 MB)
0.000000] modules : 0x7f000000 - 0x80000000 ( 16 MB)
0.000000] .text : 0x80008000 - 0x80800000 (8160 kB)
0.000000] .init : 0x80000000 - 0x80c00000 ( 1024 kB)
0.000000] .data : 0x80c00000 - 0x80c79cbc ( 488 kB)
0.000000] .bss : 0x80c7b000 - 0x80d3b9e4 ( 771 kB)
0.000000] SLUB: HWalign=64, Order=0-3, MinObjects=0, CPUs=4, Nodes=1
   0.000000] SLUB: HWalign=64, Order=0-3, MinObjects=0, CPUs=4, Nodes=1 0.000000] Hierarchical RCU implementation.
```

系統呼叫運作

Step 1: 寫一個使用系統呼叫的程式

```
andy@ubuntu:~/disk/Lab9$ gcc hello.c -o hello
andy@ubuntu:~/disk/Lab9$ ./hello
hello world~
andy@ubuntu:~/disk/Lab9$
```

Step 2: Cross Compiler

編譯程式

反組譯程式碼

```
andy@ubuntu:~/disk/Lab9$ arm-linux-gnueabihf-gcc -static hello.c -o hello.exe
andy@ubuntu:~/disk/Lab9$ arm-linux-gnueabihf-objdump -d hello.exe > assembly andy@ubuntu:~/disk/Lab9$
```

姓名:郭紘安 Email:andy8766kuo@gmail.com

Step 3:找尋 SVC

```
00027280 <__<mark>libc_ope</mark>n>:
                                         ip, [pc, #96] ; 272e8 <__<mark>libc_ope</mark>n+0x68>
  27280:
                e59fc060
                                 ldr
   27284:
                e79fc00c
                                 ldr
                                         ip, [pc, ip]
   27288:
               e33c0000
                                 tea
                                         ip, #0
               e52d7004
                                 push
  2728c:
                                          {r7}
                                                          ; (str r7, [sp, #-4]!)
                                         272ac <__libc_open+0x2c>
   27290:
               1a000005
                                 bne
               e3a07005
  27294:
                                         r7, #5
                                 mov
   27298:
               ef000000
                                 svc
                                         0x00000000
                                         {r7}
  2729c:
               e49d7004
                                                         ; (ldr r7, [sp], #4)
                                 DOD
              e3700a01
312fff1e
                                         г0, #4096
   272a0:
                                                       ; 0x1000
                                 cmn
  272a4:
                                 bxcc
                                         lr
               ea000e0c
   272a8:
                                         2aae0 <__syscall_error>
                                 Ь
   272ac:
               e92d400f
                                 push
                                          {r0, r1, r2, r3, lr}
  272b0:
               eb000816
                                 bl
                                         29310 <__libc_enable_asynccancel>
                                          ір, г0
   272b4:
               e1a0c000
                                 mov
  272b8:
               e8bd000f
                                          {r0, r1, r2, r3}
                                 pop
   272bc:
               e3a07005
  272c0:
               ef000000
                                svc
                                         0x00000000
               e1a07000
   272c4:
                                         г0, ip
   272c8:
               e1a0000c
                                 mov
               eb00083f
                                         293d0 <__libc_disable_asynccancel>
   272cc:
                                 ы
   272d0:
               e1a00007
                                 mov
                                         г0, г7
               e49de004
                                                         ; (ldr lr, [sp], #4)
; (ldr r7, [sp], #4)
  272d4:
                                 pop
                                         {lr}
   272d8:
               e49d7004
                                         {r7}
                                 DOD
                                                        ; 0x1000
               e3700a01
                                         г0, #4096
   272dc:
                                 cmn
   272e0:
               312fff1e
                                 bxcc
                                         lr
                                                  _syscall_error>
  272e4:
               ea000dfd
                                         2aae0 <
                                 Ь
   272e8:
               00073170
                                 .word
                                         0x00073170
               e1a00000
                                         0xe1a00000
  272ec:
                                 .word
```

系統呼叫(System Call)

Step 1:修改系統檔案

(Linux Host的<Linux kernel source code>/arch/arm/kernel/calls.S)

Step 2: Add define of system call in unistd.h

(Linux Host 的<Linux kernel source

code>/arch/arm/include/uapi/asm/unistd.h)

```
/* Modify */
#define __NR_mysyscall (__NR_SYSCALL_BASE+397)
/* Modify */
```

Step 3: 撰寫欲新增的 system call 的內容

1.(Linux Host 的<Linux kernel source

code>/arch/arm/kernel/mysyscall.c)(新增 mysyscall.c)

姓名:郭紘安 Email:andy8766kuo@gmail.com

2. (Linux Host的<Linux kernel source

code>/include/linux/syscalls.h) (修改)

```
asmlinkage long sys_pkey_alloc(unsigned long flasmlinkage long sys_pkey_free(int pkey); asmlinkage void sys_mysyscall(int a, char* b); #endif
```

3. 修改在<Linux kernel source code>/arch/arm/kernel下的Makefile, 其中的obj-y := compat.o entry-armv.o ...,在這行最後面加入 mysyscall.o。

(Linux Host的<Linux kernel source c ode>/arch/arm/kernel/Makefile)

4.重編 kernel

```
andy@ubuntu:~/disk/linux$ make ARCH=arm bcm2709_defconfig
#
# configuration written to .config
#
andy@ubuntu:~/disk/linux$
```

寫一個使用者應用程式,來測試新增的系統呼叫

Step 1: write user program to call mysyscall.

姓名:郭紘安 Email: andy8766kuo@gmail.com

Step 2: 使用 Cross Compiler 編譯使用者程式 mytestsys.c

```
mytestsys.c:8:2: note: in expansion of macro 'mysyscall'
mysyscall(14,"system_call_lab");
ndy@ubuntu:~/disk/Lab9$
```

Step 3: Pi 上執行 mytestsys.exe

```
1ib32
             linuxrc
                            mytestsys.exe run
./mytestsys.exe
 239.496759] system call ...
 239.502699] intl:14, staring:system call lab in kernel
```

問題與討論:

asmlinkage的描述是什麼意義?

```
For example:
asmlinkage int sys_myservice(...)
```

"asmlinkage" 是在 i386 system call 實作中相當重要的一個 gcc 標籤 (tag) •

當 system call handler 要呼叫相對應的 system call routine 時,便將 一般用途暫存器的值 push 到 stack 裡,因此 system call routine 就要 由 stack 來讀取 system call handler 傳遞的參數·這就是 asmlinkage 標 籤的用意。

system call handler 是 assembly code · system call routine (例如: sys nice)是 C code[,]當 assembly code 呼叫 C function[,]並且是以 stack 方式傳參數(parameter)時,在 C function 的 prototype 前面就要加上 "asmlinkage" •

加上 "asmlinkage" 後,C function 就會由 stack 取參數,而不是從 register 取參數(可能發生在程式碼最佳化後)。

姓名:郭紘安 Email: andy8766kuo@gmail.com

心得:

這次實驗讓我實作了如何修改並且編譯 linux kernel,以及創建自己的 system call.