CA HW2

B11901148 李承彥

```
Statistics for case statements in always block at line 85 in file
        '/home/raid7_2/userb11/b11148/new2/HW2_2024/01_RTL/HW2.v'
===============================================
|        Line          | full/ parallel  |
===============================================
|         86           |    auto/auto    |
===============================================

Inferred memory devices in process
        in routine ALU line 96 in file
            '/home/raid7_2/userb11/b11148/new2/HW2_2024/01_RTL/HW2.v'.
==================================================================================
|  Register Name   |    Type    | Width | Bus | MB | AR | AS | SR | SS | ST |
==================================================================================
|    cnt_reg       | Flip-flop  |   6   |  Y  | N  | N  | N  | N  | N  | N  |
==================================================================================

Inferred memory devices in process
        in routine ALU line 106 in file
            '/home/raid7_2/userb11/b11148/new2/HW2_2024/01_RTL/HW2.v'.
==================================================================================
|  Register Name   |    Type    | Width | Bus | MB | AR | AS | SR | SS | ST |
==================================================================================
|   result_reg     | Flip-flop  |  65   |  Y  | N  | N  | N  | N  | N  | N  |
==================================================================================

Inferred memory devices in process
        in routine ALU line 323 in file
            '/home/raid7_2/userb11/b11148/new2/HW2_2024/01_RTL/HW2.v'.
==================================================================================
|  Register Name   |    Type    | Width | Bus | MB | AR | AS | SR | SS | ST |
==================================================================================
|  o_data_in_reg   | Flip-flop  |  64   |  Y  | N  | N  | N  | N  | N  | N  |
|   ready_reg      | Flip-flop  |   1   |  N  | N  | N  | N  | N  | N  | N  |
==================================================================================

Inferred memory devices in process
        in routine ALU line 337 in file
            '/home/raid7_2/userb11/b11148/new2/HW2_2024/01_RTL/HW2.v'.
==================================================================================
|  Register Name   |    Type    | Width | Bus | MB | AR | AS | SR | SS | ST |
==================================================================================
|  operand_b_reg   | Flip-flop  |  32   |  Y  | N  | Y  | N  | N  | N  | N  |
|    inst_reg      | Flip-flop  |   3   |  Y  | N  | Y  | N  | N  | N  | N  |
|    state_reg     | Flip-flop  |   2   |  Y  | N  | Y  | N  | N  | N  | N  |
|  operand_a_reg   | Flip-flop  |  32   |  Y  | N  | Y  | N  | N  | N  | N  |
==================================================================================
Presto compilation completed successfully.
Current design is now '/home/raid7_2/userb11/b11148/new2/HW2_2024/01_RTL/ALU.db:ALU'
Loaded 1 design.
Current design is 'ALU'.
```