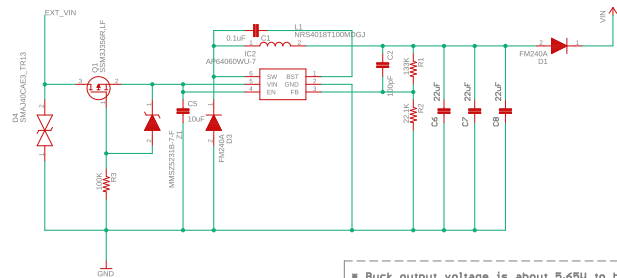
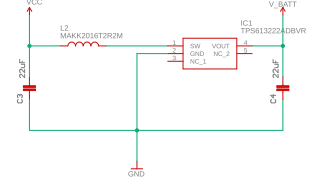


6V to 36V Buck



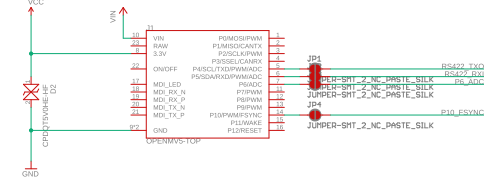
■ Buck output voltage is about 5.65V to be 5V after the diode forward voltage drop.

3.3V to 5V Boost

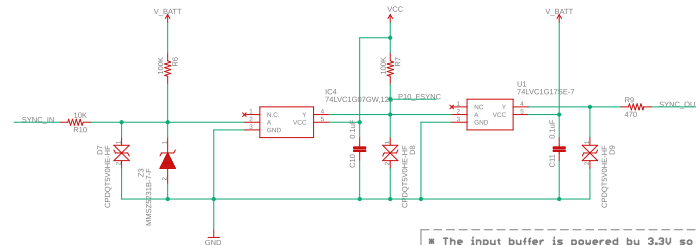


■ 3.3V is used to create the 5V rail so it can be turned off in low power mode.

Shield Headers



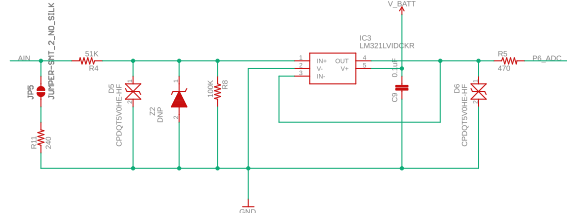
FSYNC Input and Output



■ The input buffer is open drain so that SYNCIN is OR'ed with multiple shields attached.

■ The input buffer is powered by 3.3V so that it can accept 3.3V inputs.

ADC Input

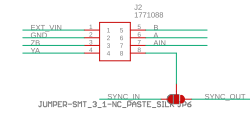


■ The shunt resistor when connected allows the ADC circuit to read 4-20mA sensors.

■ The front end scales a 0-5V signal down to 0-3.3V. Reverse/Over-Voltage is clamped.

■ The opamp is powered by 5V so that it can pass 0-3.3V signals (0-5V on input).

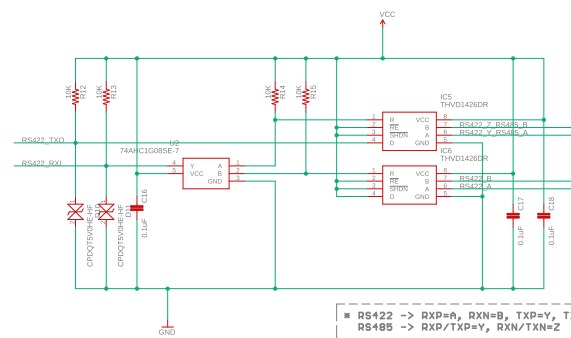
Terminal



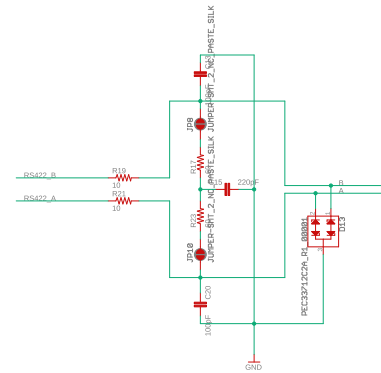
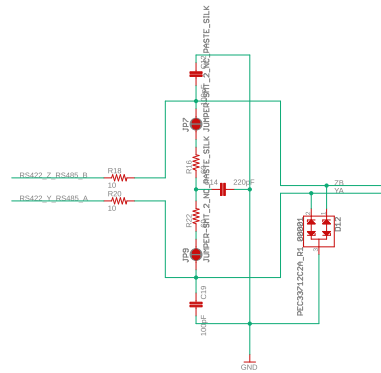
Mechanical

○ ○ ○ ○ ○ ○ ○ ○ ○ ○

RS422 and RS485 Interface



■ RS422 -> RXD=A, RXN=B, TXD=Y, TXN=Z
RS485 -> RXD=TXD=A, RXN=TXN=B



Copyright (c) 2013-2023 OpenMV <openmv@openmv.io>

This work is licensed under the Creative Commons Attribution-ShareAlike 3.0 License.

To view a copy of this license, visit <http://creativecommons.org/licenses/by-sa/3.0/>

TITLE: rs422-rs485

Document Number:

REV:

Date: 5/10/2023 9:29 PM

Sheet: 1/1