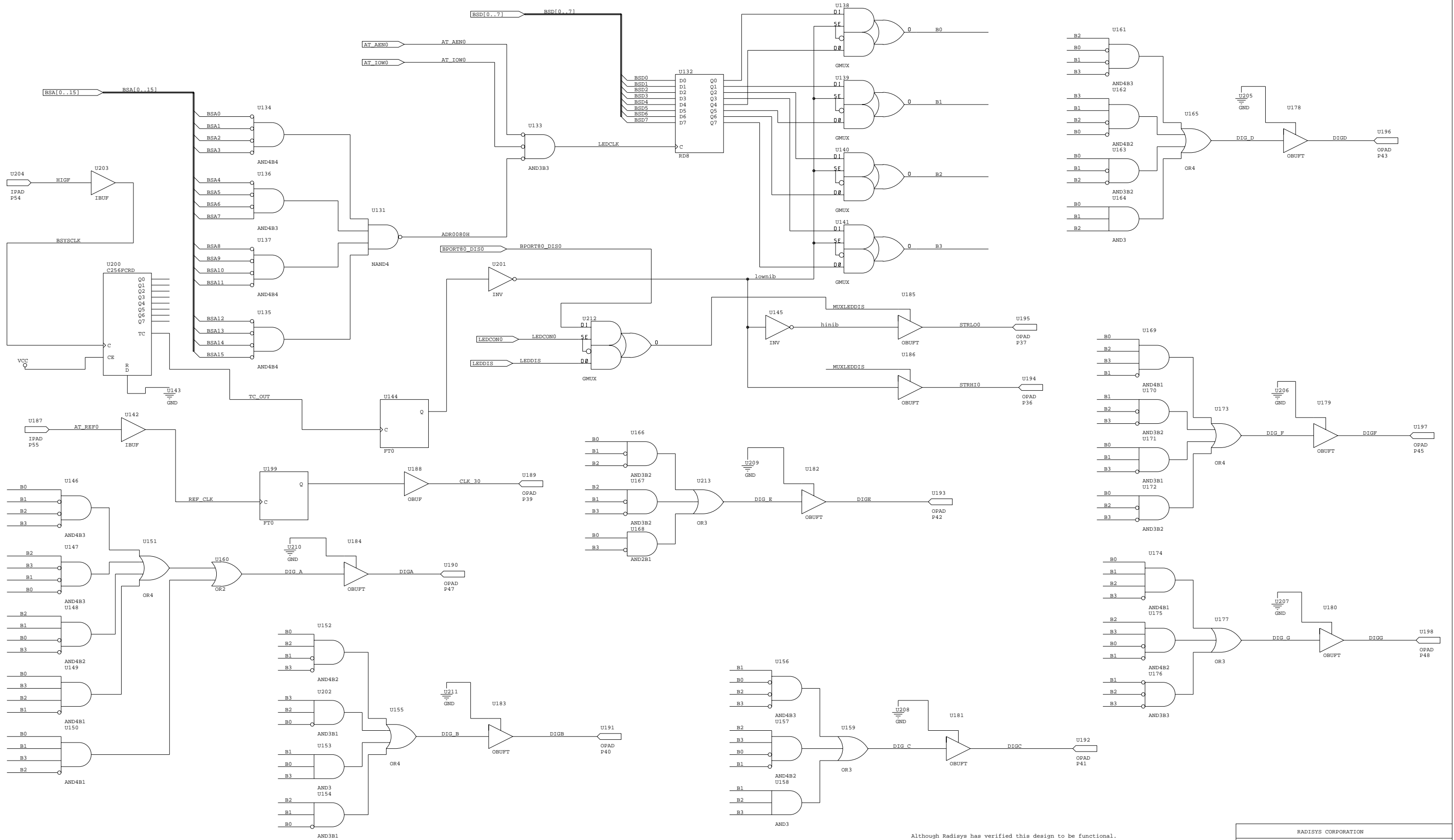


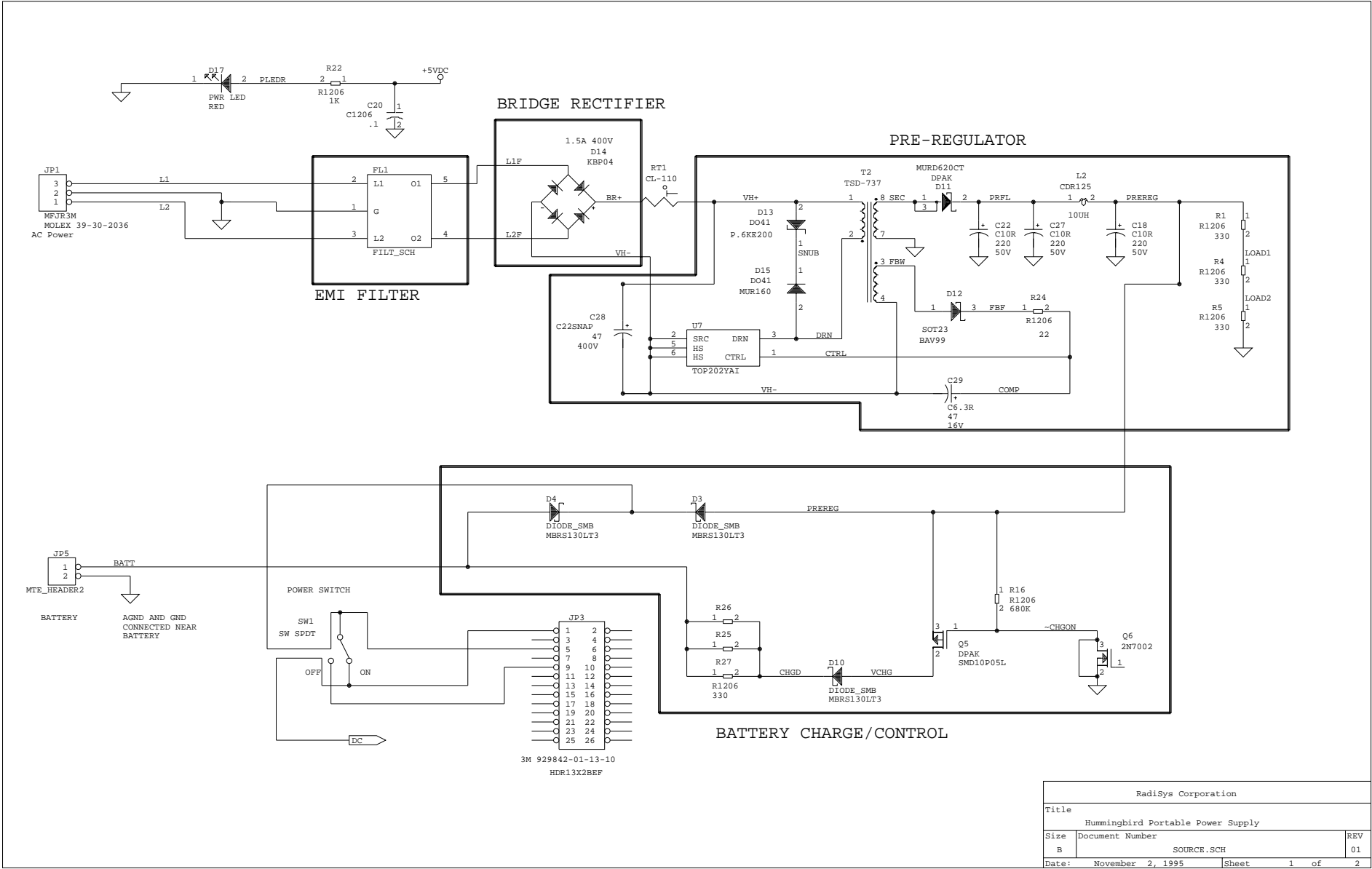


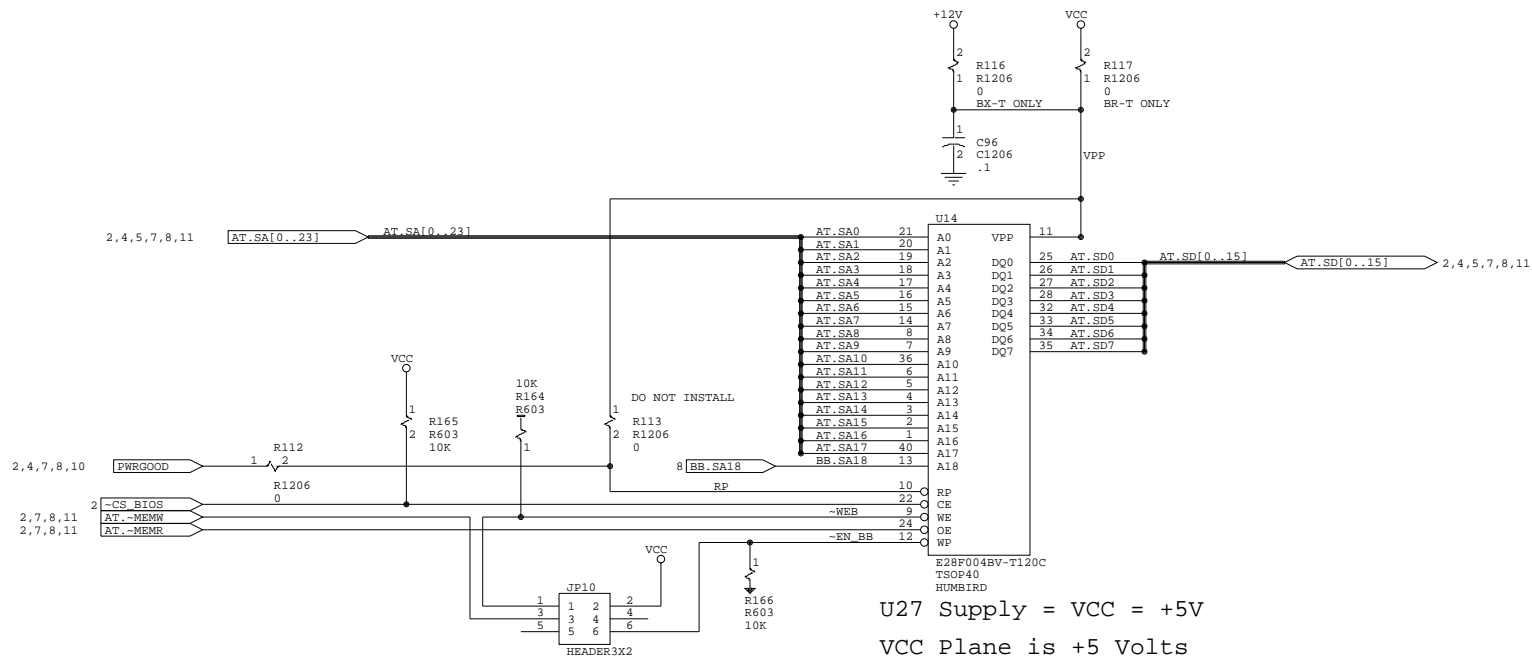
RadiSys Corporation			
Title			
Hummingbird Portable Power Supply			
Size	Document Number		REV
B	CNTRL.SCH		01
Date:	November 1, 1995	Sheet	2 of 2



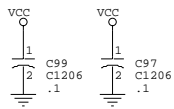
Although Radisys has verified this design to be functional. Neither Radisys or Intel assume any responsibility for any errors which may appear in the design. Both Radisys and Intel reserves the right to modify this design without notice.

RADISYS CORPORATION			
Title			
Port 80 Display for HEB			
Size	Document Number	REV	
C	PORT80.SCH		
Date:	November 16, 1995	Sheet	2 of 2



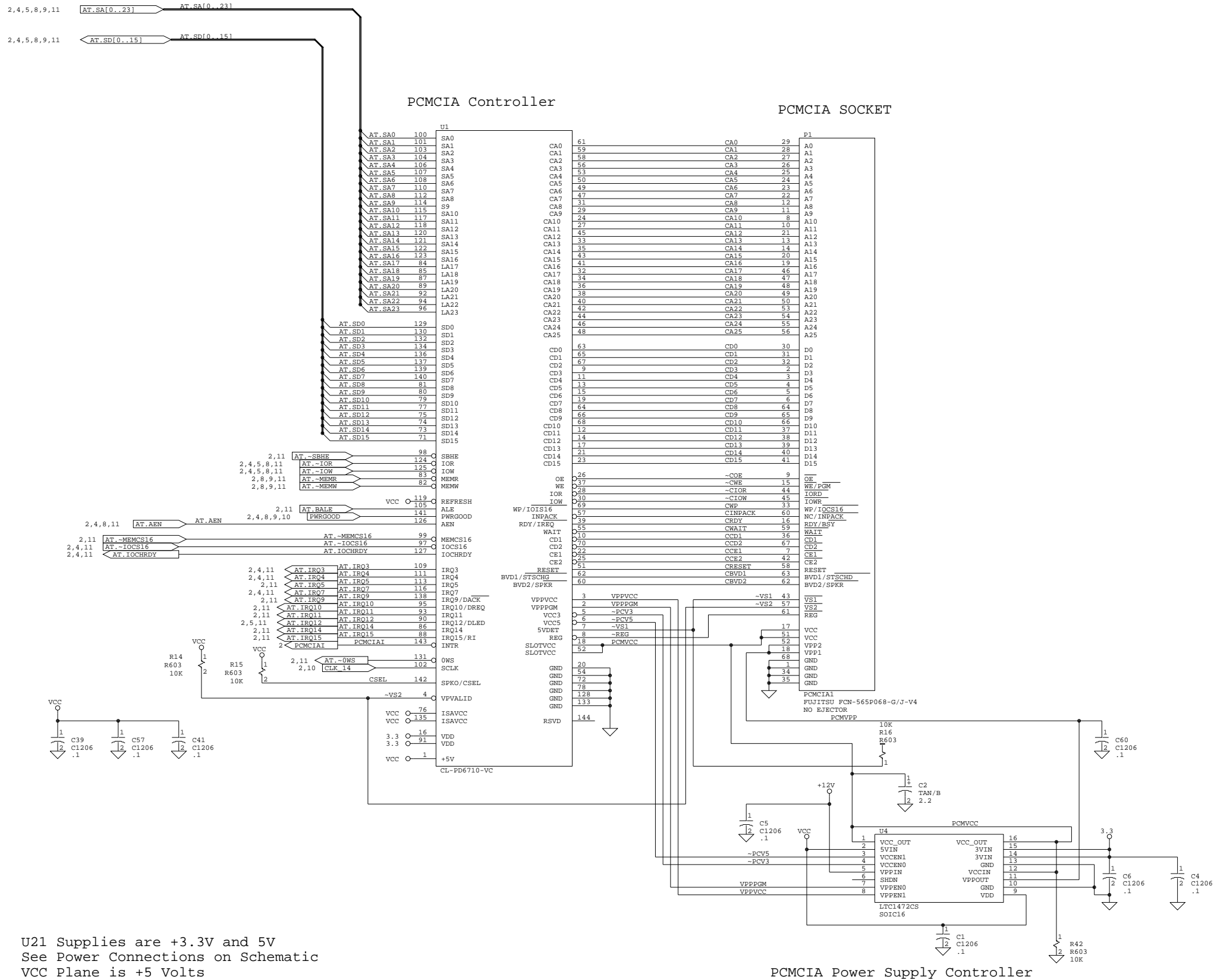


Jumpers	Comment
1-3	Write to Flash except Boot Block
1-3 & 2-6	Write to Flash and Boot Block



Although Radisys has verified this design to be functional. Neither Radisys or Intel assume any responsibility for any errors which may appear in the design. Both Radisys and Intel reserves the right to modify this design without notice.

Radisys Corp.			
Title			
HEB DRAM and BOOT BLOCK FLASH			
Size	Document Number	REV	
C	01-0193-02	1	
Date:	September 28, 1995	Sheet	9 of 11

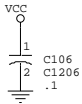
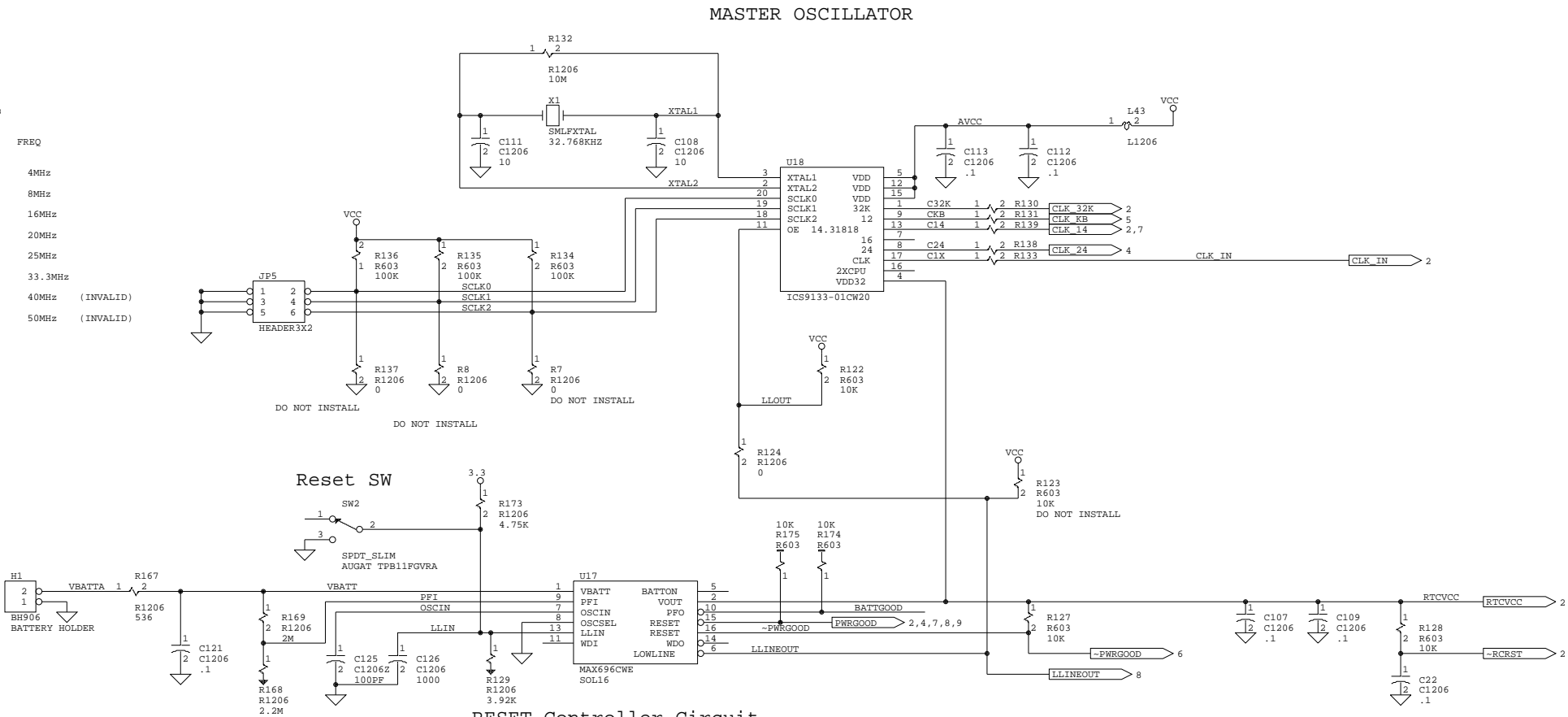


Although Radisys has verified this design to be functional.  
Neither Radisys or Intel assume any responsibility for any errors  
which may appear in the design. Both Radisys and Intel reserves  
the right to modify this design without notice.

Radisys Corporation		
Title Humming Bird PCMCIA CKTS		
Size C	Document Number 01-0193-02	REV
Date: September 28, 1995	Sheet 7 of	11

CPU Frequency vs Jumper Settings

SCLK2		SCLK1		SCLK0		FREQ
0	IN	0	IN	0	IN	4MHz
0	IN	0	IN	1	OUT	8MHz
0	IN	1	OUT	0	IN	16MHz
0	IN	1	OUT	1	OUT	20MHz
1	OUT	0	IN	0	IN	25MHz
1	OUT	0	IN	1	OUT	33.3MHz
1	OUT	1	OUT	0	IN	40MHz (INVALID)
1	OUT	1	OUT	1	OUT	50MHz (INVALID)



Although Radisys has verified this design to be functional. Neither Radisys or Intel assume any responsibility for any errors which may appear in the design. Both Radisys and Intel reserves the right to modify this design without notice.

Radisys Corp.			
Title			
Humming Bird CLK/Reset Ckts			
Size	Document Number	REV	
C	01-0193-02	1	
Date:	September 28, 1995	Sheet	10 of 11

\* = Plastic pin locator

J2 LEFT			
VADJ	1	2	* 3.3
@PCD	3	4	@PWT
@D/-C	5	6	@M/-IO
GND	7	8	VADJ
@-BE3	9	10	@-BE2
@-BE1	11	12	@-BE0
@BRQ	13	14	3.3
GND	15	16	@W/-R
@HLDA	17	18	@CLK CPU
VADJ	19	20	GND
@TCK	21	22	@HOLD
@AHOLD	23	24	VADJ
GND	25	26	@RDY
@-KEN	27	28	NC
NC	29	30	@-BS16
@-BS8	31	32	@-BRDY
@-BOFF	33	34	3.3
GND	35	36	@-LOCK
@-PLOCK	37	38	@-BLAST
	39	40	

J1 TOP			
@-ADS	1	2	@A2
GND	3	4	3.3
@A26	5	6	@A27
@A3	7	8	@A4
@A5	9	10	@RESVD
@A6	11	12	@A7
@A8	13	14	GND
@A9	15	16	@A10
3.3	17	18	GND
@A11	19	20	@A12
@A13	21	22	@A14
3.3	23	24	GND
@A15	25	26	@A16
@A17	27	28	GND
NC	29	30	@TDI
@TMS	31	32	@A18
@A19	33	34	@A20
3.3	35	36	3.3
@A21	37	38	@A22
GND	39	40	@A24
@A23 *			

J3 Right			
GND	1	2	GND
3.3	3	4	@A25
@A26	5	6	@A27
@A28	7	8	3.3
@A29	9	10	@A30
@A31	11	12	GND
@D0	13	14	@D1
@D1	15	16	@D2
@D4	17	18	VADJ
GND	19	20	VADJ
3.3	21	22	@D5
@D6	23	24	3.3
NC	25	26	@D7
GND	27	28	@D8
@D9	29	30	GND
@D10	31	32	@D11
@D12	33	34	@D13
GND	35	36	3.3
@D14 *	37	38	@D15
	39	40	

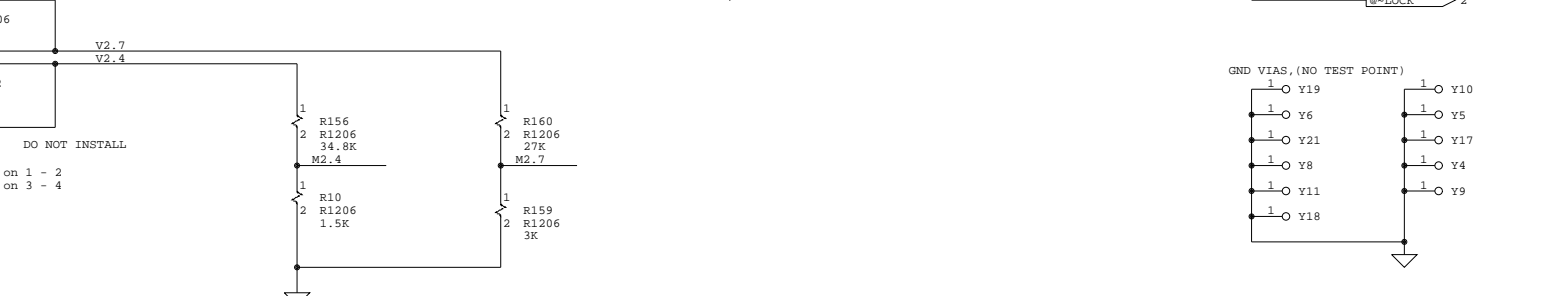
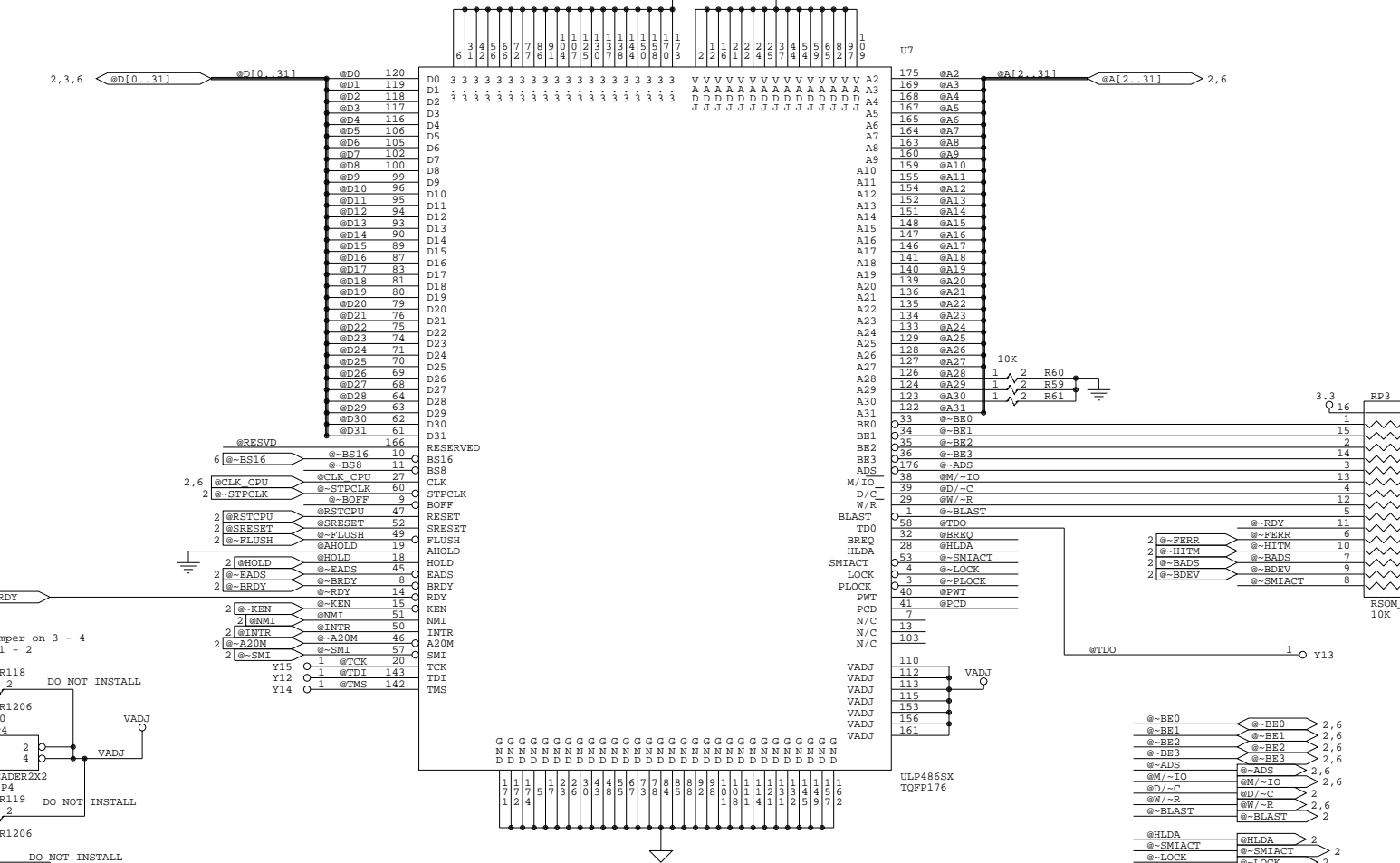
J4 Bottom			
GND	1	2	@D16
3.3	3	4	GND
@D18	5	6	@D17
@D20	7	8	@D19
GND	9	10	GND
3.3	11	12	@D21
@D22	13	14	@D23
GND	15	16	3.3
@D24	17	18	@D25
@D26	19	20	@D27
GND	21	22	@D29
@D28	23	24	@D31
@D30	25	26	@TD0
@-STPCLK	27	28	3.3
@-SMI	29	30	@-SMIACT
GND	31	32	@NMI
@SRESET	33	34	@-FLUSH
@INTR	35	36	@RSTCPU
GND	37	38	@-RAD5
@-A20M *	39	40	

HDR40\_50X50  
20X2  
SFM-120-L3-S-D

HDR40\_50X50  
20X2  
SFM-120-L3-S-D

HDR40\_50X50  
20X2  
SFM-120-L3-S-D

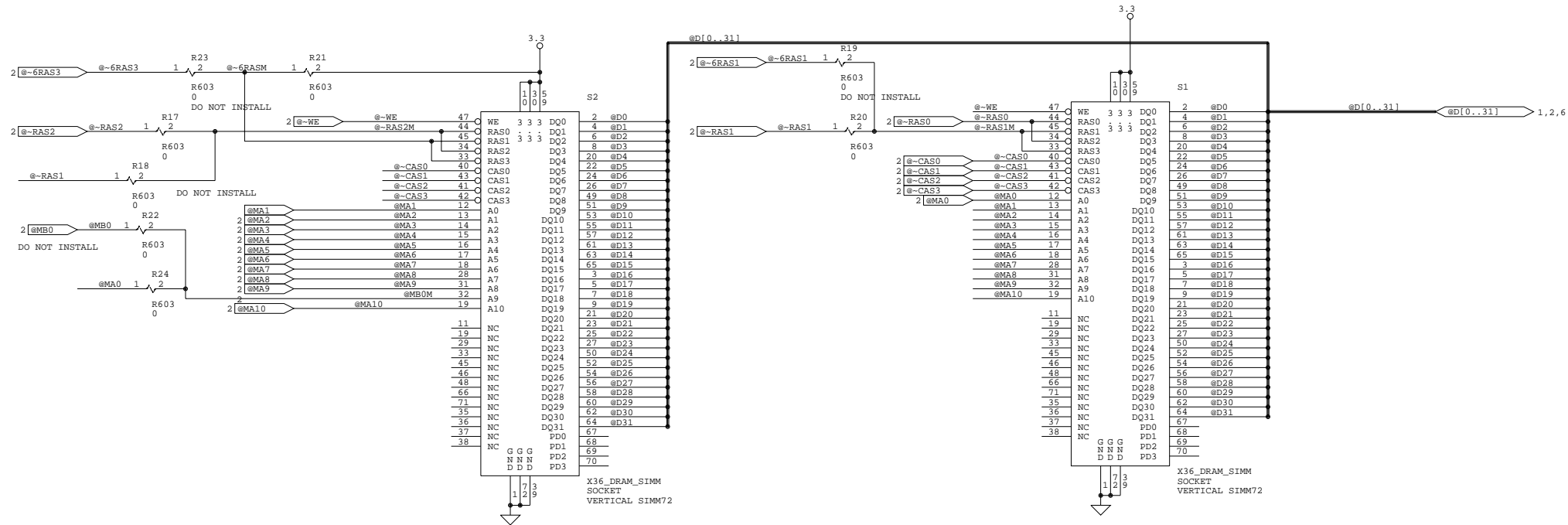
HDR40\_50X50  
20X2  
SFM-120-L3-S-D



NOTES:  
VCC is the +5 Volt Plane  
Signals starting with @ are 3.3V signals  
VCCP = +3.3V, VCC = VADJ for ULP 486SX Processor

Although RadiSys has verified this design to be functional.  
Neither RadiSys or Intel assume any responsibility for any errors  
which may appear in the design. Both RadiSys and Intel reserves  
the right to modify this design without notice.

RadiSys Corp.			
Title Humming Bird Eval BD CPU			
Size	Document Number	REV	
C	01-0193-02	1	
Date:	September 28, 1995	Sheet	1 of 11



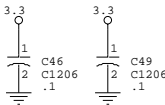
FLASH DRAM SOCKET

Socket Closest to Processor

DRAM SOCKET

Socket Closest to Edge of Board

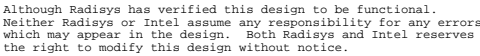
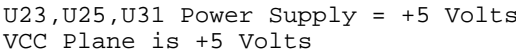
Power Supply for SIMM Sockets is +3.3 Volts



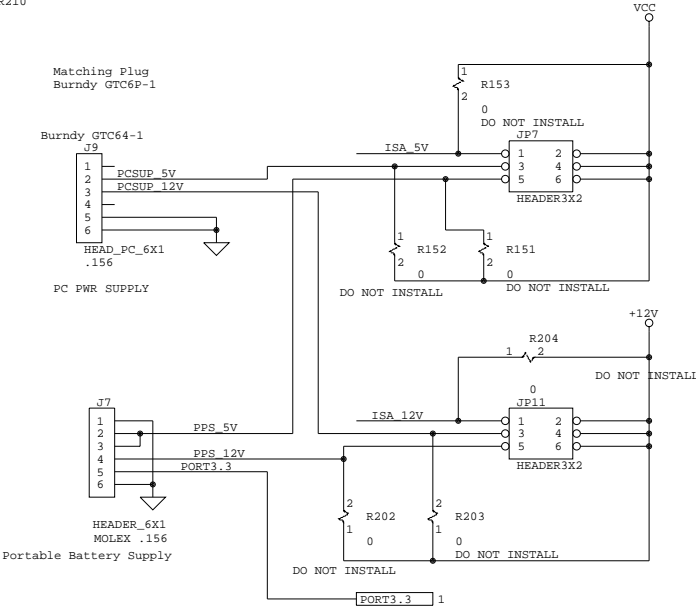
Although RadiSys has verified this design to be functional. Neither RadiSys or Intel assume any responsibility for any errors which may appear in the design. Both RadiSys and Intel reserves the right to modify this design without notice.

RadiSys Corp.			
Title			
HUMBIRD DRAM			
Size	Document Number	REV	
C	01-0193-02	1	
Date:	September 28, 1995	Sheet	3 of 12



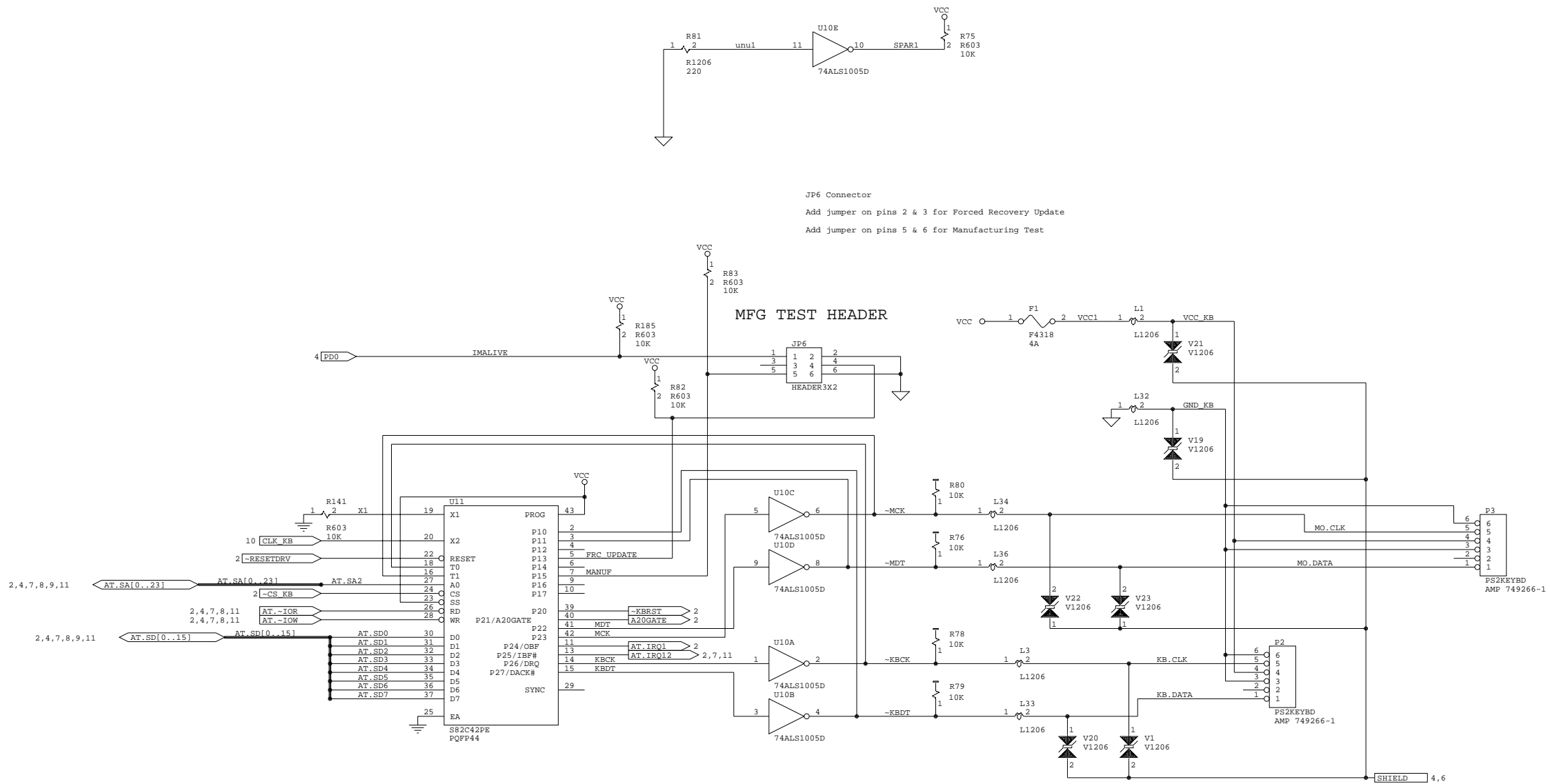


Radisys Corporation				
Title				
Humming Bird HEB, Flash File				
Size	Document Number			REV
C	01-0193-02			
Date:	September 28, 1995	Sheet	8 of	11



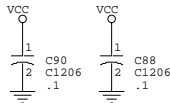
Although Radisys has verified this design to be functional. Neither Radisys or Intel assume any responsibility for any errors which may appear in the design. Both Radisys and Intel reserves the right to modify this design without notice.

RadiSys Corp.			
Title			
Humming Bird ISA Circuits			
Size	Document Number	REV	
C	01-0193-02	0	
Date:	September 28, 1995	Sheet	11 of 11



JP6 Connector  
Add jumper on pins 2 & 3 for Forced Recovery Update  
Add jumper on pins 5 & 6 for Manufacturing Test

U16 Power Supply = +5 Volts  
U17 Power Supply = +5 Volts  
VCC Plane is + 5 Volts



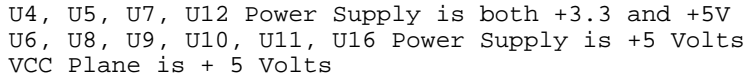
Although Radisys has verified this design to be functional.  
Neither Radisys or Intel assume any responsibility for any errors  
which may appear in the design. Both Radisys and Intel reserves  
the right to modify this design without notice.

Radisys Corp.			
Title			
Humming Bird Keyboard/Mouse			
Size	Document Number	REV	
C	01-0193-02	1	
Date:	September 28, 1995	Sheet	5 of 11

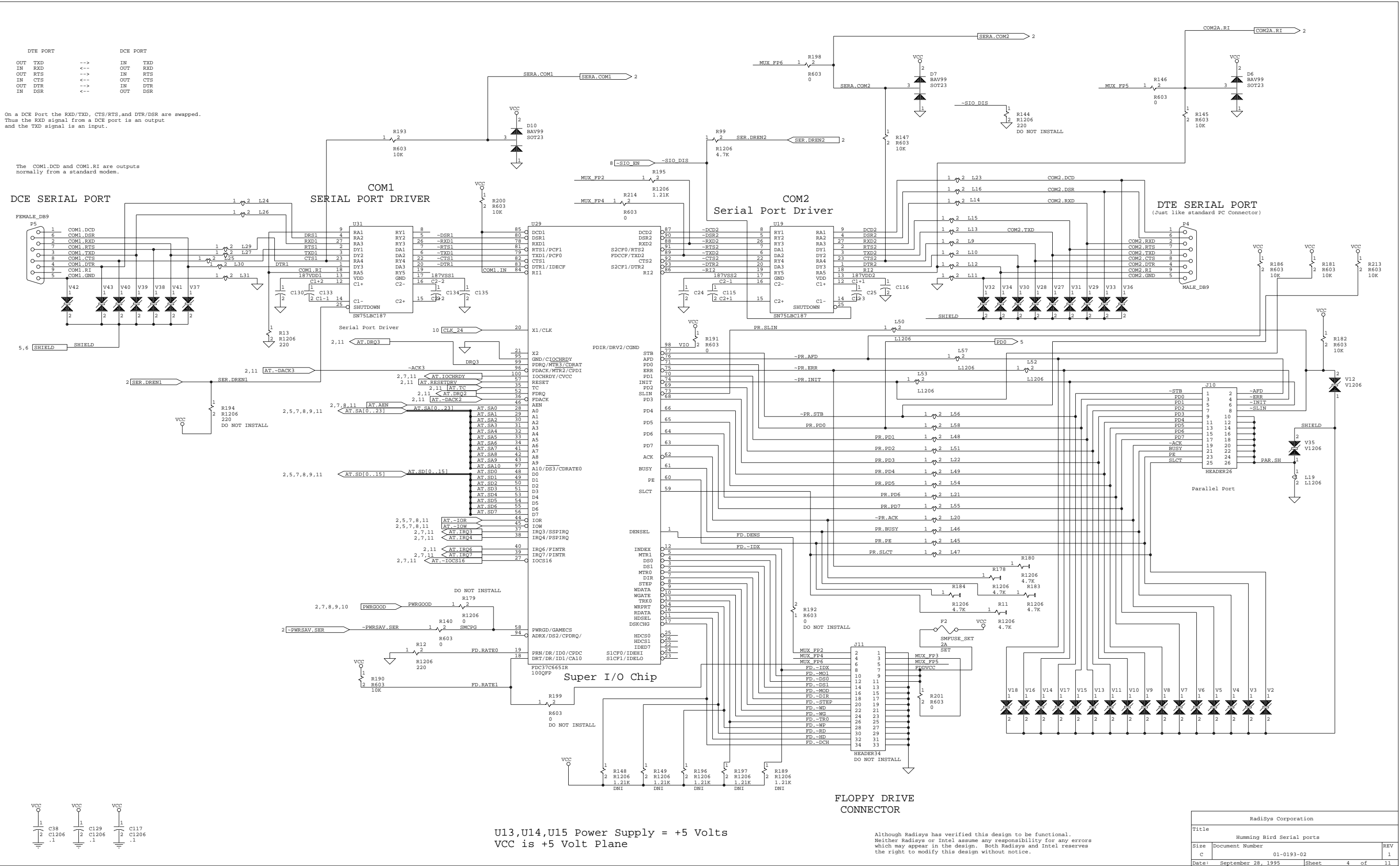
```

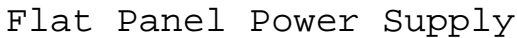
Programming of MA0-MA11 lines
At Rising edge of PWRGOOD
@MA0A LOW implies 160 Pin Pkg
@MA1 High implies 386, low implies 486
@MA4 High implies Intel Processor with L1 WB
@MA4 Low implies other CPUs
@MA11 Low implies internal RTC, High implies External

```



Although Radisys has verified this design to be functional. Neither Radisys or Intel assume any responsibility for any errors which may appear in the design. Both Radisys and Intel reserves the right to modify this design without notice.

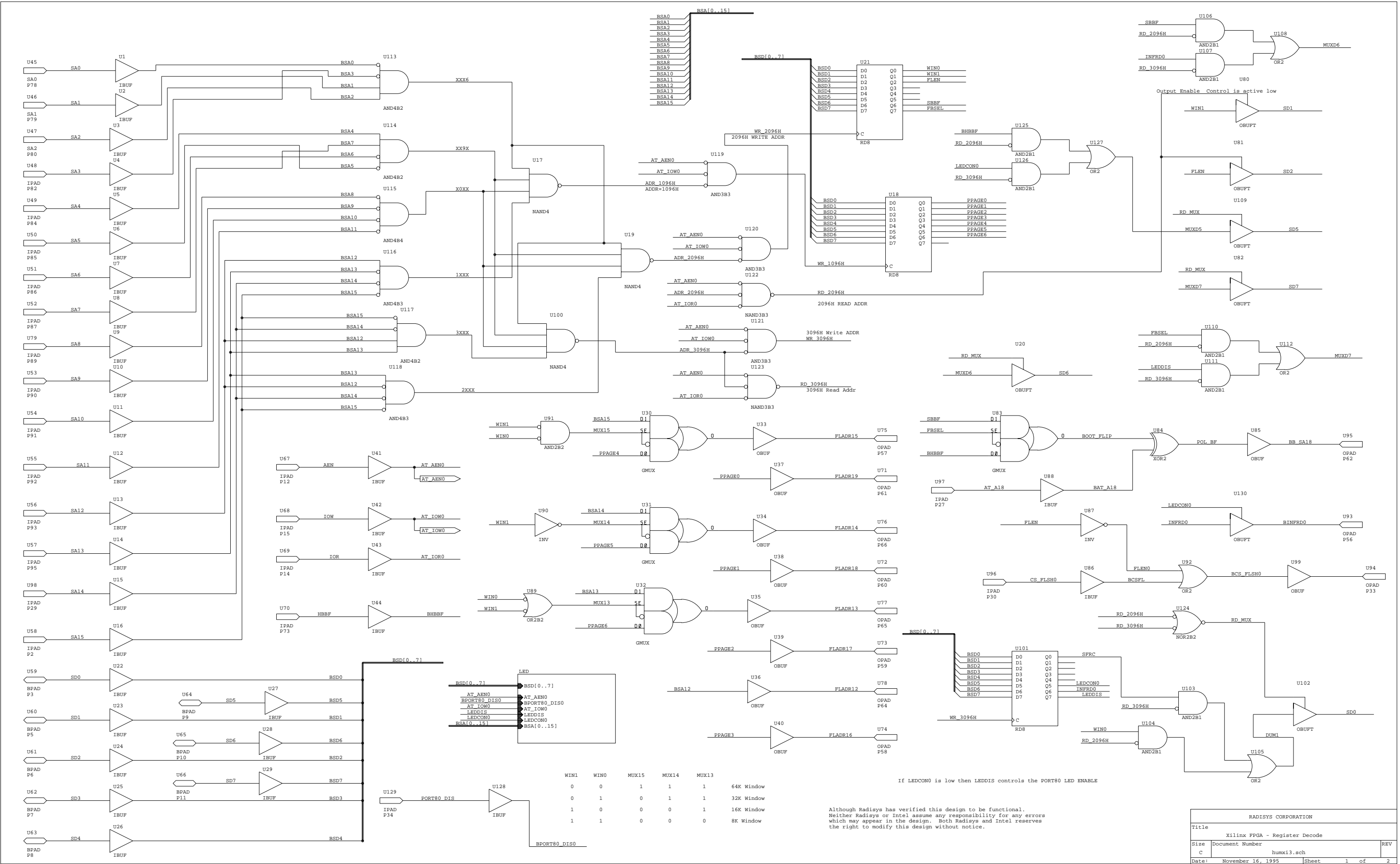




VCC Power Supply = +5 Volt Plane

Although Radisys has verified this design to be functional. Neither Radisys or Intel assume any responsibility for any errors which may appear in the design. Both Radisys and Intel reserves the right to modify this design without notice.

RADISYS CORPORATION				
Title				
Humming Bird Video Display				
Size	Document Number			REV
C	01-0193-02			
Date:	September 28, 1995	Sheet	6 of	11



WIN1	WIN0	MUX15	MUX14	MUX13	
0	0	1	1	1	64K Window
0	1	0	1	1	32K Window
1	0	0	0	1	16K Window
1	1	0	0	0	8K Window

Although Radisys has verified this design to be functional. Neither Radisys or Intel assume any responsibility for any errors which may appear in the design. Both Radisys and Intel reserves the right to modify this design without notice.

RADISYS CORPORATION		
Title		
Xilinx FPGA - Register Decode		
Size	Document Number	REV
C	humx13.sch	
Date:	November 16, 1995	Sheet 1 of 2