

Half word address = X...XX1 penalty: ____2____ cycles/instruction

Full word address = X...X01 penalty: ____3____ cycles/instruction

Full word address = X...X10 penalty: ____2____ cycles/instruction

Full word address = X...X11 penalty: ____3____ cycles/instruction

Address dependency penalty: ____2____ cycles/instruction

Data dependency penalty: ____2____ cycles/instruction

Maximum VDIV/VSQRT overlap: ____15____ cycles/instruction