



CtrlStateMachine

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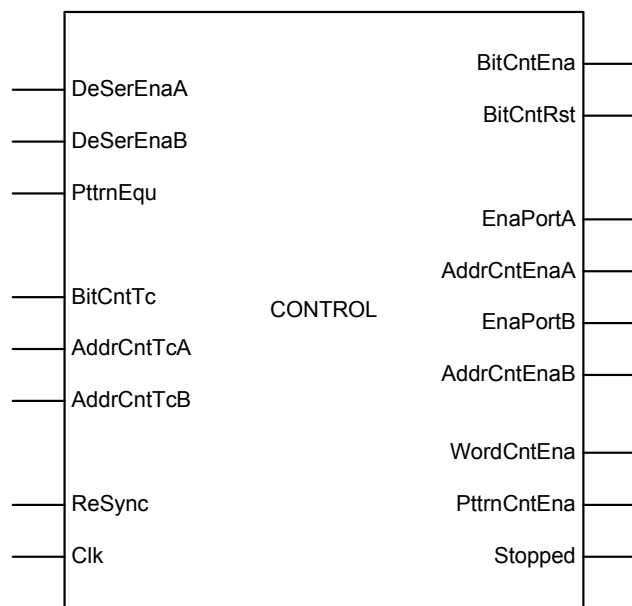
CRITICAL APPLICATIONS

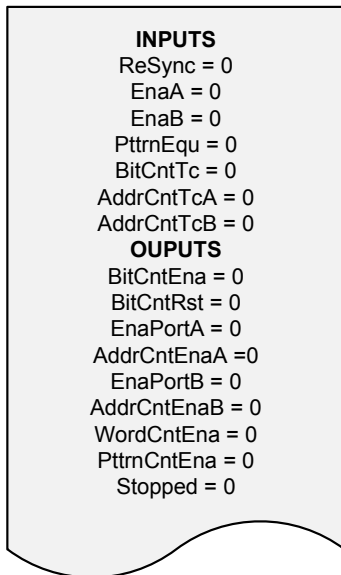
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The counter enables are high for one clock period only thanks to a proceed signal

