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ECE2700J SU23 RC3

Logic Optimization, K-map

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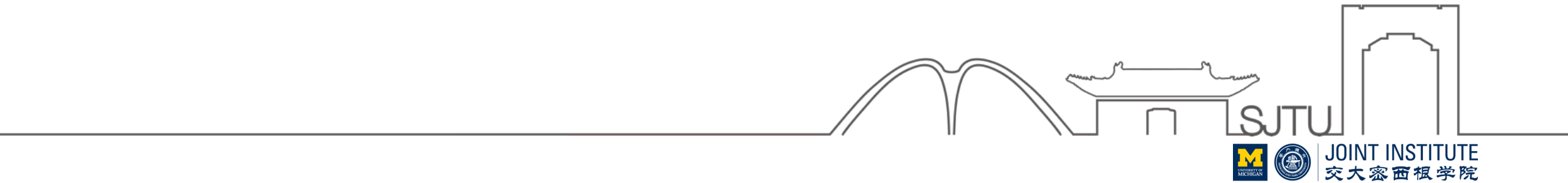


Logic Optimization

Simplification and Optimization

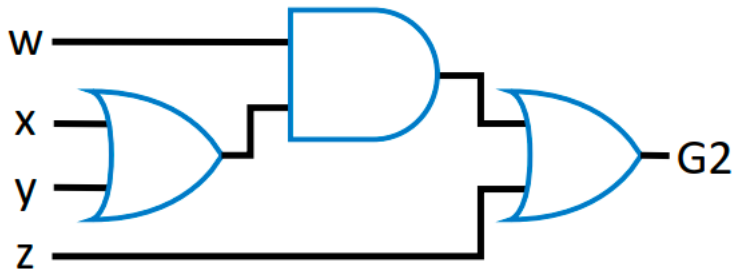
Intention : design a **better** circuit

– Delay – the time from input change to correct stable output response

- Every gate has delay of “1 gate-delay”, the gate delay of a circuit is judged by its critical path

– Size – the number of transistors

- Every gate input requires 2 transistors



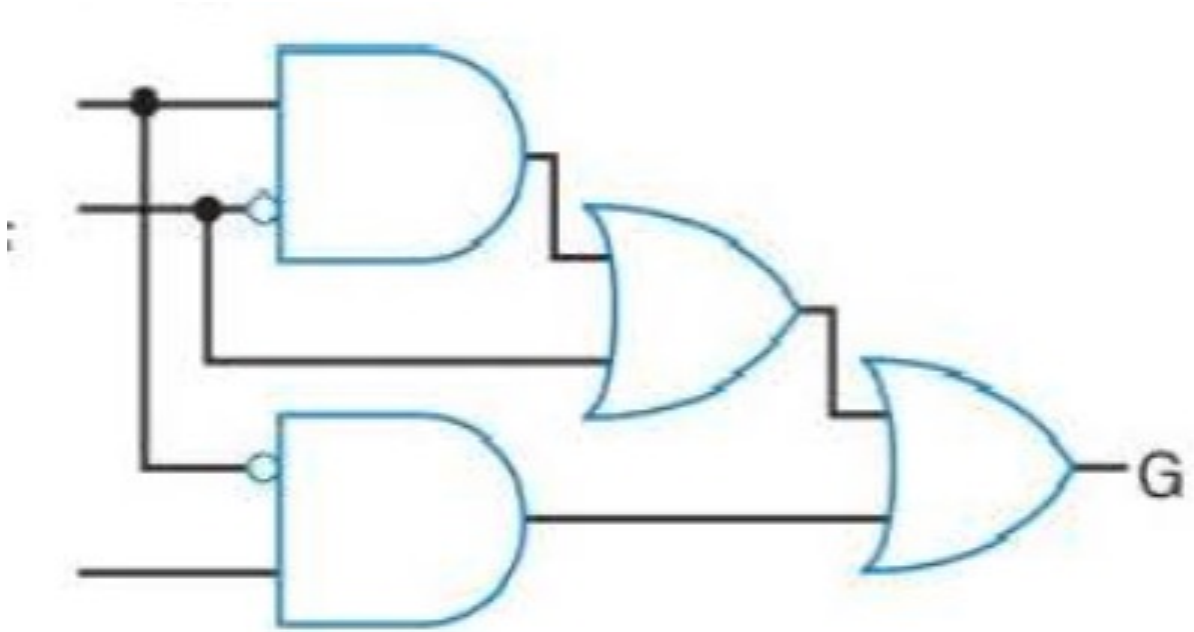
- Ignore inverters for simplicity

– Sacrifices delay for smaller size

Logic Optimization

Simplification and Optimization

Exercise: find the sizes and Delay of the following circuit



Logic Optimization

Simplification and Optimization

Exercise: Optimize the circuit with the equation shown below. Write its original size and delay and the optimized sizes and delay.

$$F = xyz + xyz' + x'y'z' + x'y'z$$

K-map

- A graphical technique used to simplify a logic equation

- A way to show the relationship between the logic inputs and corresponding output
  – Like truth table

- Much cleaner and more procedural than algebraic simplification by theorems of Boolean algebra.

- Theoretically, it can be used for any number of input variables,

– BUT is only practical for less than six, we will limit our discussion to logic equations with five or less variables

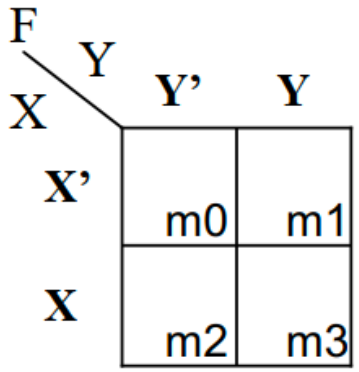
K-map

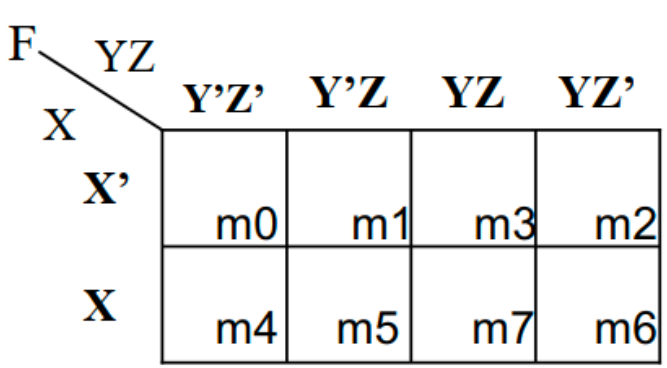
How to draw K-map

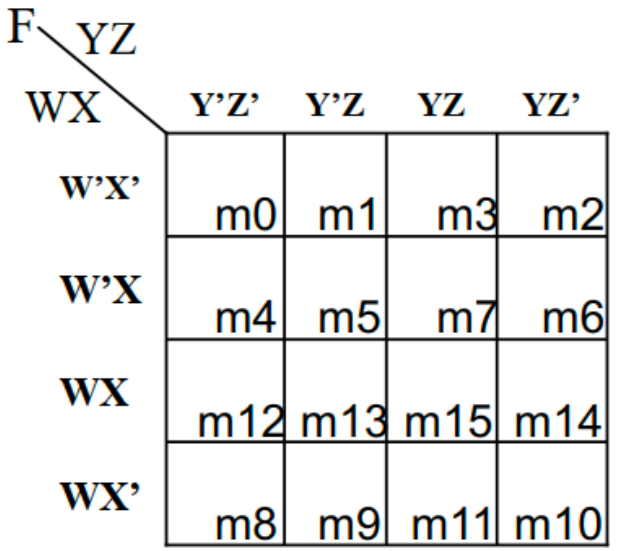
• K-map can be filled up directly from a truth table

– Each minterm corresponds to a cell in the K-map

• K-map cells are labeled so that both horizontal and vertical movement differ only in one variable







K-map

Basic knowledge of K-map

Label the Rows and Columns by 0 and 1

F: WX \ YZ	Y'Z' 00	Y'Z 01	YZ 11	YZ' 10
W'X' 00	1 m0	0 m1	1 m3	1 m2
W'X 01	0 m4	0 m5	1 m7	0 m6
WX 11	1 m12	0 m13	1 m15	0 m14
WX' 10	1 m8	1 m9	0 m11	0 m10

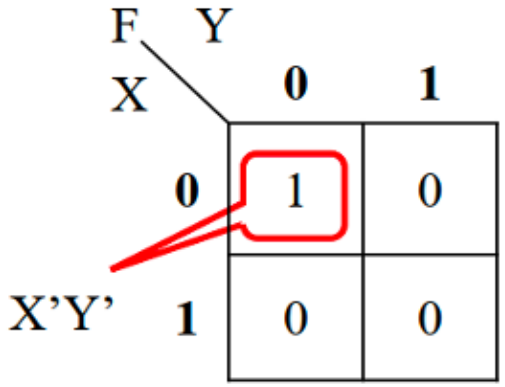
We can write a equation from K-map similar to truth table.

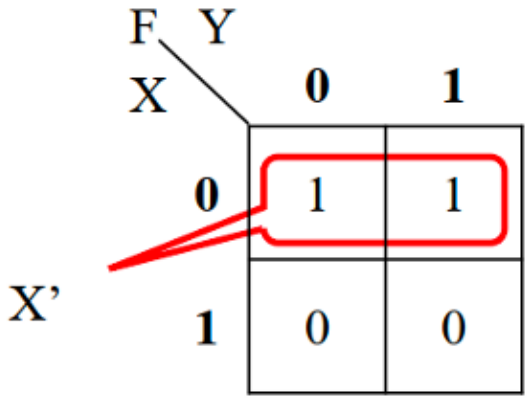
F=

K-map

Grouping and Cancelling

Why we need to group?



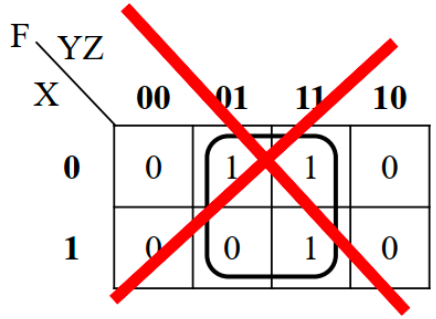


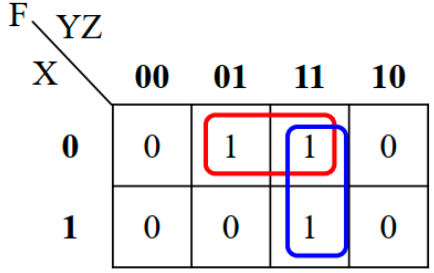
K-map

Grouping and Cancelling

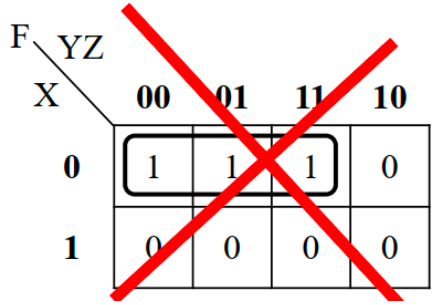
Grouping rules

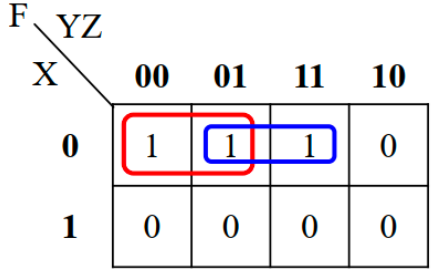
- **No zeros in the group**



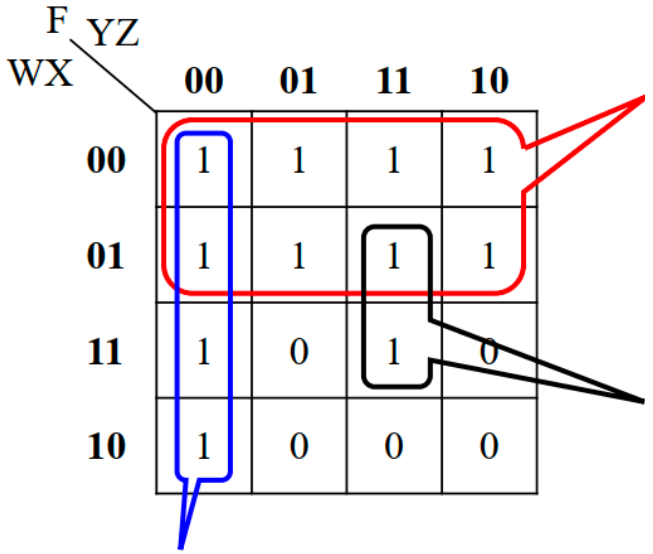


- **The number of 1's in a group should be 2^N, N = 0, 1, 2, …**

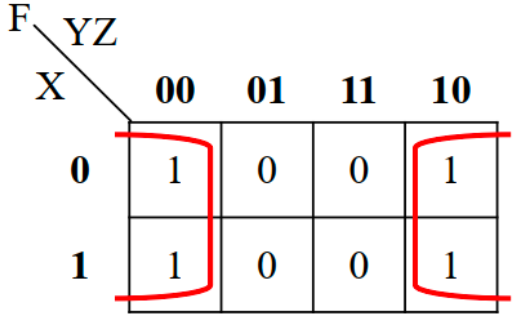




- **Group as many adjacent 1's as possible**



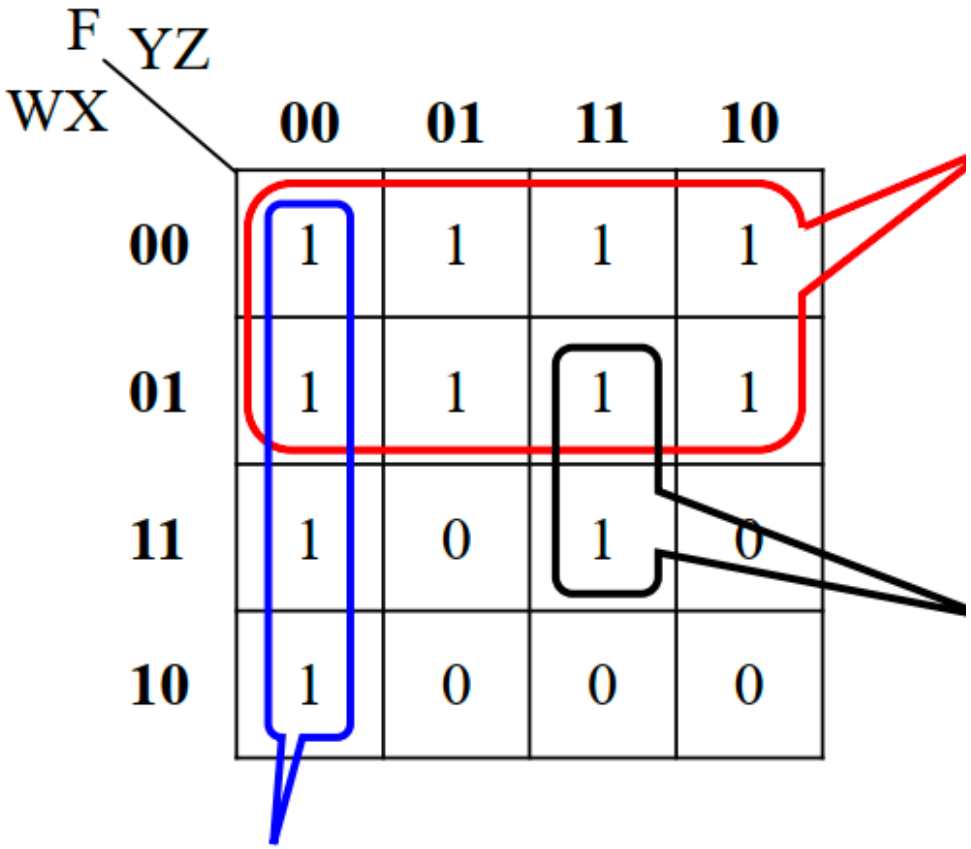
- **Edges wrap around**



K-map

Grouping and Cancelling

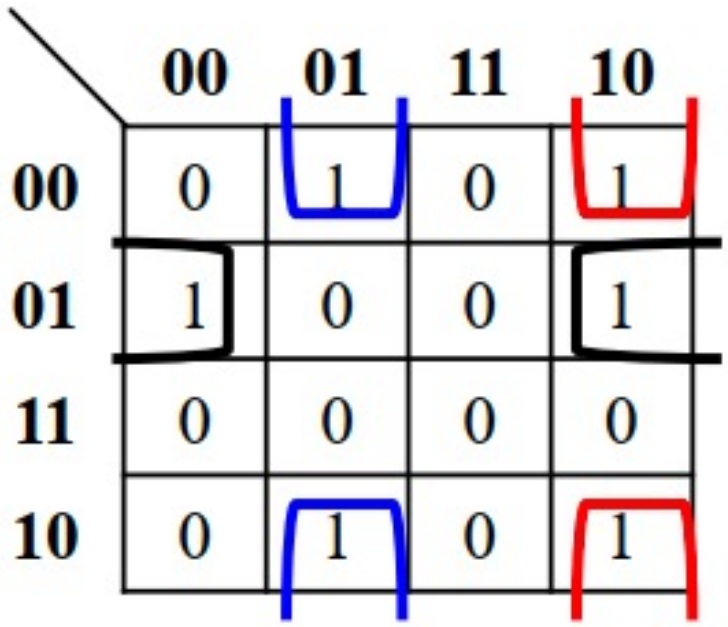
How to quickly use group to simplify a circuit?



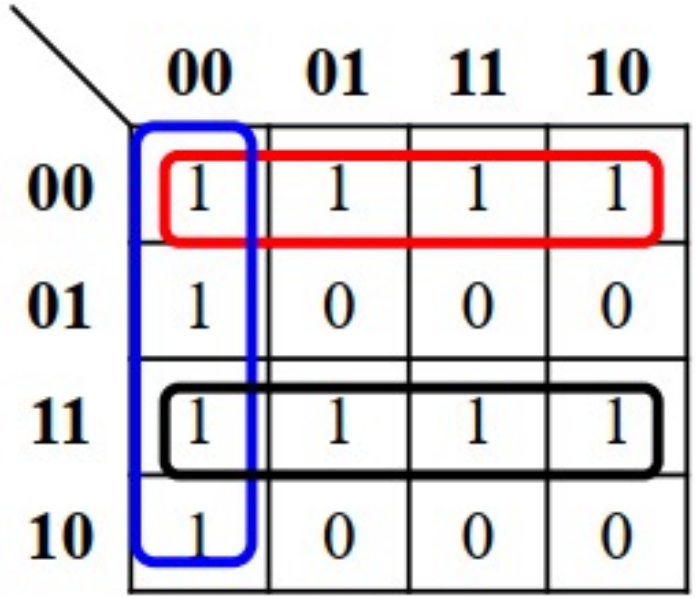
K-map

Grouping and Cancelling

Exercise: Quickly write the equation of the circled groups.



	00	01	11	10
00	0	1	0	1
01	1	0	0	1
11	0	0	0	0
10	0	1	0	1



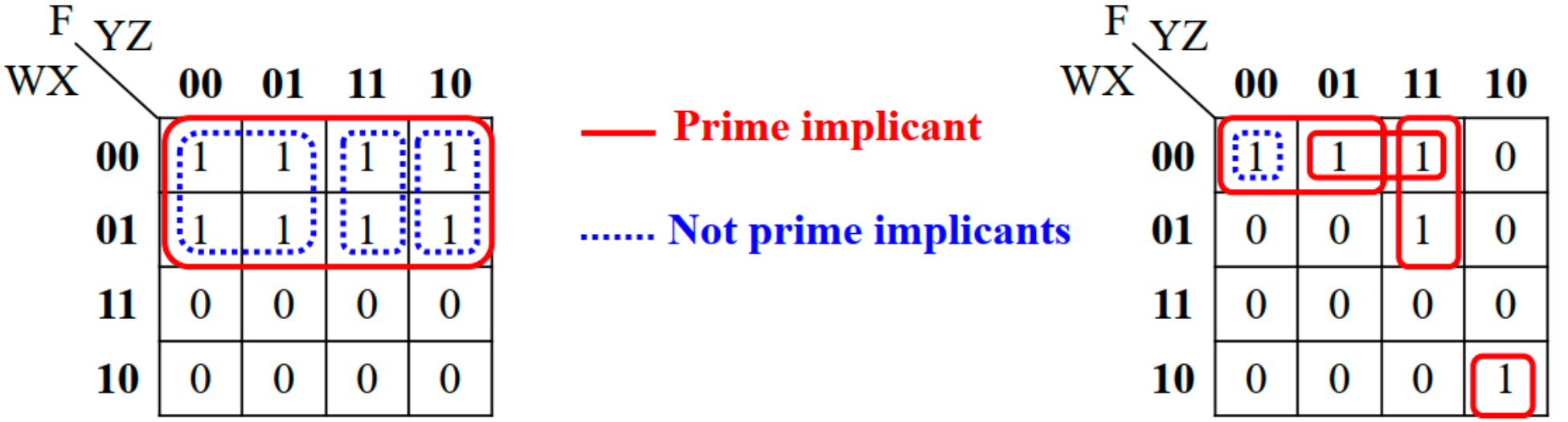
	00	01	11	10
00	1	1	1	1
01	1	0	0	0
11	1	1	1	1
10	1	0	0	0

K-map

Prime Implicants

- Implicant: is a product term

- A prime implicant (PI) is a group that cannot be entirely contained by another implicant



K-map

Prime Implicant

Exercise: Find all PIs in the following K-map

F WX \ YZ	00	01	11	10
00	1	0	1	1
01	0	1	1	0
11	0	1	1	0
10	1	1	1	1

Essential Prime Implicant

- A prime implicant (PI) is essential if a a cell is covered ONLY by that PI

- The essential PIs can be found by
  - looking at each cell marked as 1 and not covered by any other essential PI
  - and checking the number of PIs that cover it

F WX \ YZ	00	01	11	10
00	1	0	1	1
01	0	1	1	0
11	0	1	1	0
10	1	1	1	1

K-map

Essential Prime Implicant

- Essential PIs **have to be used in the simplified equation**

- Cells not covered by essential PIs can be represented by any PIs covering them

F: WX \ YZ	00	01	11	10
00	1	0	1	1
01	0	1	1	0
11	0	1	1	0
10	1	1	1	1

Don’t care conditions in K-map

- The possible input combinations might not be all valid or not for consideration for a device
  - Hence we don’t care what the corresponding outputs are under those conditions
  - Called **don’t care** conditions
  - Mark the corresponding outputs by **X**

A	B	C	D	F
0	*0*	*0*	*0*	*X*
0	0	0	1	0
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	*0*	*0*	*1*	*X*
1	0	1	0	1
1	0	1	1	1
1	*1*	*0*	*0*	*X*
1	*1*	*0*	*1*	*X*
1	*1*	*1*	*0*	*X*
1	1	1	1	1