ANIL SHARMA

b150023ec@nitsikkim.ac.in +91-7602579355 Boys Hostel 10, NIT SIKKIM, Ravangla, South Sikkim, India

EDUCATION

B.Tech in **Electronics and Communication Engineering**, Final Year   2015-2019
National Institute of Technology Sikkim, India
- CGPA of **7.52**/10 (Dec 2018)

High School - Govt. Senior Secondary School, Rhenock, Sikkim (CBSE) - 78.4%   2015
Secondary School - Govt. Senior Secondary School, Rhenock, Sikkim (CBSE) - 8.0/10   2013

EXPERIENCE

Summer Trainee, Motilal Nehru National Institute of Technology   Jun, 2017 - Jul, 2017
30 days of training on VLSI Design and Embedded System (working with AT89C51 Micro-controller Board, Verilog HDL, ISE design suite, mikroC Pro and Proteus). Made a Digital Clock using 8051 Micro controller development kit and 64 bit single port RAM using Verilog.

Summer Intern, Indian Institute of Technology Bombay   May, 2018 - July, 2018
Mentored by Prof. Virendra Singh, Dept. of Electrical Engineering, IIT Bombay.
Implemented an efficient Single Pipelined 128 bit AES (Advanced Encryption Standard) Encryption algorithm for obtaining high throughput and frequency of about 1 GHz for high speed operation and also save the area of FPGA for better implementation. I have used Verilog HDL, Vivado Software and Basys3 FPGA development board for the implementation.

TECHNICAL SKILLS

Strongest Areas –Digital Design, RTL Coding, Algorithms and Cryptography.
Languages
- **Proficient -** Verilog HDL, Assembly Language, C and Core Java.
- **Familiar –** System Verilog**,** Python 3, and SQL.

Software Packages
- **Proficient** - ISE Design Suite, Vivado, MATLAB, IntelliJ IDEA and mikroC pro and Microsoft Office.
- **Familiar -** ModelSim, HFSS 13.0, Proteus, Arduino, Git and NetBeans.

Tools
- **EDA Tools -** Familiar with Cadence Virtuoso and Mentor Graphics.
- **FPGA -** Familiar with Basys-3 and Spartan-3E kit.

Platforms
- Comfortable with Windows and Linux (Ubuntu) Systems.

RELEVANT COURSES

Logic Design, Electronic Circuits, Micro Processor & Micro Controller, Computer Organization and Architecture, Analog & Digital Communication, Digital Signal Processing, Analog MOS IC, Computer Networks, Wireless Communication and Cryptography.

PROJECTS

Projects done during Summer Internship/Training and Curriculum
- Working on Frequency Selective Surfaces (**Final Year Project**).
- **Single Pipelined 128 bit AES Algorithm –** Implemented a Single Pipelined 128 bit AES Encryption algorithm using the Verilog and Basys-3 FPGA Development Board.
- **Digital Clock -** Design a Digital Clock using Embedded C, AT89C51 micro Controller board and displayed the DATE, TIME and NAMES of days on 16X2 LCD Screen of the development board.
- **Class C Power Amplifier -** Design a Class C Power Amplifier for RF application Using Cadence Virtuoso Software.

WORKSHOPS ATTENDED	* Workshop on Arduino by TechTrunk Ventures Pvt. Ltd during Abhiyantran'15. Made Robocar which senses the edges and moves accordingly.
	* Workshop on Open Source by Kharagpur Open Source Society (KOSS). Contributed to Open Source, learned basics of Git and GitHub and participated in Hacktober fest.
ACHIEVEMENTS AND AWARDS	* Student Training and Placement Coordinator of NIT Sikkim.
	* Technical Team Lead of Udgam'17 – Cultural fest of NIT Sikkim.
	* Technical Team Lead of Abhiyantran'17 – Tech fest of NIT Sikkim.
	* Active participation on online competitive coding website – HackerRank, HackerEarth etc.
EXTRA CURRICULAR ACTIVITIES	* Represented NIT Sikkim at "Inter NIT Sports Meet 2015" in Football as Goal Keeper.
	* Winner in Football Tournament during Annual Sports Week 2016 at NIT Sikkim.
	* Runner up of the "Treasure Hunt" competition in Annual Technical Fest.
	* Won Silver Medal in "State Level Karate Championship" during April 2012.
HOBBIES	Loves to code, Singing, Travelling, Playing Chess, Cricket and Football.