



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD, ANDHRA PRADESH, INDIA
CONSOLIDATED MARKS MEMO / CREDIT SHEET

MM. No.: **C 339906** BACHELOR OF TECHNOLOGY -- ELECTRICAL & ELECTRONICS ENGINEERING
Roll No.: **752437601222** Name of the College: **K.J.E.T., KORANGLE.G.DIST.**
ame: **DUKKA RAJESH** Month & Year of Final Exam: **June, 2007**

ul Ticket No.:		03B25A0203		Year of Admission		2003-2004		Class Awarded		*** SECOND CLASS ***	
COURSE TITLE		INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S No.	COURSE TITLE		INT. MARKS	EXT. MARKS	TOTAL
THEORY		20	80	100			LAB		25	50	75

I YEAR

DIRECT ADMISSION INTO II-YEAR UNDER LATERAL ENTRY SCHEME

I SEMESTER				II SEMESTER			
1. MATHEMATICS - II	14	28	42	1. MANAGERIAL ECONOMICS & FINANCIAL A	18	36	54
2. HYDRAULICS & HYDRAULIC MACHINERY	14	37	51	2. MATHEMATICS - III	9	31	40
3. PULSE & DIGITAL CIRCUITS	17	29	46	3. LINEAR & DIGITAL IC APPLICATIONS	11	29	40
4. SWITCHING THEORY & LOGIC DESIGN	13	35	48	4. POWER SYSTEMS - I	16	30	46
5. ELECTROMAGNETIC FIELDS	14	23	42	5. ELECTROMECHANICS - II	15	43	58
6. ELECTRO MECHANICS - I	20	29	49	6. CONTROL SYSTEMS	11	29	40
7. HYDRAULICS & HYDRAULIC MACHINERY (LAB)	23	21	44	7. IC & PULSE & DIGITAL CIRCUITS (LAB)	22	33	55
8. NETWORKS (LAB)	22	33	55	8. ELECTROMECHANICS (LAB) - I	21	39	60

I SEMESTER				II SEMESTER			
1. COMPUTER ORGANIZATION	10	30	40	1. ANALYSIS OF LINEAR SYSTEMS	13	29	42
2. ELECTRICAL MEASUREMENTS	9	48	57	2. MICROPROCESSORS AND INTERFACING	13	29	42
3. POWER SYSTEMS-II	9	31	40	3. MODELLING OF POWER SYSTEM COMPONENTS	15	42	57
4. POWER ELECTRONICS	8	44	52	4. INSTRUMENTATION	14	29	43
5. ELECTROMECHANICS-III	7	38	45	5. POWER SYSTEMS - III	12	29	41
6. MANAGEMENT SCIENCE	15	39	54	6. UTILIZATION OF ELECTRICAL ENERGY	15	41	56
7. ELECTROMECHANICS (LAB) - II	15	40	55	7. ELECTRICAL MEASUREMENTS LAB	23	44	67
8. CONTROL SYSTEMS (LAB)	20	37	57	8. POWER ELECTRONICS LAB	22	45	67

I SEMESTER				II SEMESTER			
1. DIGITAL SIGNAL PROCESSING	14	28	42	1. H.V.D.C. TRANSMISSION	20	28	48
2. POWER SEMICONDUCTOR DRIVES	17	39	56	2. DATABASE MANAGEMENT SYSTEMS	17	39	56
3. COMPUTER METHODS IN POWER SYS.	17	28	45	3. PROJECT & SEMINAR	37	138	175
4. POWER SYSTEM OPERATION & CONTROL	12	28	40				
5. H.V. ENGINEERING	16	28	44				
6. ELECTRICAL DISTRIBUTION SYS.	17	39	56				
7. MICROPROCESSORS LAB	21	39	60				
8. SIMULATION OF ELEC. SYS. LAB	24	44	68				

Number of Credits registered for: 156
Aggregate Marks Secured for best: 156 CREDITS TOTAL 2262 OUT OF 4150 (54.51 %)

Date of Declaration of Result: June 22, 2007
(See overleaf for instructions) (*Courses registered but not counted for calculation of aggregate)

B. Deep Kumar
CONTROLLER OF EXAMINATIONS