

# BOARD OF SECONDARY EDUCATION ANDHRA PRADESH

GG 0450431



REGULAR PC/22/21385/350431/2

## SECONDARY SCHOOL CERTIFICATE

**CERTIFIED THAT** **PUTHA VISWANADHA REDDY** bearing Roll No **0684271**  
**S/O PUTHA NAGASUBBA REDDY**  
 belongs to **SURYA HIGH SCHOOL KAMALAPURAM**

has appeared and **PASSED SSC EXAMINATION** held in **MARCH 2008** in **FIRST**

Division with **TELUGU** as medium of instruction.

DATE OF BIRTH		DAY	MONTH	YEAR
	16/04/1993	ONE SIX	APRIL	ONE NINE NINE THREE

### THE CANDIDATE SECURED THE FOLLOWING PERCENTAGE OF MARKS

SUBJECT	Marks Secured (in figures)	Marks Secured (in words)
FIRST LANGUAGE : ( TELUGU )	87	EIGHT SEVEN
THIRD LANGUAGE : ENGLISH	72	SEVEN TWO
MATHEMATICS :	82	EIGHT TWO
GENERAL SCIENCE :	78	SEVEN EIGHT
SOCIAL STUDIES :	86	EIGHT SIX
TOTAL :	405	FOUR ZERO FIVE
SECOND LANGUAGE : ( HINDI )	78	SEVEN EIGHT
GRAND TOTAL :	483	FOUR EIGHT THREE
Life Skills Education : GRADE SECURED :		
Marks of Identification : <i>A black mole on the right fore arm.</i> <i>A black mole on the right thumb</i>		

Head of Institution  
with School Stamp

*P.V. Sankar*  
**HEAD MASTER**  
**SURYA HIGH SCHOOL**  
**KAMALAPURAM-515 289**

Date of issue : 15.05.2008

*W. S. Lakshmi*  
**SECRETARY**  
**BOARD OF SECONDARY EDUCATION**  
**A.P. HYDERABAD**

1. Life skills Education : The Grade shall be incorporated by the respective Head of the Institution before delivery of the certificates to the candidates.
2. Any corrections in the certificate will not be entertained after one year from the date of issue.
3. Any unauthorised correction in the certificate will result in cancellation of certificate.
4. The Marks with asterisk indicate the old marks secured in previous appearances.

2008





Sl. No. J160236



# Board of Intermediate Education, A. P.

Vidya Bhavan, Nampally, Hyderabad - 500 001



## INTERMEDIATE PASS CERTIFICATE CUM MEMORANDUM OF MARKS

This is to certify that **PUTHA VISWANADHAREDDY** son  
of **PUTHA NAGASUBBAREDDY** bearing  
Registered No. **1009219445** has appeared at the Intermediate Public  
Examination held in **MARCH-2010** and passed in **A GRADE**  
with **ENGLISH** as the medium of instruction.

The subjects in which he was examined and the marks awarded are as follows:

Subject	I Year		II Year	
	Maximum Marks	Marks Secured	Maximum Marks	Marks Secured
Part - I : <b>ENGLISH</b>	100	084	100	079
Part - 2 : <b>SANSKRIT</b>	100	096	100	096
Part - 3 : Optional Subjects				
<b>MATHEMATICS - A</b>	075	073	075	074
<b>MATHEMATICS - B</b>	075	073	075	075
<b>PHYSICS</b>	060	054	060	050
<b>CHEMISTRY</b>	060	051	060	059
<b>PHYSICS PRACTICAL</b>			030	030
<b>CHEMISTRY PRACTICAL</b>			030	030
<b>ENVIRONMENTAL EDUCATION</b>	<b>Q U A L I F I E D</b>			
Total Marks in Figures	<b>924 ⇒ 924</b>			
In words	<b>"NINE""TWO""FOUR"</b>			
Date	<b>30-04-2010</b>			

PRINCIPAL

SRI CHAITANYA CO-EDUCATIONAL Jr. COLLEGE

P.M.R. Colony, Hyderabad



Controller of Examinations

1009219445





# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

## CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM No. JAC 129014

Serial No. 000130762

Name PUTHA VISWANADHA REDDY

Course ELECTRICAL & ELECTRONICS ENGINEERING

Admission No. 104G1A02B8

Year of Admission 2010

Year of Completion 2014

Name of the College SJRT-ROTARY PURAM ANANTAPUR

Class Awarded First Class

S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
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### I YEAR

1 ENGINEERING DRAWING	30	36	66	6	7 ENGINEERING PHYSICS	18	44	62	4
2 ENGINEERING CHEMISTRY	27	50	77	4	8 ENGINEERING & I.T. WORKSHOP	23	42	65	4
3 PROGRAMMING IN C AND DATA STRUCTURES	24	32	56	6	9 C PROGRAMMING & DATA STRUCTURES LAB	23	42	65	4
4 MATHEMATICS-I	30	57	87	6	10 ENGINEERING PHYSICS AND ENGINEERING CHEMISTRY LAB	23	40	63	4
5 MATHEMATICAL METHODS	28	41	69	6	11 ENGLISH LANGUAGE & COMMUNICATION SKILLS LAB	21	33	54	4
6 ENGLISH	20	50	70	4					

### I SEMESTER

### II YEAR

### II SEMESTER

1 FLUID MECHANICS AND HYDRAULIC MACHINERY	25	38	63	4	1 ANALOG ELECTRONIC CIRCUITS	21	44	65	4
2 ENVIRONMENTAL SCIENCE	24	47	71	4	2 NETWORK THEORY	25	42	67	4
3 ELECTRICAL CIRCUITS	22	30	52	4	3 ELECTRICAL MACHINES - II	19	33	52	4
4 ELECTRONIC DEVICES AND CIRCUITS	21	27	48	4	4 GENERATION OF ELECTRIC POWER	22	27	49	4
5 ELECTRICAL MACHINES-I	15	34	49	4	5 SWITCHING THEORY AND LOGIC DESIGN	24	29	53	4
6 MATHEMATICS-II	26	36	62	4	6 ELECTROMAGNETIC FIELDS	20	39	59	4
7 BASIC FLUID MECHANICS AND HYDRAULIC MACHINES LAB	24	42	66	2	7 ELECTRICAL MACHINES LAB-I	20	44	64	2
8 ELECTRONIC DEVICES & CIRCUITS LAB	22	37	59	2	8 ELECTRIC CIRCUITS AND SIMULATION LAB	22	33	55	2

### I SEMESTER

### III YEAR

### II SEMESTER

1 TRANSMISSION OF ELECTRIC POWER	25	56	76	4	1 POWER SEMICONDUCTOR DRIVES	20	32	52	4
2 CONTROL SYSTEMS	28	36	64	4	2 POWER SYSTEM ANALYSIS	27	34	61	4
3 MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	20	27	47	4	3 POWER SYSTEM OPERATION AND CONTROL	15	47	62	4
4 POWER ELECTRONICS	26	35	61	4	4 LINEAR & DIGITAL IC APPLICATIONS	24	25	49	4
5 ELECTRICAL MACHINES-III	23	37	60	4	5 MANAGEMENT SCIENCE	17	25	42	4
6 ELECTRICAL & ELECTRONIC MEASUREMENTS	27	47	74	4	6 MICROPROCESSORS AND MICROCONTROLLERS	24	28	52	4
7 ELECTRICAL MACHINES LAB-II	21	40	61	2	7 ADVANCED ENGLISH COMMUNICATION SKILLS LAB	22	41	63	2
8 CONTROL SYSTEMS AND SIMULATION LAB	17	44	61	2	8 ELECTRICAL MEASUREMENTS LAB	23	40	63	2

### I SEMESTER

### IV YEAR

### II SEMESTER

1 DIGITAL SIGNAL PROCESSING	27	34	61	4	1 PRINCIPLES OF POWER QUALITY	19	36	55	4
2 SWITCH GEAR AND PROTECTION	17	36	53	4	2 UTILIZATION OF ELECTRICAL ENERGY	21	39	60	4
3 RENEWABLE ENERGY SOURCES	19	40	59	4	3 MODERN CONTROL THEORY	23	55	78	4
4 SOFT COMPUTING TECHNIQUES	21	31	52	4	4 EMBEDDED SYSTEMS	19	29	48	4
5 DISTRIBUTION OF ELECTRIC POWER	22	55	77	4	5 SEMINAR	48	0	48	2
6 FUNDAMENTALS OF HVDC & FACTS DEVICES	26	46	72	4	6 PROJECT WORK	50	125	175	10
7 MICROPROCESSORS AND MICROCONTROLLERS LAB	20	39	59	2					
8 POWER ELECTRONICS AND SIMULATION LAB	24	30	54	2					

Number of Credits registered for : 220  
 Total Credits Acquired : 220  
 Aggregate Marks Secured for best : 3477  
 Percentage of Marks : 66.87%  
 Date : July 2014  
 CMM Issue Date :  
 (See overleaf for instructions)

Max. Marks : 5200

*E. Madan Reddy*

(\*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS

CC080029

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR



ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

H.T.No:104G1A02B8

*Putha Viswanadha Reddy*

*He Putha Nagasubba Reddy*


having fulfilled the academic requirements and passed the examination held  
during *June, 2014* in \*\*\* *First Class* \*\*\*

has this day been admitted by the Monitoring & Development Committee to  
the Degree of

*Bachelor of Technology*  
(ELECTRICAL & ELECTRONICS ENGINEERING)

Wednesday, 24 December, 2014.

Given under the Seal of the University

  
Director of Evaluation

  
Registrar

  
Vice Chancellor



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

ANANTAPUR - 515 002, ANDHRA PRADESH, INDIA

Adm. No. 104G1A02B8

Sl. No. PC 0138503



**PROVISIONAL CERTIFICATE**

*This is to certify that Mr. PUTHA VISWANADHA REDDY*

*Son of Sri PUTHA NAGASUBBA REDDY, Passed the*

**\*\*\* ELECTRICAL & ELECTRONICS ENGINEERING \*\*\***

*Examination of this University held in the month of June 2014 and that he was placed in*

**\*\*\* First Class \*\*\***

*He has satisfied all the requirements for the award of B.TECH Degree of the*

*Jawaharlal Nehru Technological University Anantapur, Ananthapuramu.*

*Thursday 21 August 2014*

DIRECTOR OF EVALUATION  
Anantapur - A.P.

REGISTRAR