

## SE2432L: 2.4 GHz Smart Energy/ZigBee Front End Module

### **Applications**

- Smart Meters
- In-home appliances
- Smart thermostats

#### **Features**

- Integrated PA with up to 24 dBm output power
- Integrated LNA with programmable bypass
- Integrated antenna switching with Tx and Rx diversity function
- Low FEM noise figure of 2 dB typical
- Differential Rx/Tx interface with integrated baluns
- Fast switch ON/OFF time <800 nsec</li>
- 2.0 V 3.6 V supply operation
- Sleep mode current 0.05 μA typical
- 3 x 4 x 0.9 mm 24 pin QFN, NiPdAu plated
- Pb-free, RoHS compliant and Halogen free

## **Ordering Information**

Part No.	Package	Remark
SE2432L-S	24 pin QFN	Samples
SE2432L-R	24 pin QFN	Tape & Reel
SE2432L-EK1	N/A	Evaluation kit

#### **Product Description**

The SE2432L is a high performance, fully integrated RF Front End Module designed for ZigBee/Smart Energy applications.

The SE2432L is designed for ease of use and maximum flexibility, with integrated fully matched input baluns, integrated inter-stage matching and harmonic filter, and digital controls compatible with 1.6 – 3.6 V CMOS levels.

The RF blocks operate over a wide supply voltage range from 2.0 to 3.6V allowing the SE2432L to be used in battery powered applications over a wide spectrum of the battery discharge curve.



## SE2432L: 2.4 GHz Smart Energy/ZigBee Front End Module

## **Functional Block Diagram**

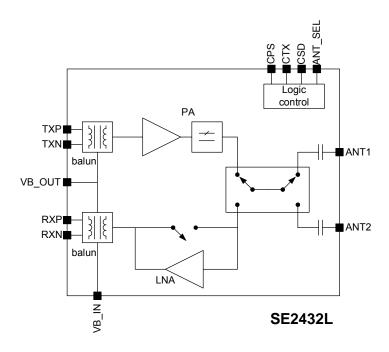


Figure 1: Functional Block Diagram



## **Pin Out Diagram**

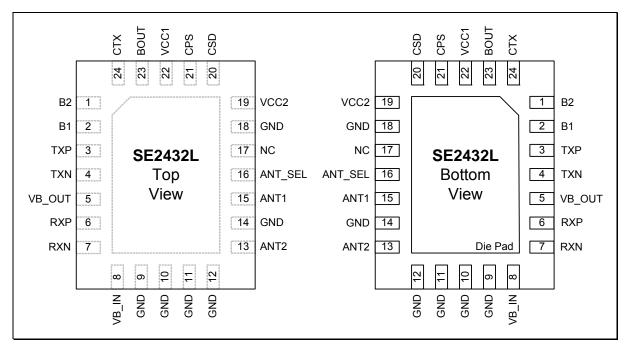


Figure 2: SE2432L Pinout

## **Pin Out Description**

Pin No.	Name	Description
1	B2	Connect through bias resistor to Pin 23
2	B1	Connect through bias resistor to Pin 23
3	TXP	Transmit input signal from transceiver, 200 $\Omega$ differential
4	TXN	Transmit input signal from transceiver, 200 $\Omega$ differential
5	VB_OUT	Tx balun bias core supply from transceiver
6	RXP	Receiver output signal to transceiver, 200 $\Omega$ differential
7	RXN	Receiver output signal to transceiver, 200 $\Omega$ differential
8	VB_IN	Rx balun bias core supply from transceiver
9	GND	Connect to PCB ground
10	GND	Connect to PCB ground
11	GND	Connect to PCB ground
12	GND	Connect to PCB ground
13	ANT2	Connect to 50 Ω antenna
14	GND	Connect to PCB ground



Pin No.	Name	Description
15	ANT1	Connect to 50 Ω antenna
16	ANT_SEL	Connect to GPIO signal to control antenna switch (see "Logic controls" table)
17	NC	Not connected internally to the device
18	GND	Connect to PCB ground
19	VCC2	Connect to positive supply
20	CSD	Connect to GPIO signal to control SE2432L modes (see "Logic controls" table)
21	CPS	Connect to GPIO signal to control SE2432L modes (see "Logic controls" table)
22	VCC1	Connect to positive supply
23	BOUT	Star connect to Bias1 and Bias2 resistors
24	CTX	Connect to GPIO signal to control SE2432L modes (see "Logic controls" table)
Paddle	GND	Exposed die paddle; electrical and thermal ground; Connect to PCB ground

## **Absolute Maximum Ratings**

These are stress ratings only. Exposure to stresses beyond these maximum ratings may cause permanent damage to, or affect the reliability of the device. Avoid operating the device outside the recommended operating conditions defined below. This device is ESD sensitive. Handling and assembly of this device should be at ESD protected workstations.

Symbol	Definition	Min.	Max.	Unit
VCC1	Supply Voltage	-0.3	3.6	V
VCC2	Supply Voltage	-0.3	3.8	V
	Control pin voltages		3.6	V
T <sub>OP</sub>	Operating temperature	-40	85	°C
T <sub>STORAGE</sub>	Storage temperature	-40	125	°C
	ESD Voltage ALL pins (HBM)	-	1000	V
Pout_Tx_max	Pout_Tx_max		24	dBm
Pin_Tx_max	Pin_Tx_max  Tx input power at TR port		+6	dBm
Pin_Rx_max	Rx input power at ANT1 or ANT2 ports	-	+5	dBm



## SE2432L: 2.4 GHz Smart Energy/ZigBee Front End Module

## **Recommended Operating Conditions**

Symbol	Parameter	Min.	Тур.	Max.	Unit
TA	Ambient temperature	-40	25	85	°C
VB_IN VB_OUT	Supply voltage for baluns bias	1.6	-	3.6	V
VCC1	Supply voltage on VCC pins	2.0	3.0	3.6	V
VCC2	Supply voltage on VCC pins	2.0	3.0	3.6	V

## **DC Electrical Characteristics**

Conditions: VCC1 = VCC2 = 3.0 V, T<sub>A</sub> = 25 °C, as measured on Skyworks Solutions' SE2432L-EK1 evaluation board (de-embedded to device), unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Icc-тx20	Total Supply Current	Tx mode P <sub>OUT</sub> = 20 dBm CPS = CSD = CTX = 3.0 V	-	110	-	mA
Icc-тx17	Total Supply Current	Tx mode P <sub>OUT</sub> = 17 dBm CPS = CSD = CTX = 3.0 V	-	90	-	mA
ICC-Tx10	Total Supply Current	Tx mode $P_{OUT}$ = 10 dBm CPS = CSD = CTX = 3.0 V	-	45	-	mA
Іса-тх	Quiescent Current	No RF CPS = CSD = CTX = 3.0 V	-	30	ı	mA
Icc-rx	Total Supply Current	Rx mode CPS = CSD = 3.0 V, CTX = 0 V	-	5	7	mA
ICC- RxBypass	Total Supply Current	Rx bypass mode CSD = 3.0 V, CPS = CTX = 0 V	-		300	uA
Icc_off	Sleep Supply Current	No RF, CTX=CPS=CSD = 0 V	-	0.05	1	μΑ

## **Logic Characteristics**

Conditions: VCC1 = VCC2 = 3.0 V, T<sub>A</sub> = 25 °C, as measured on Skyworks Solutions' SE2432L-EK1 evaluation board (de-embedded to device), unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>IH</sub>	Logic input high		1.6	-	3.6	V
V <sub>IL</sub>	Logic input low		0	-	0.3	V
I <sub>IH</sub>	Logic input high		-	-	1	μΑ
I <sub>IL</sub>	Logic input low		-	-	1	μΑ



## SE2432L: 2.4 GHz Smart Energy/ZigBee Front End Module

## **Logic Controls**

Conditions: VCC1 = VCC2 = 3.0 V, TA = 25 °C

Mode	Mode description	Note	CPS	CSD	СТХ
0	All off (sleep mode)	1, 3	0	0	0
1	Rx bypass mode	1, 2	0	1	0
2	Rx LNA mode	1, 2	1	1	0
4	Tx mode	1, 2	Х	1	1

**Note:** (1) Logic '0' level compliant to  $V_{IL}$  as specified in the "Logic Characteristics" table

(2) Logic '1' level compliant to  $V_{\rm IH}$  as specified in the "Logic Characteristics" table

(3) All controls must be at logic '0' in order to achieve the specified sleep current

Conditions: VCC1 = VCC2 = 3.0 V, TA = 25 °C

Mode description	Note	CPS	CSD	СТХ	ANT_SEL
ANT1 port enabled	1	Х	Х	Х	0
ANT2 port enabled	2	X	X	X	1

**Note:** (1) Logic '0' level compliant to  $V_{IL}$  as specified in the "Logic Characteristics" table

(2) Logic '1' level compliant to  $V_{IH}$  as specified in the "Logic Characteristics" table

#### **AC Electrical Characteristics, Transmit**

Conditions: VB\_IN = VB\_OUT = 1.6 to 3.6 V, VCC1 = VCC2 = 3.0 V,  $T_A$  = 25 °C, as measured on Skyworks Solutions' SE2432L-EK1 evaluation board (de-embedded to device), all unused ports terminated with 50  $\Omega$ , unless otherwise noted.

Symbol	Parameter	Condition	Note	Min.	Тур.	Max.	Unit
Fin	Frequency Range			2400	-	2483	MHz
Pout_Hi	Output power at ANT1 or ANT2 ports	VCC1 = VCC2 = 3.6 V VCC1 = VCC2 = 3.3 V VCC1 = VCC2 = 3.0 V VCC1 = VCC2 = 2.7 V VCC1 = VCC2 = 2.0 V	1, 6	-	24.0 22.5 21.0 20.5 17.0	-	dBm
S <sub>21_Hi</sub>	Small Signal Gain high power mode	2400 – 2483 MHz		20	22	24	dB
Δ\$21	Small Signal Gain Variation	Gain variation across all ZigBee channels	1	-	-	1	dBp-p
HD2, HD3	2 <sup>nd</sup> , 3 <sup>rd</sup> Harmonics	Роит <b>= 20 dBm</b>	1, 2	-	-	-42	dBm/MHz
S22	Output return loss	At ANT1 or ANT2 ports	1	-	-10	-5	dB



## SE2432L: 2.4 GHz Smart Energy/ZigBee Front End Module

Symbol	Parameter	Condition	Note	Min.	Тур.	Max.	Unit
Z <sub>IN</sub>	Transmit port impedance	Measured differentially between TXP and TXN		-	200	-	Ω
ACP	Spectral Mask		1, 3	-	-	-30	dBm
Trise	Turn on time		4	-	-	800	ns
Tfall	Turn off time		5	-	-	800	ns
STAB	Stability	CW, P <sub>IN</sub> = 0 dBm 0.1 GHz – 20 GHz Load VSWR = 6:1		All non-harmonically related outputs less than -42 dBm/MHz			
RU	Ruggedness	CW, PIN = +6 dBm, Load VSWR = 10:1		No permanent damage			

Note: 2400 – 2483 MHz (1)

(2) IEEE 802.15.4 source

(3)

Integrated power from band edges to Fc ± 3.5 MHz From 50% of CTX edge to 90% of final RF output power From 50% of CTX edge to 10% of final RF output power (4) (5)

OEVM = 1% typ



## SE2432L: 2.4 GHz Smart Energy/ZigBee Front End Module

## **AC Electrical Characteristics, Receive**

Conditions: VB\_IN = VB\_OUT = 1.6 to 3.6 V, VCC1 = VCC2 = 3.0 V,  $T_A$  = 25 °C, as measured on Skyworks Solutions' SE2432L-EK1 evaluation board (de-embedded to device), all unused ports terminated with 50  $\Omega$ , unless otherwise noted.

Symbol	Parameter	Condition	Note	Min.	Тур.	Max.	Unit
Fin	Frequency Range			2400	-	2483	MHz
Rx_gain	Receive gain	CPS = CSD = logic '1', CTX = logic '0'	1	10	11.5	13	dB
NF	Receive noise figure	CPS = CSD = logic '1', CTX = logic '0'	1	-	2	2.5	dB
IIP3	Input 3 <sup>rd</sup> order intercept	CPS = CSD = logic '1', CTX = logic '0'	1	-3	2	1	dBm
IP1dB	Input 1-dB compression point	CPS = CSD = logic '1', CTX = logic '0'	1	-13	-8	-	dBm
S <sub>11</sub>	Input return loss	At ANT1 or ANT2 ports	1	-	-10	-5	dB
Z <sub>OUT</sub>	Receive port impedance	Measured differentially between RXP and RXN		-	200	-	Ω
Trise	Turn on time		2	-	-	800	ns
Tfall	Turn off time		3	-	-	800	ns
G_bp	Gain in bypass mode	CPS = CTX = logic '0', CSD = logic '1'			-3	-	dB
IP1dB	Input 1-dB compression point in bypass mode	CPS = CTX = logic '0', CSD = logic '1'		10	-	-	dBm

**Note:** (1) 2400 – 2483 MHz

(2) From 50% of CTX edge to 90% of final RF output power

(3) From 50% of CTX edge to 10% of final RF output power

## **AC Electrical Characteristics, Dual Antenna Switch**

Conditions: VCC1 = VCC2 = 3.0 V, T<sub>A</sub> =  $25 ^{\circ}\text{C}$ , as measured on Skyworks Solutions' SE2432L-EK1 evaluation board (de-embedded to device), all unused ports terminated with  $50 ^{\circ}\Omega$ , unless otherwise noted.

Symbol	Parameter	Min.	Тур.	Max.	Unit
ISOL <sub>ANTSW</sub>	Isolation Between ANT1 and ANT2 Ports	-	-20	-	dB
T <sub>ANT1-ANT2</sub>	Antenna 1 to Antenna 2 switching time	-	400	- nsec	



## **Package Information**

This package is Pb free, RoHS compliant and halogen free. The product is also rated MSL1.

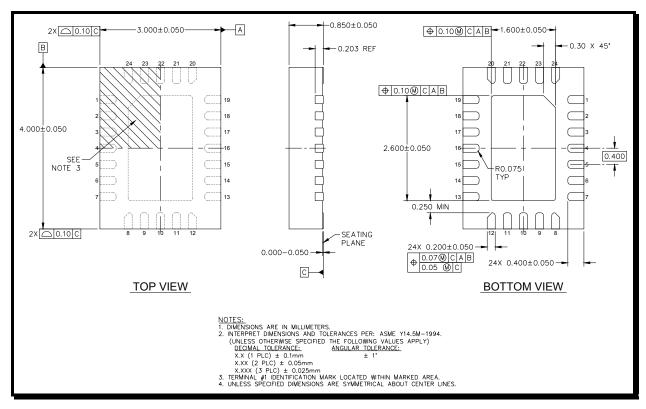


Figure 3: SE2432L Package Diagram



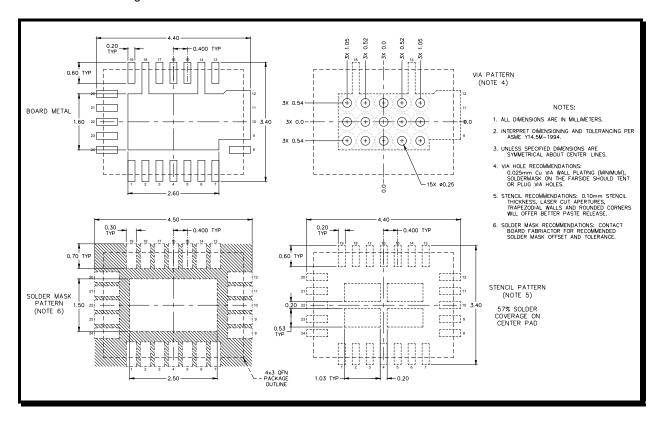
## SE2432L: 2.4 GHz Smart Energy/ZigBee Front End Module

## **Recommended PCB Footprint**

The PCB footprint below is only for reference.

The user should modify the design layout in order to meet their specific solder fillet requirements & solder joint reliability requirements.

All dimensions in the figure below are in mm.





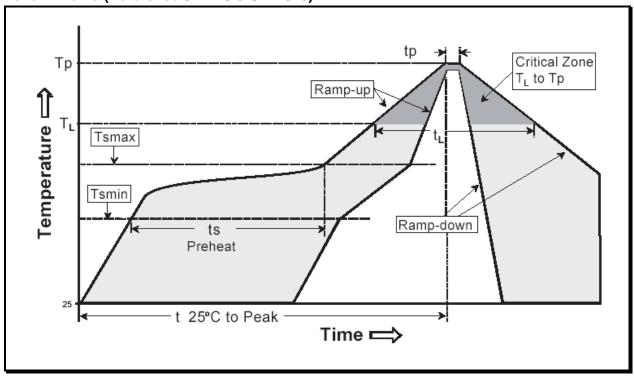
## SE2432L: 2.4 GHz Smart Energy/ZigBee Front End Module

## **Recommended Reflow Temperature Profile**

Profile Feature	SnPb Eutectic Assembly	Lead (Pb) Free Assembly				
Average Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/s (max)	3°C/s (max)				
Preheat						
Temperature Min. (T <sub>smin</sub> )	100°C	150°C				
Temperature Max. (T <sub>smax</sub> )	150°C	200°C				
Time (Min. to Max) (t <sub>s</sub> )	60 - 120s	60 - 80s				
Ramp Up						
Tsmax to t∟	-	3°C/s (max)				
Time 25°C to Peak Temperature	6 mins. (max)	8 mins. (max)				
Reflow						
Temperature (t <sub>L</sub> )	183°C	217°C				
Time maintained above t <sub>L</sub>	60 - 150s	60 - 150s				
Peak Temperature (t <sub>p</sub> )	240 ±5°C	260 +0/-5°C				
Time Within 5°C of Actual Peak Temperature (t <sub>p</sub> )	10 - 30s	20 - 40s				
Ramp-Down						
Ramp-Down Rate	6°C/s (max)	6°C/s (max)				



## **Reflow Profile (Reference JEDEC J-STD-020)**





## SE2432L: 2.4 GHz Smart Energy/ZigBee Front End Module

## **Branding Information**

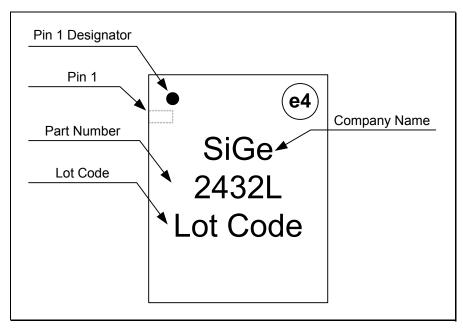


Figure 4: SE2432L Branding

## **Tape and Reel Information**

Parameter	Value			
Devices Per Reel	3000			
Reel Diameter	13 inches			
Tape Width	12 millimeters			
pin 1 corner				
	00000			
Product Code Ltd. Number Ltd. Number Ltd. Number Ltd. Number Ltd. Number Ltd. Number	Product Code Let Number  Product Code Let Number Let Number			

Figure 5: SE2432L-R Tape and Reel Information



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