Lab 3

READ INSTRUCTIONS CAREFULLY BEFORE YOU START THE LAB.

This lab is due on Sunday, March 8, 2020.

Lab must be submitted electronically to iLearn on https://ilearn.csumb.edu by 11:55 p.m. on the due date. Late lab assignments will not be accepted.

Lab submission must contain all files zipped/compressed into a single folder. Any other formats will not be accepted. The naming convention of the file should be Lab3_lastname of each group member. **Put your names in the documents as well.**

This lab is worth 15 points.

Name   :   Antonio Felix,Guillermo Flores, David Gaeta, Erik Gallardo

NandToTetris

You will be using the software tool NandToTetris for initial labs (Lab 1 - 3). In this lab, you will complete some of the gates listed in Project 3 of NandToTetris at:

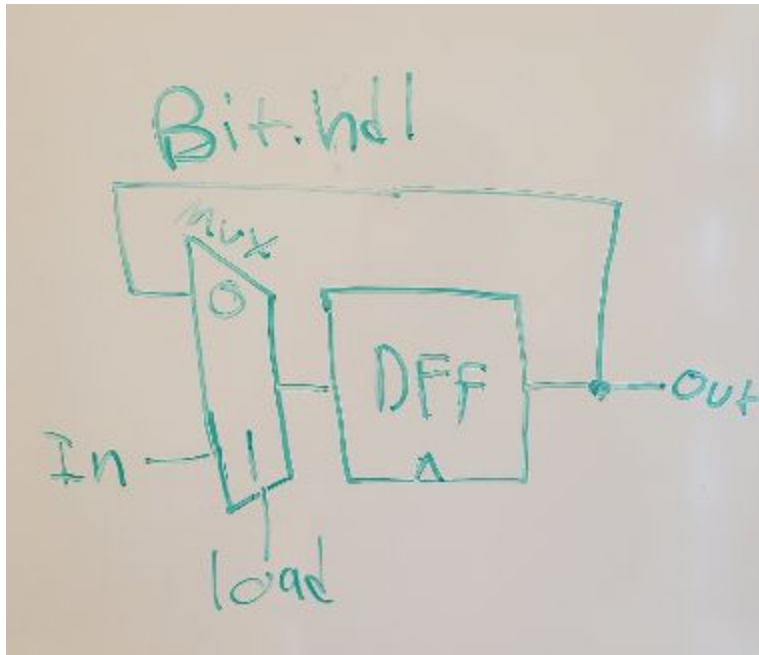https://www.nand2tetris.org/project03

You will submit an HDL file for each gate/block. You must also submit a single pdf with the diagrams of all logic gates/blocks in Project 3 of NandToTetris.

You need not submit test scripts or compare files to iLearn. Your HDL files for the gates/blocks must work with the test scripts and compare files provided in nandtotetris/projects/02 folder.
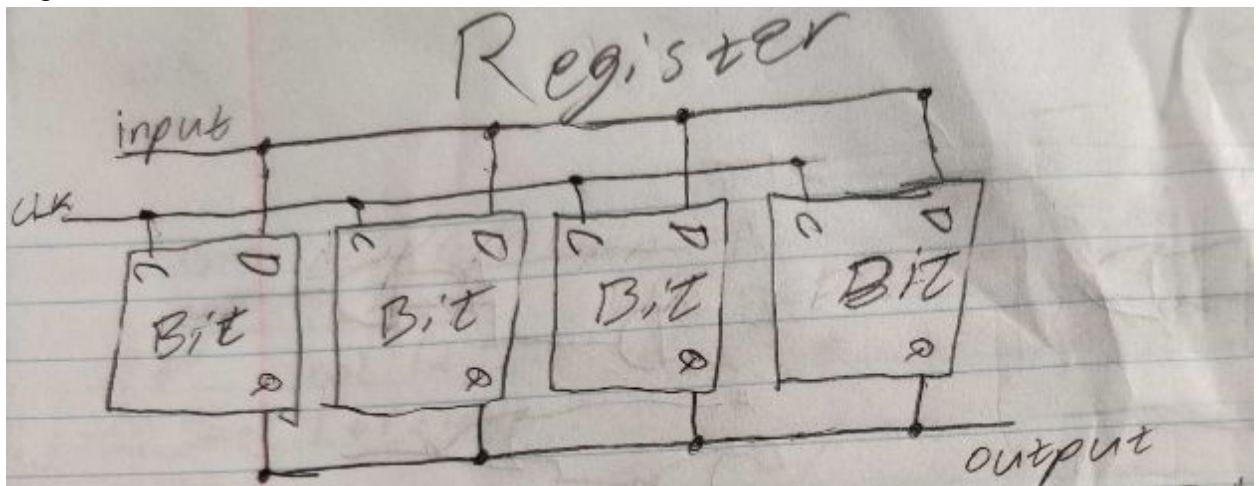
There is no partial credit for hdl files that do not load/compile.

The following chips are required to be completed for this lab:

1. Bit

2. Register

3. RAM8

RAM8

load

in $\xrightarrow{16}$

DMUX 8way

address $\xrightarrow{3}$

All registers

Mux 8 way16 $\xrightarrow{16}$ out