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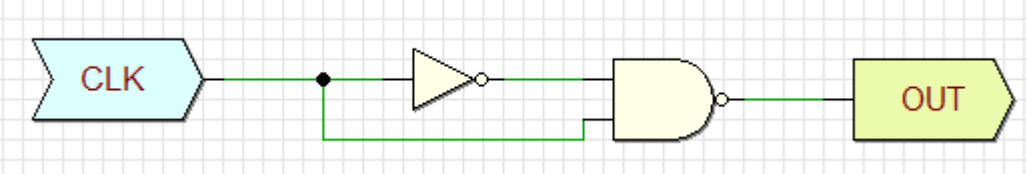
2MM7

Simulacion de circuitos con Deeds -DcS

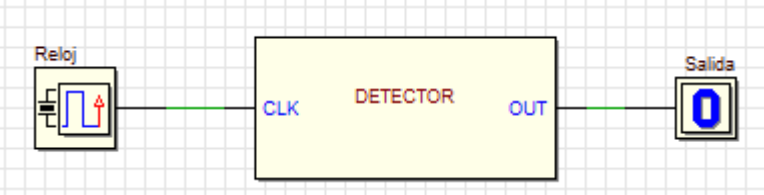
Deeds es un entorno de aprendizaje para la electrónica que incluye tres herramientas de diseño.

- Deeds-DcS (Digital Circuit Simulator)
- Deeds-FsM (Finite State Machine Simulator)
- Deeds-McE (Micro Computer Emulator)

Crear un Bloque



Bloque



Implementación del bloque

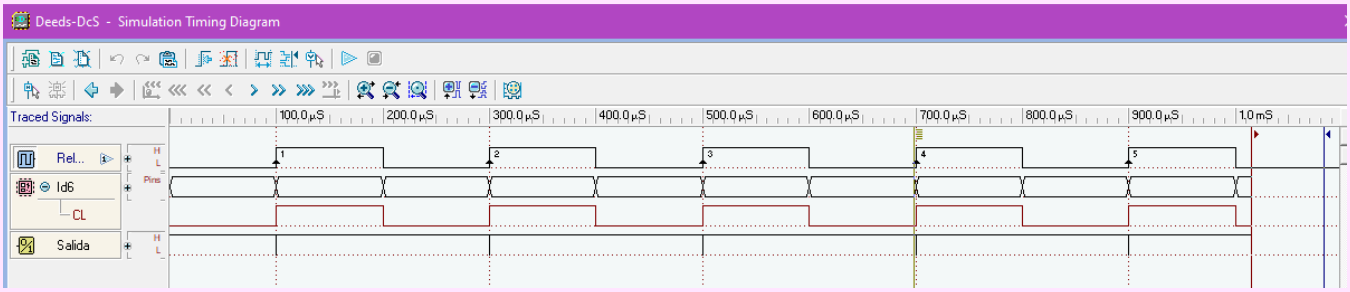
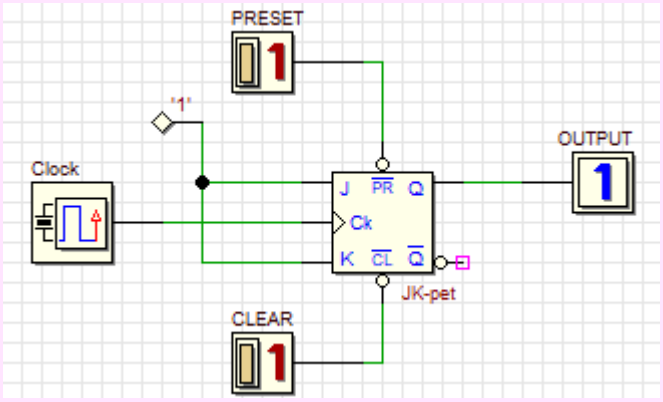
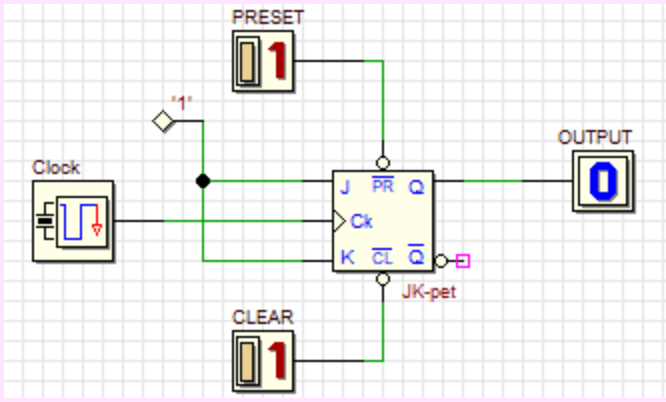


Diagrama de tiempo

Flip-Flop JK con reloj





Flip Flop JK implementado

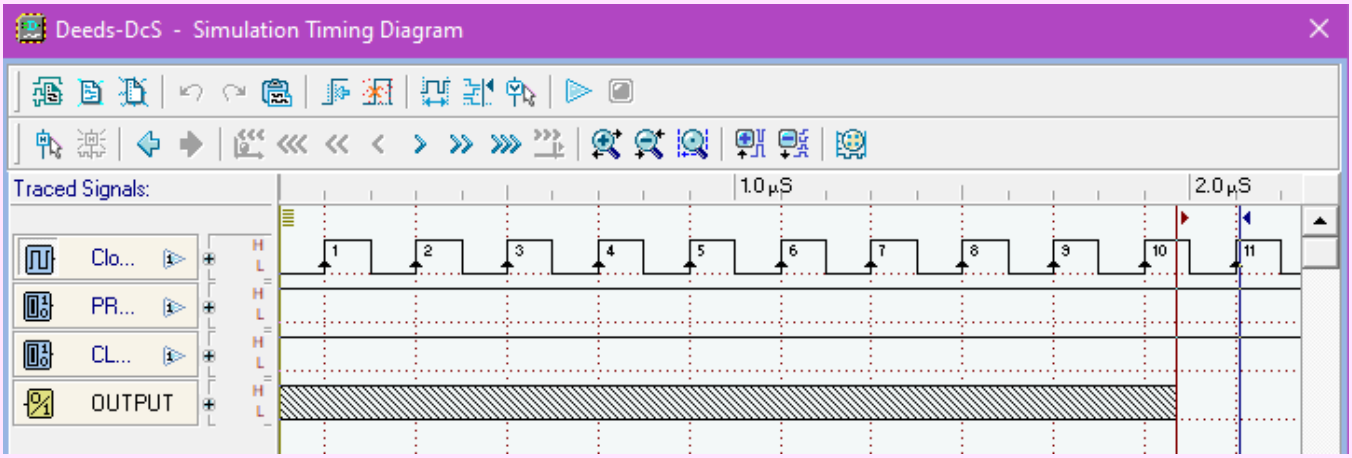
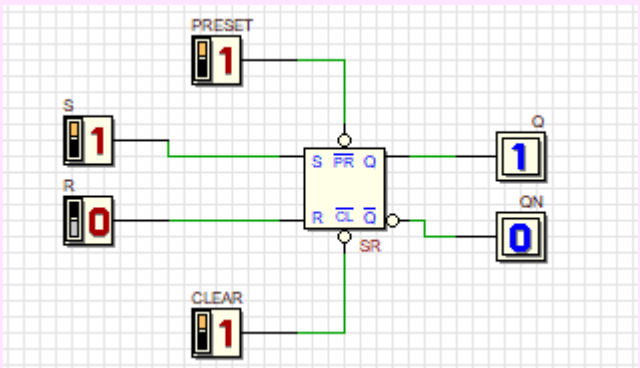
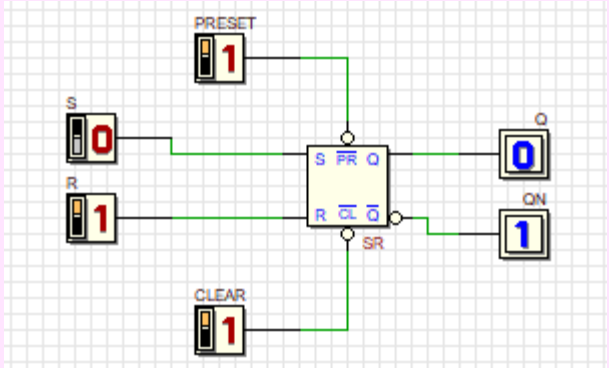
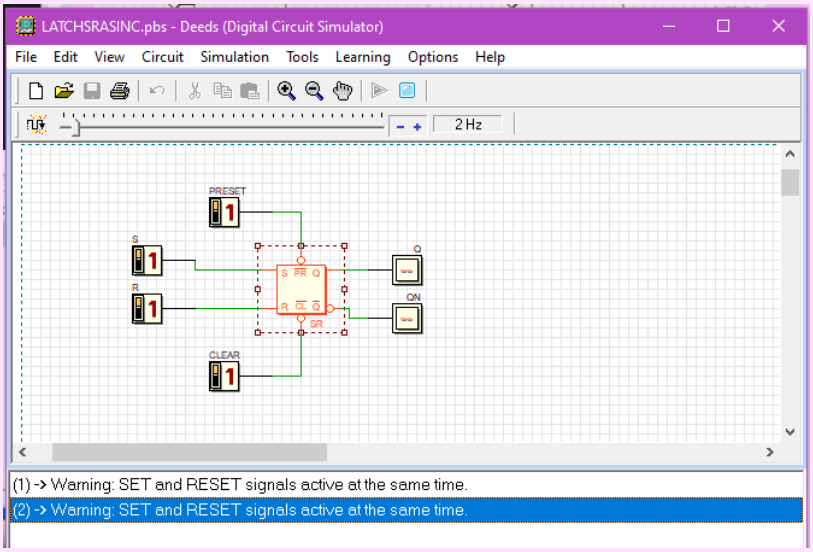


Diagrama de tiempo

LATCH SR con Señales Ansíncronas

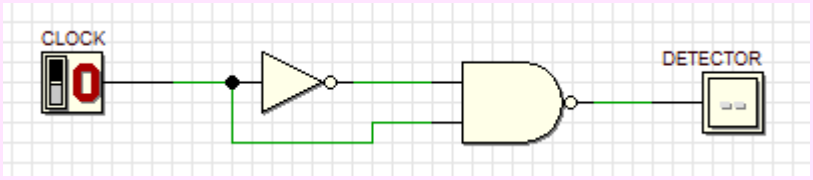






Latch SR implementado

Detector de Flanco



Circuito implementado

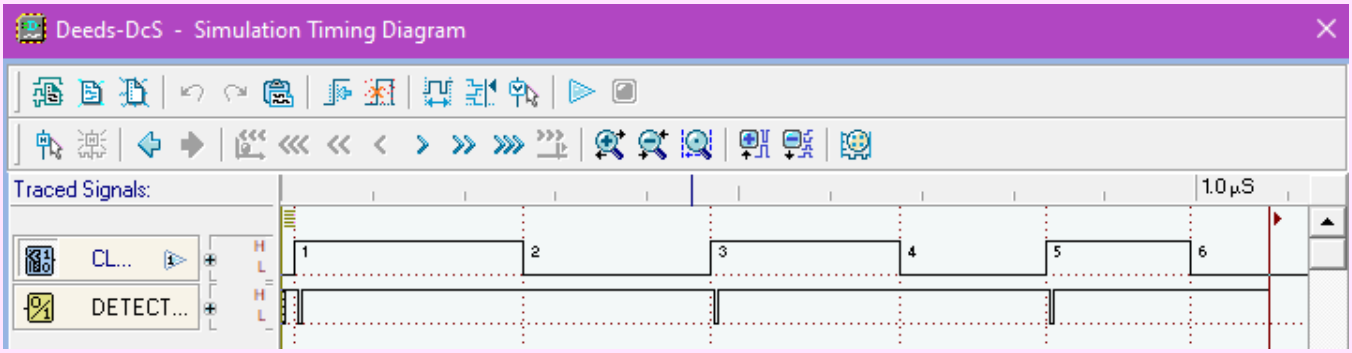
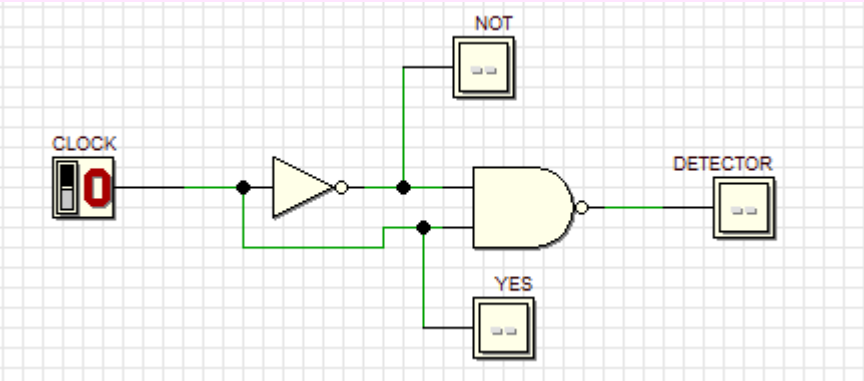


Diagrama de tiempo



Circuito detector con análisis de señales de entrada a NAND

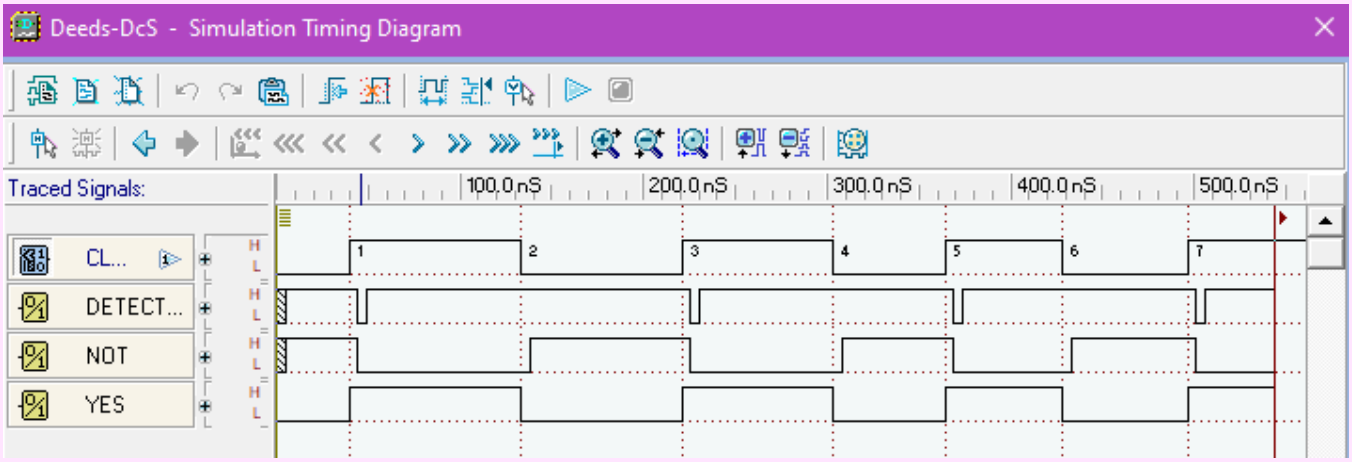
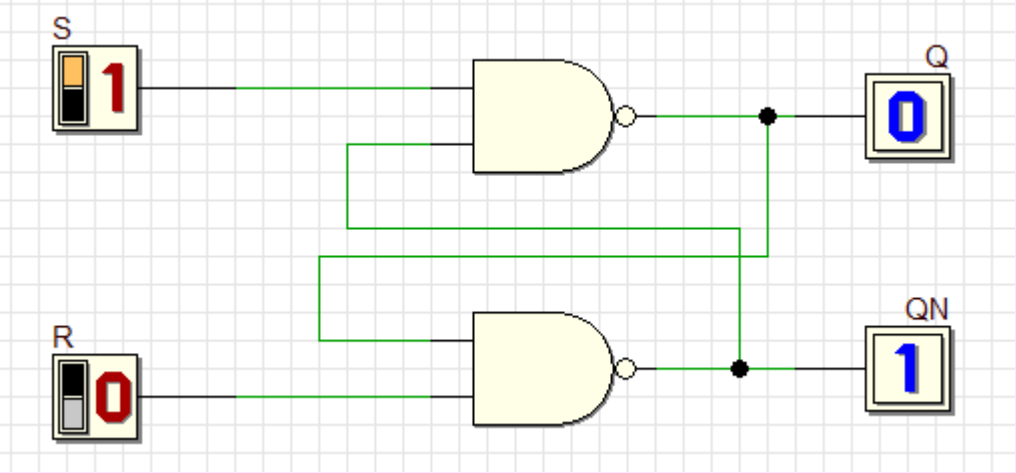


Diagrama de tiempo

LATCH SR Diagrama de tiempo



Circuito Latch SR implementado

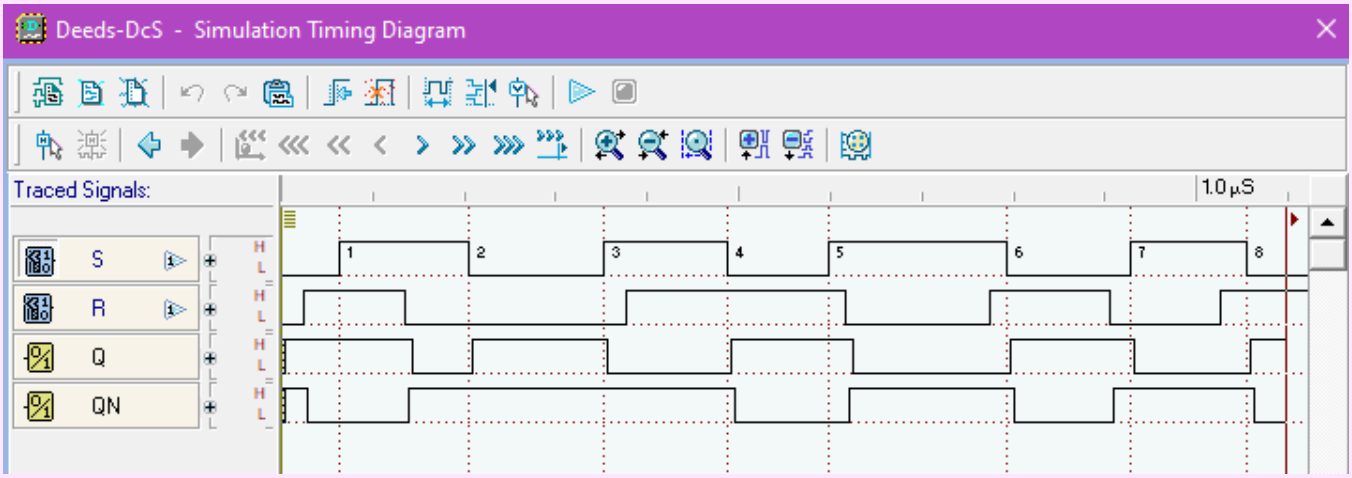
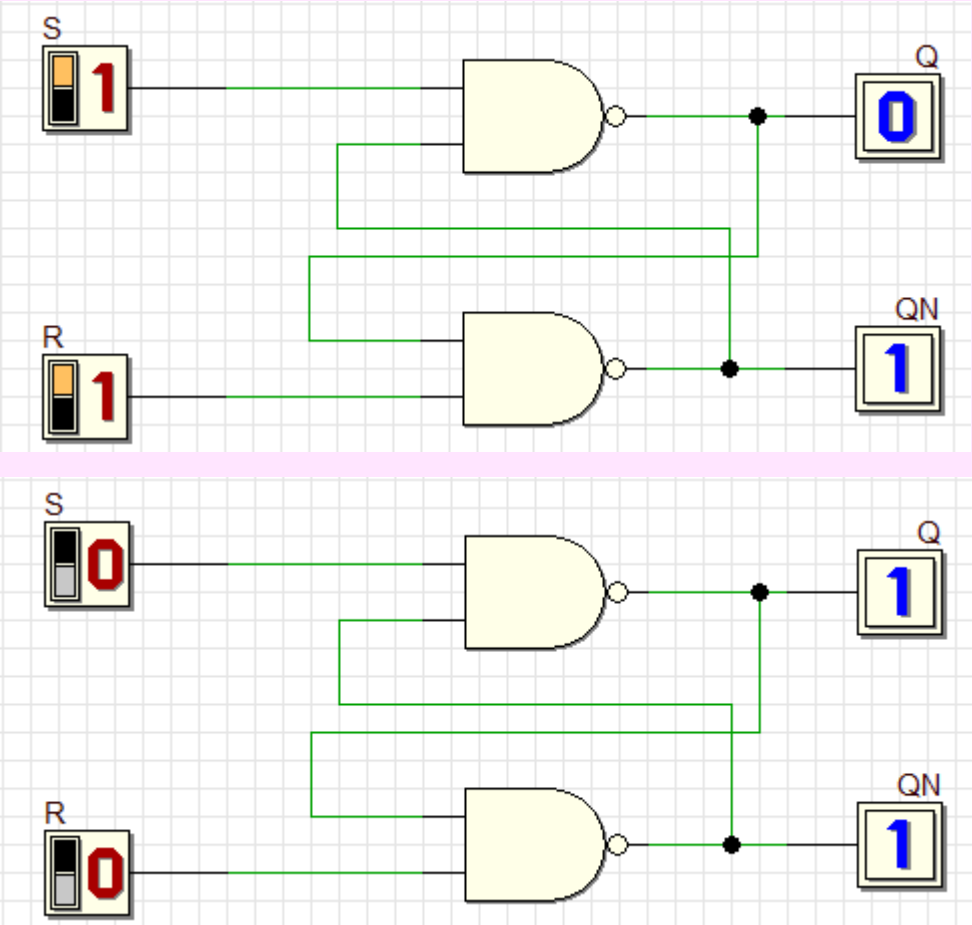
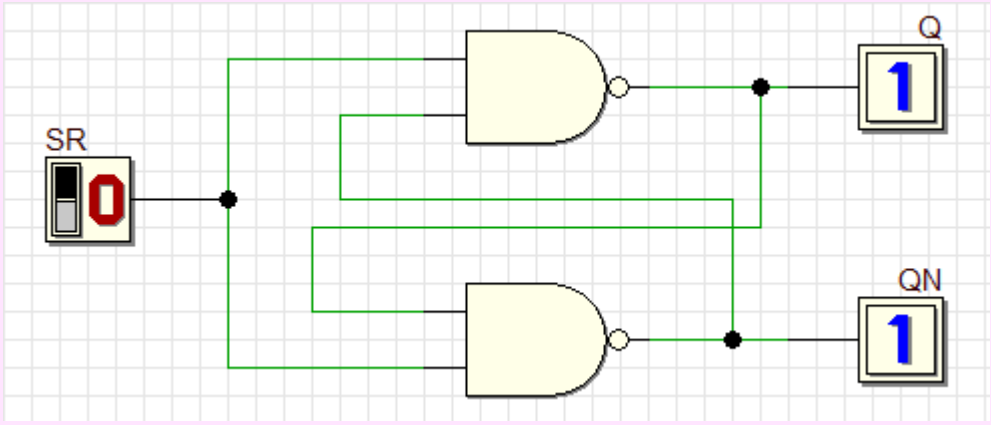


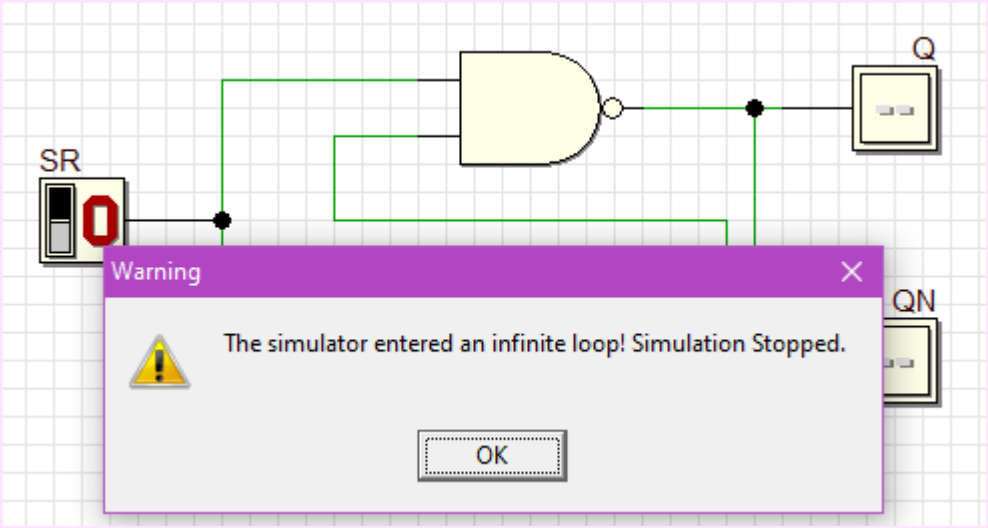
Diagrama de tiempo

LATCH SR



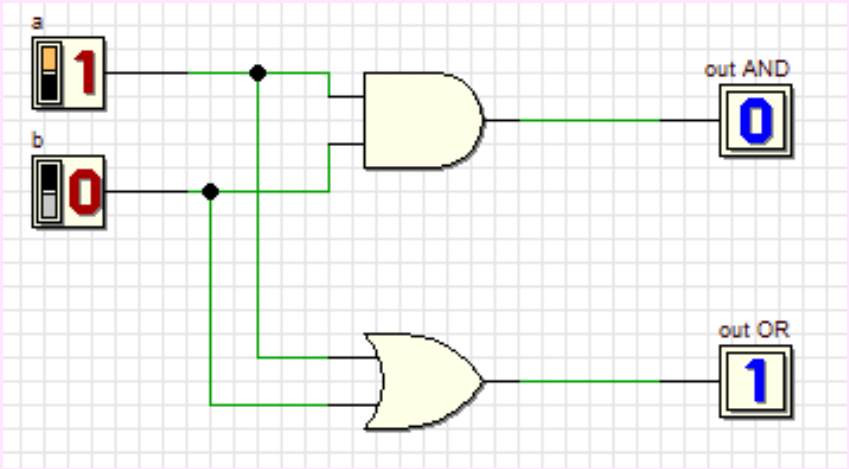
Circuito Latch SR





Circuito Latch no válido

Compuertas Básicas



Circuito que describe la salida de una compuerta AND y OR

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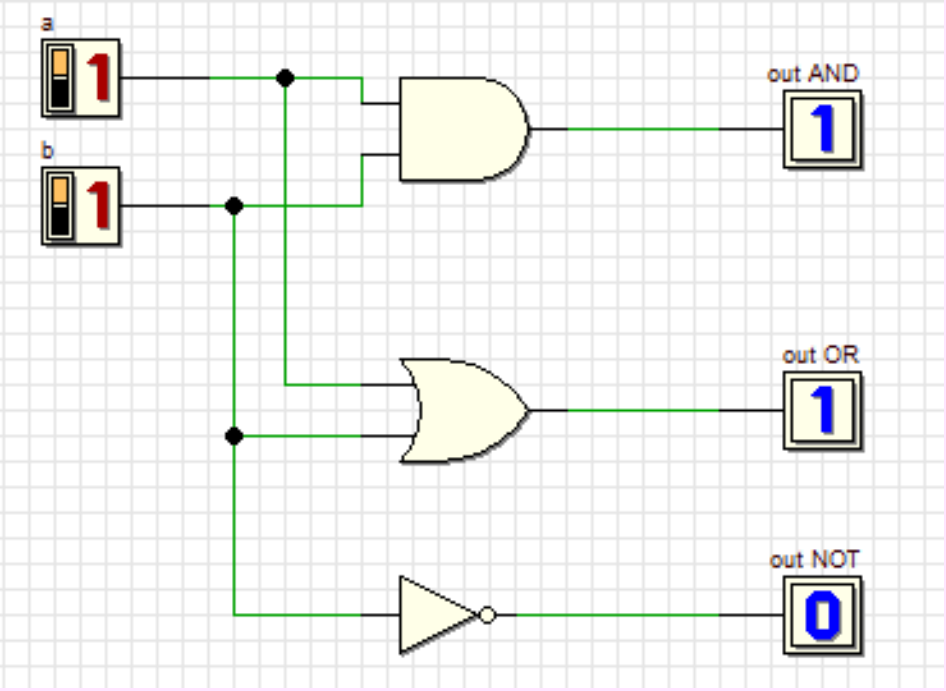

Circuito que describe la salida de una compuerta NOT, AND y OR