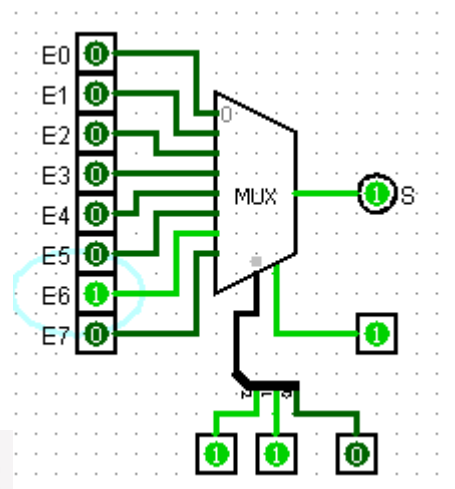
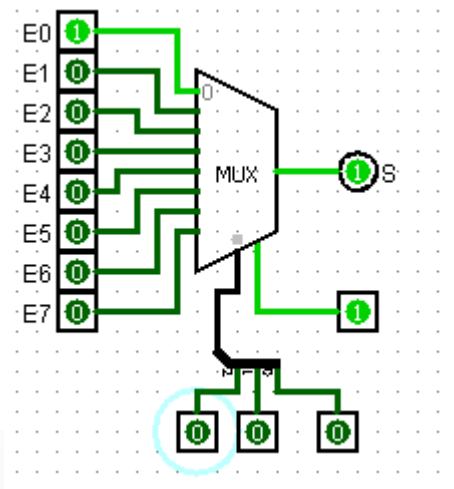
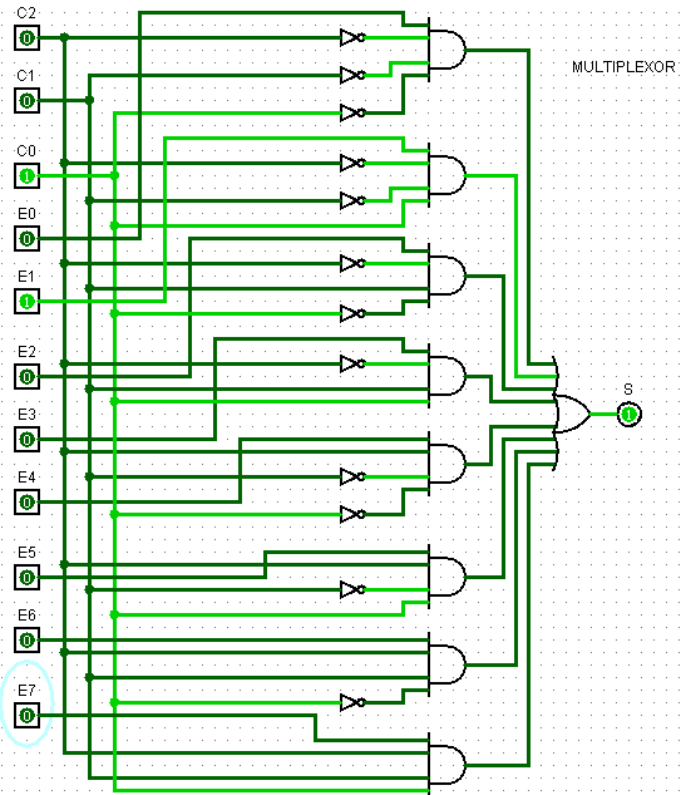
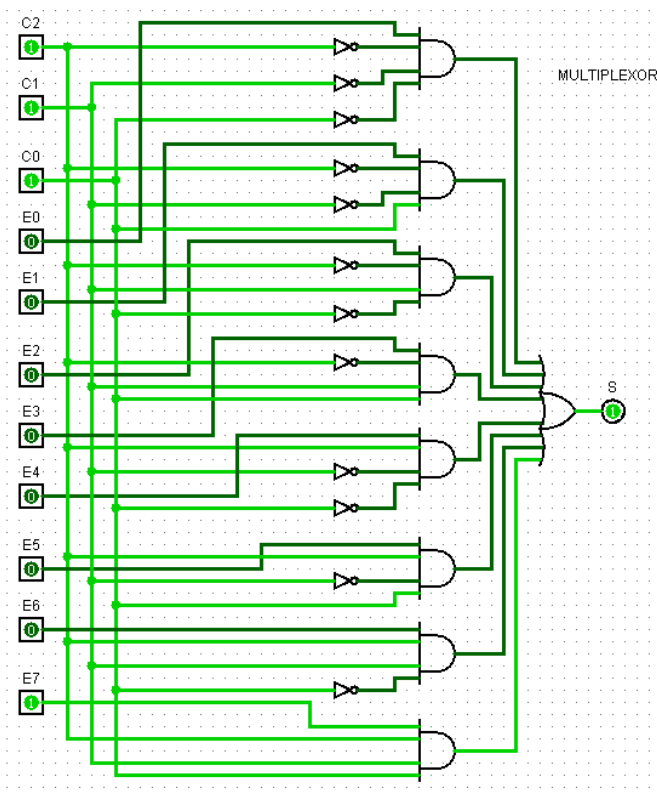


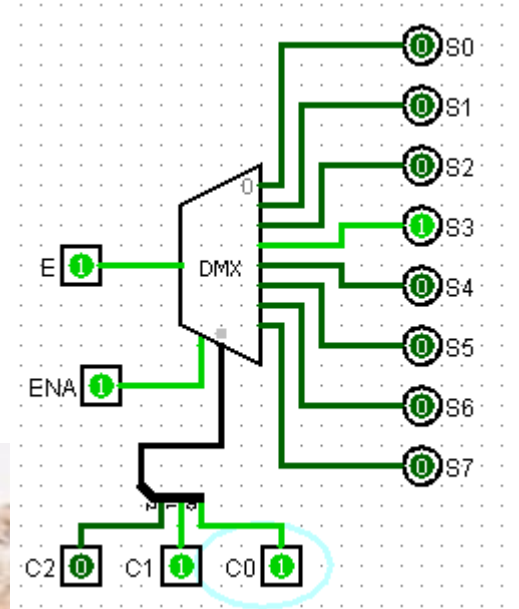
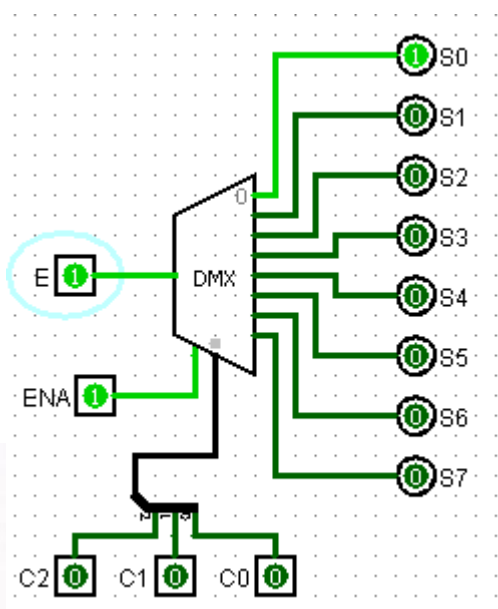
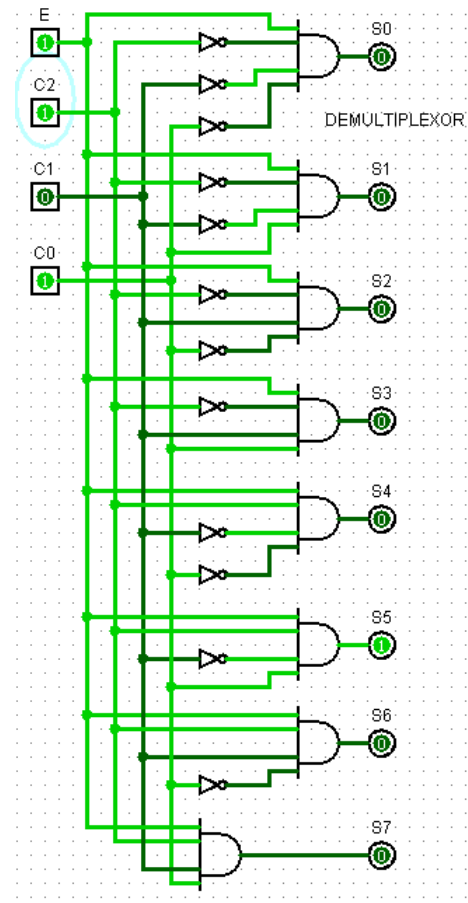
Tarea: Multiplexor y Demultiplexor

MULTIPLEXOR



The diagram shows a 2-to-4 demultiplexer circuit. It has two data inputs, C1 and C0, and one enable input, E. The outputs are S0, S1, S2, and S3. The circuit is labeled "DEMULTIPLEXOR".

Input C1	Input C0	Enable E	Output S0	Output S1	Output S2	Output S3
0	0	1	1	0	0	0
0	1	1	0	1	0	0
1	0	1	0	0	1	0
1	1	1	0	0	0	1



Multiplexor de 4 entradas dobles

