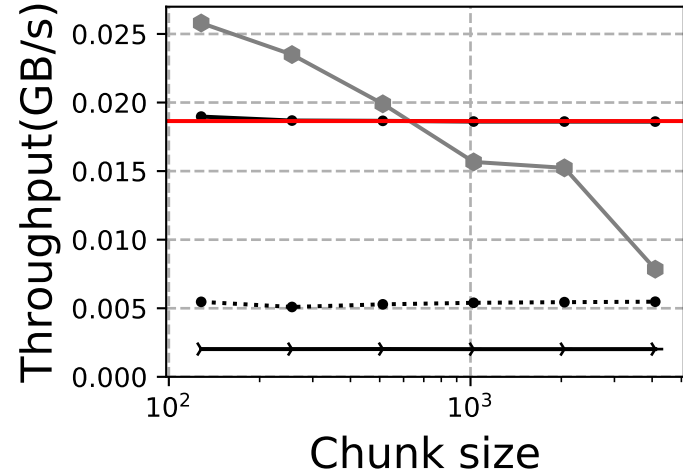
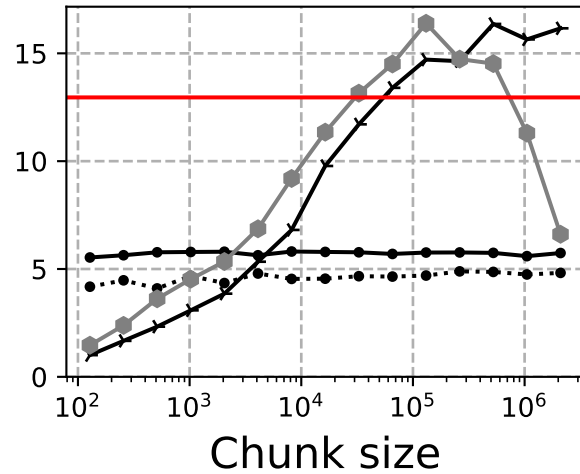


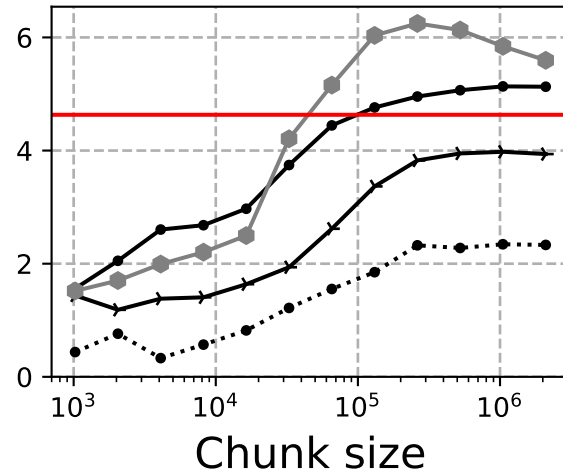
Matrix Multiplication



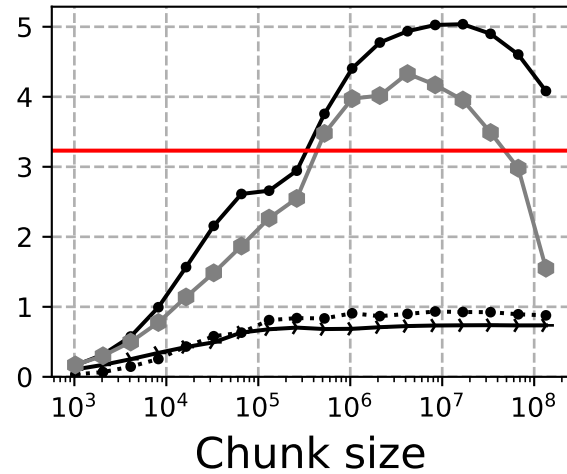
Mersenne Twister



Watermarking



Sobel Filter



→ CPU —●— GPU FPGA —◆— CPU+GPU+FPGA