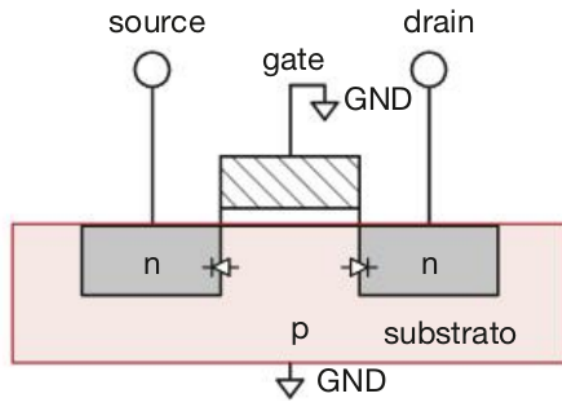
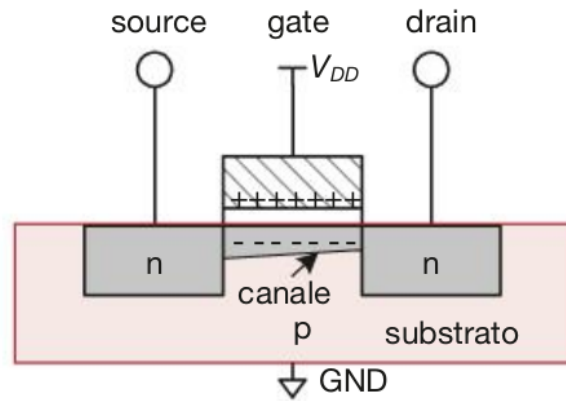


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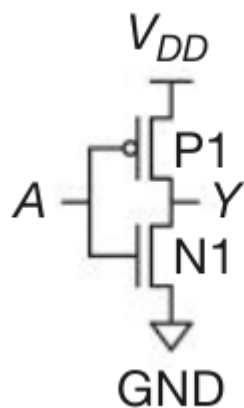
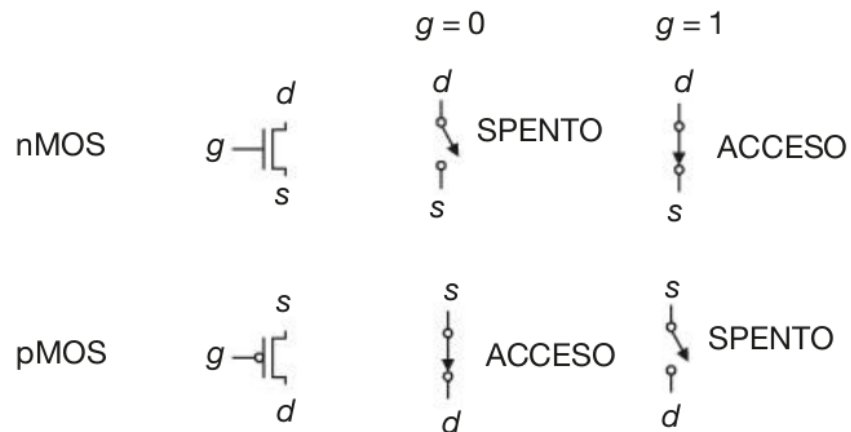
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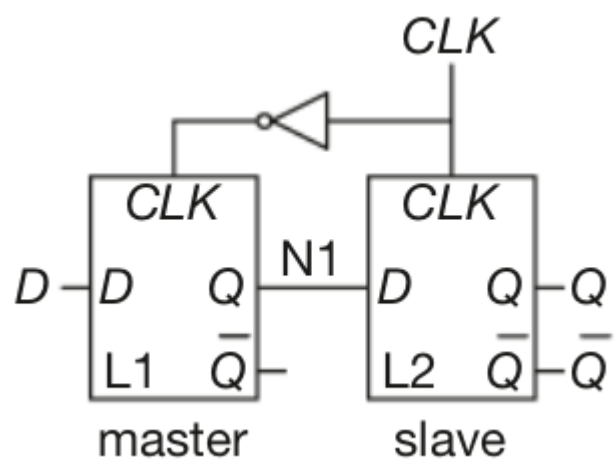
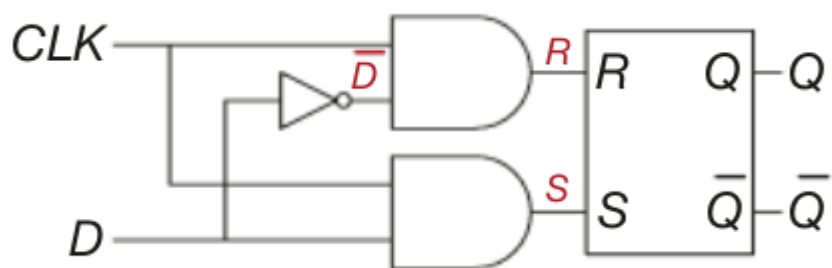
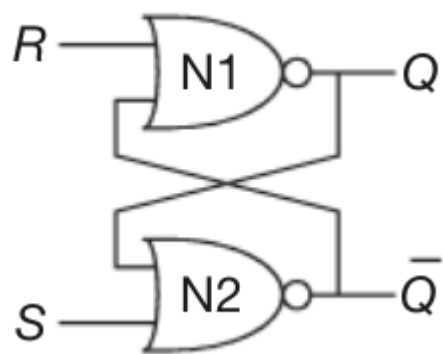
(a)



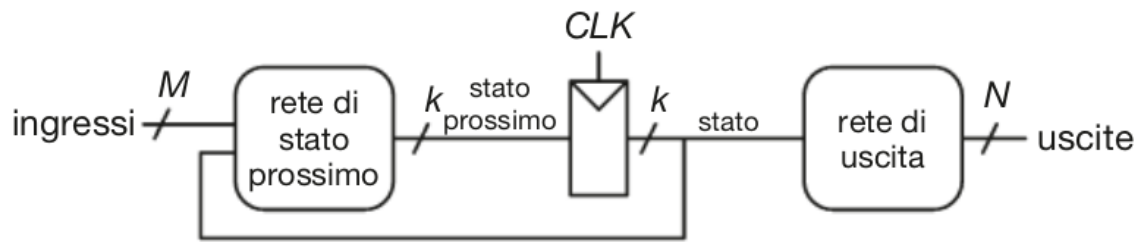
(b)



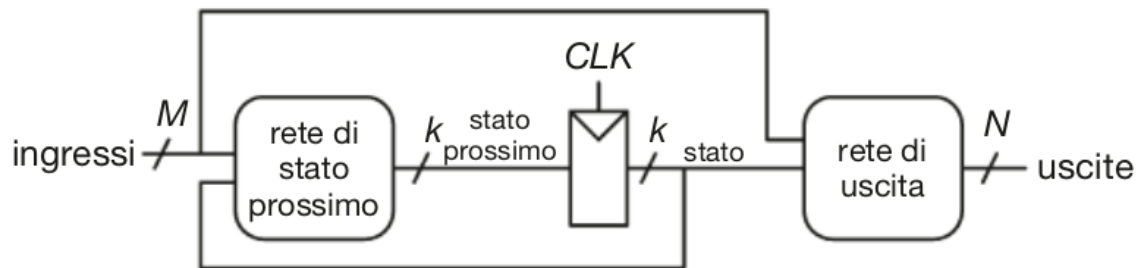
Circuiti Sequenziali



Automi

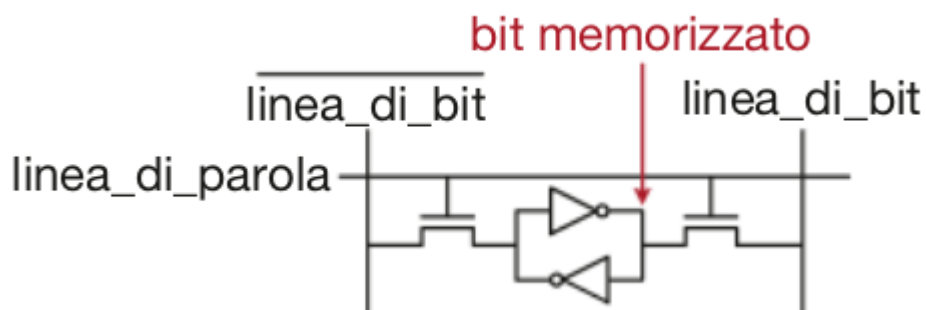
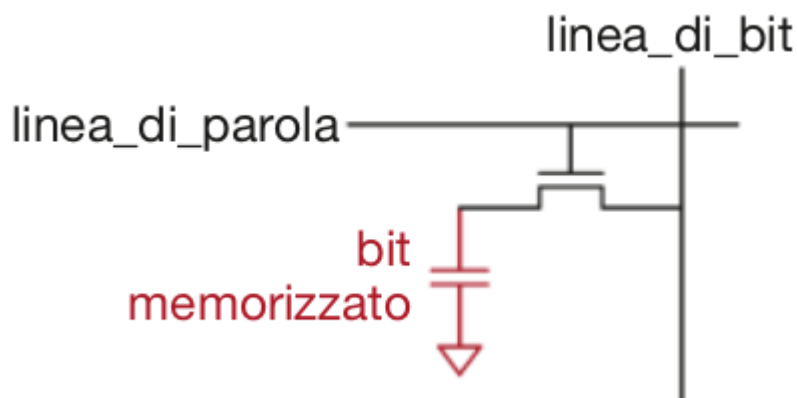


(a)

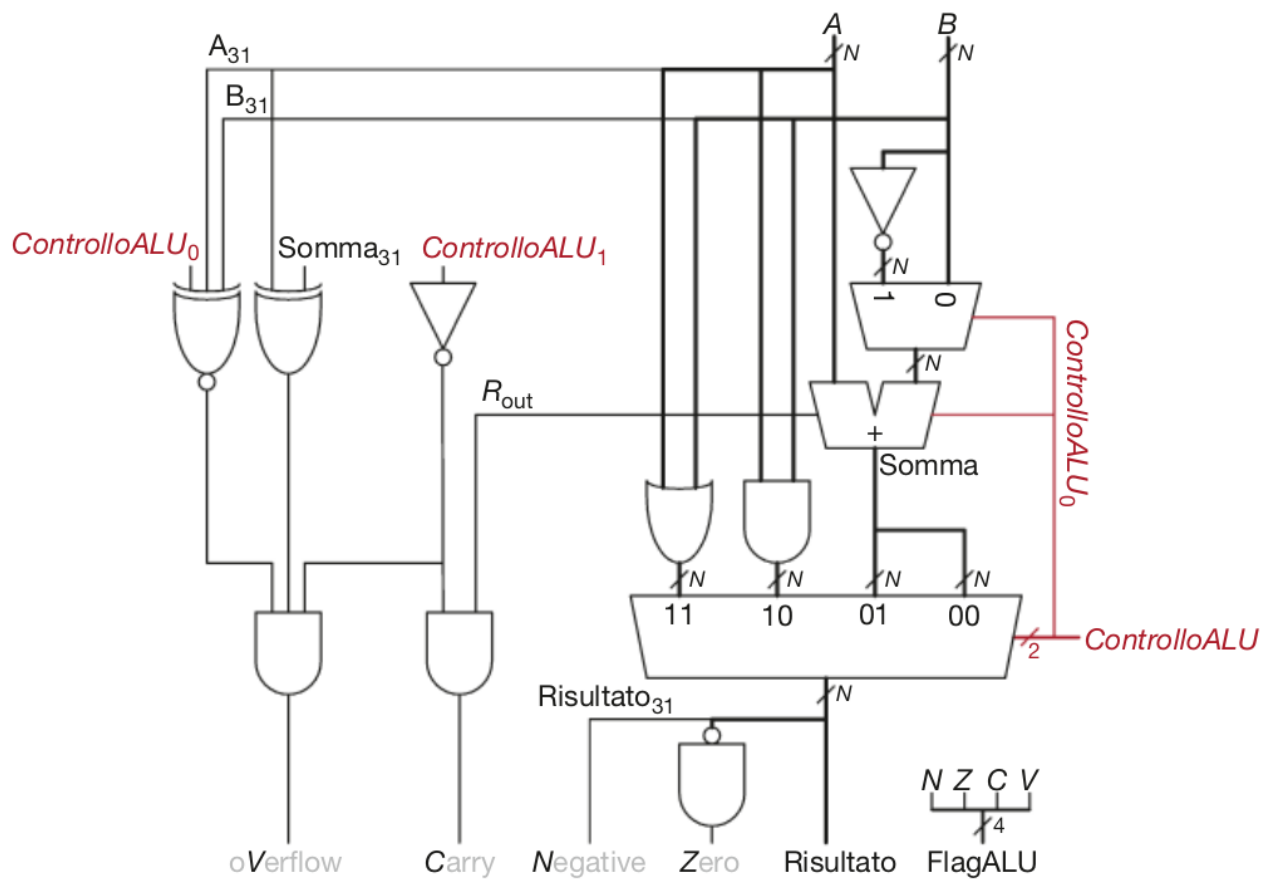


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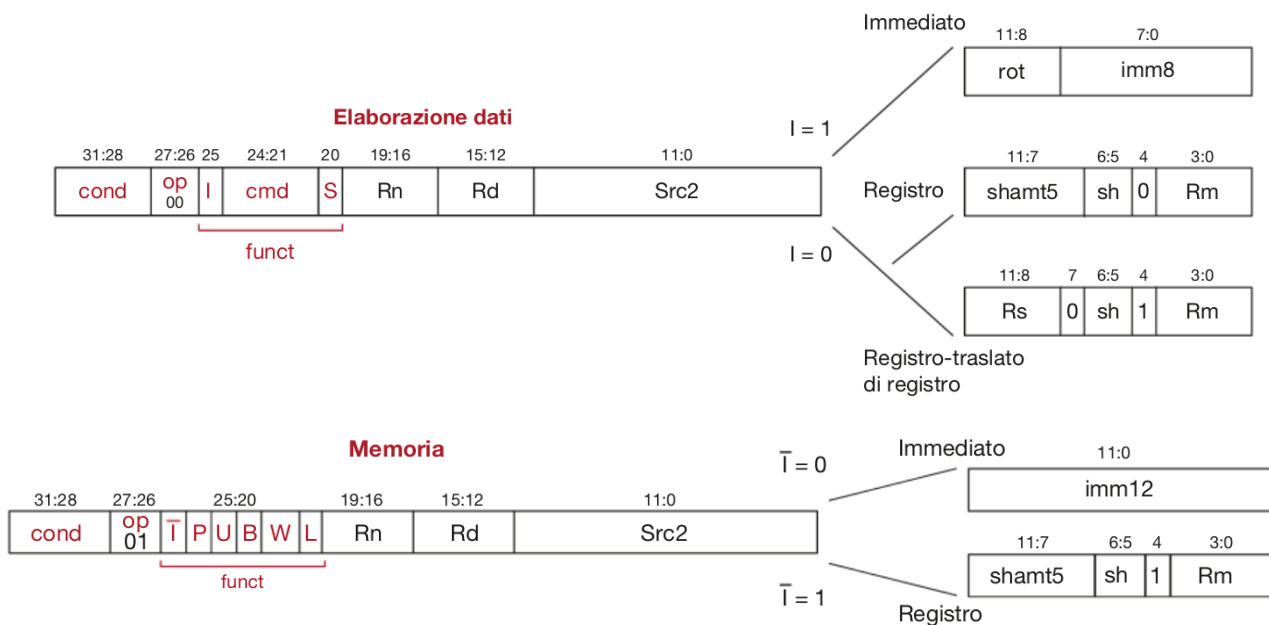
Memoria



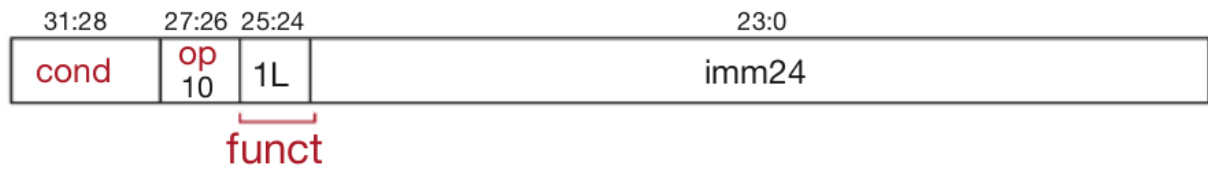
ALU



Formato Istruzioni

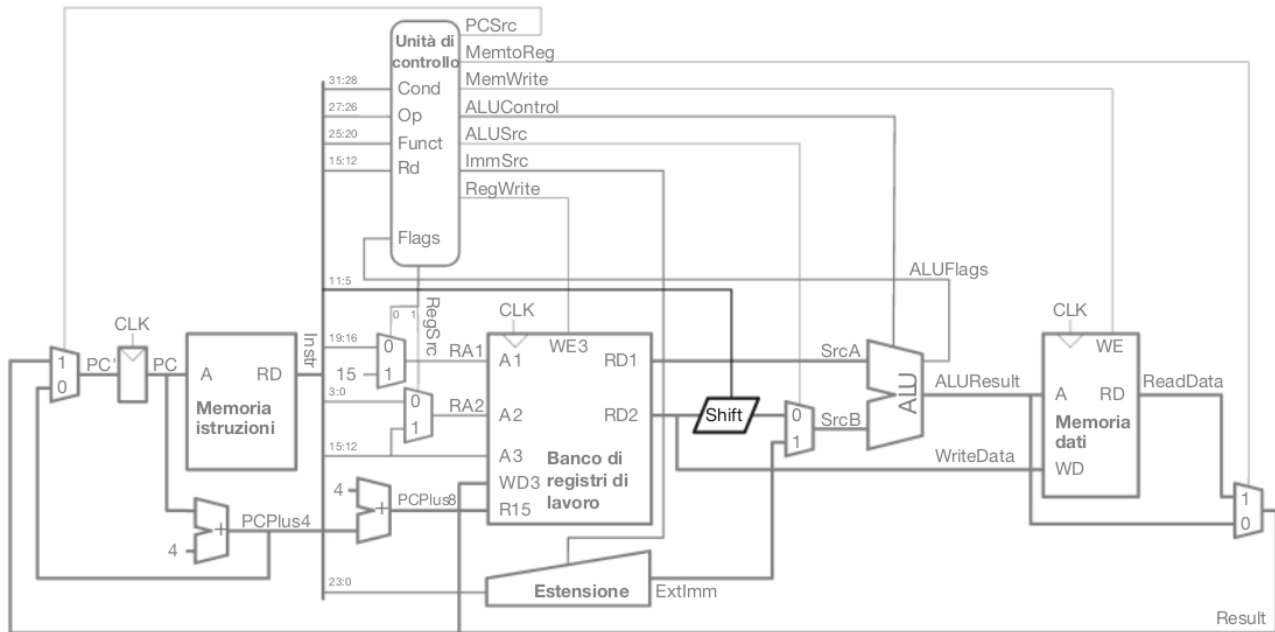


Branch

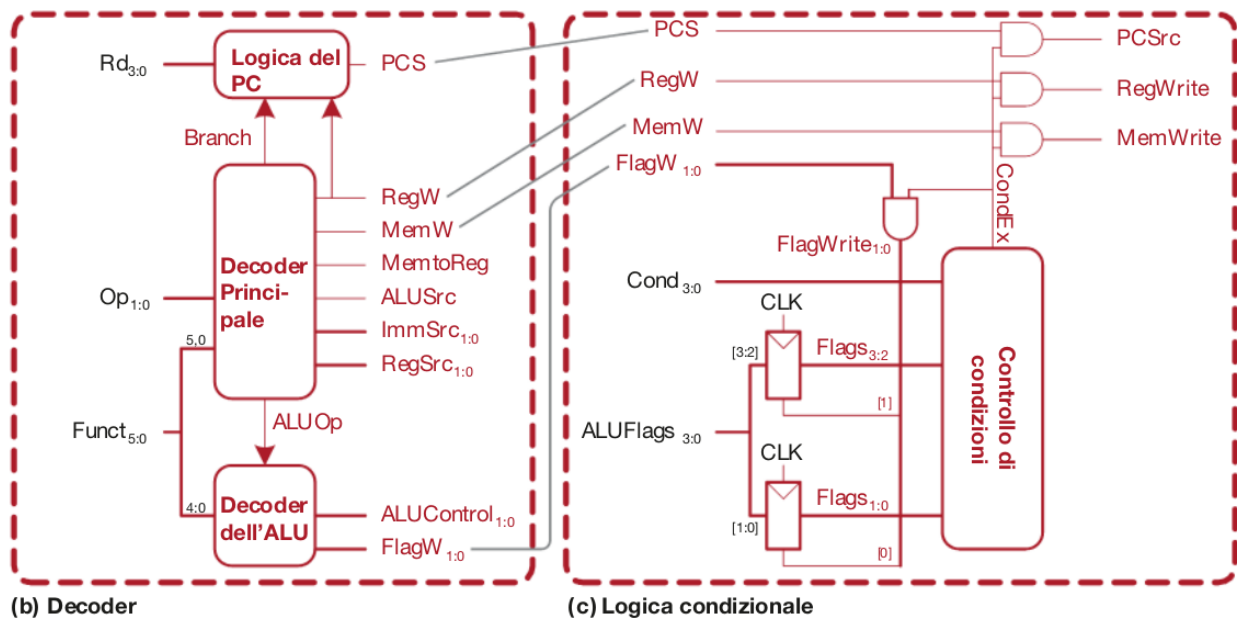
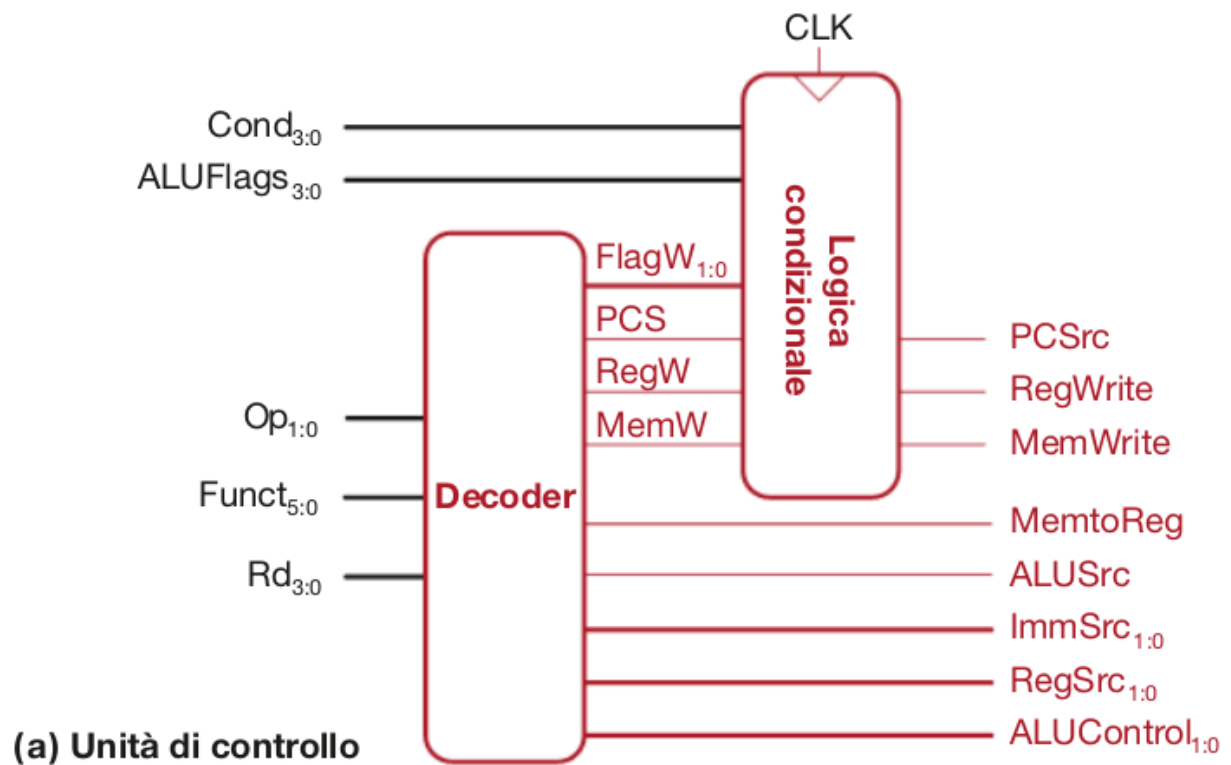


Processori

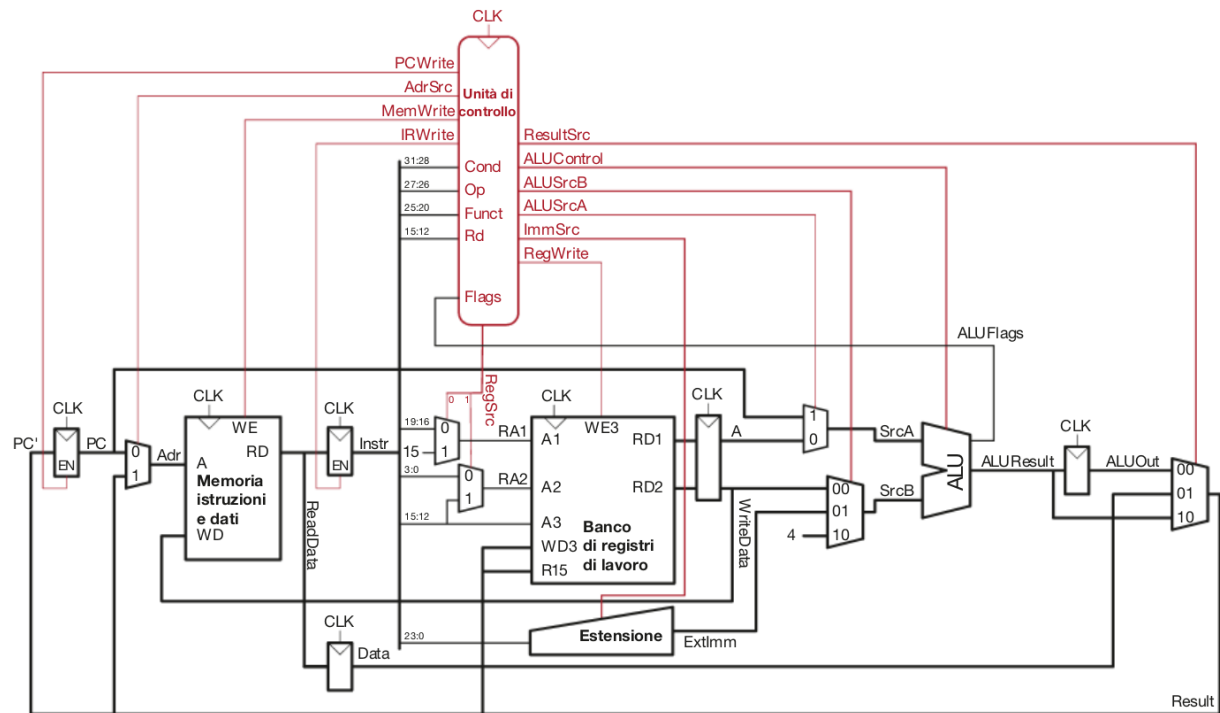
Single-Cycle



Unità di Controllo



Multi-Cycle



Pipeline

