















The I/O direction of each port in X1-004 is probably determined by connecting the pins 36,38,39, 42 and 43 to VCC and GND accordingly.

In tnzs IOD -> IOC, IOB -> IOA

E1	E2	E3	Active output
H	L	L	INA -> D, IOB -> IOA
H	L	H	INB -> D, IOB -> IOA
L	H	L	IOC -> IOD, IOB -> IOA
X	X	X	IOB -> IOA

All inputs are active low, when the outputs are not active they are in tri-state state. Except for IOA in this case.

