**JAMMA_CONN**

JM1

| | | | |
|---|---|---|---|
| GND | 1 | GND | A |
| VSSA | 2 | GND | B |
| VDDA | 3 | +5V | C |
| +5V | 4 | +5V | D |
| -5V | 5 | -5V | E |
| +12V | 6 | +12V | F |
| COIN_CNT_1 | 8 | COIN_CNT_2 | J |
| NC | 9 | NC | K |
| SPEAKER_(+) | 10 | SPEAKER_(-) | L |
| NC | 11 | NC | M |
| RED | 12 | GREEN | N |
| BLUE | 13 | SYNC | P |
| VIDEO_GND | 14 | SERVICE | R |
| NC | 15 | NC | S |
| COIN_1 | 16 | COIN_2 | T |
| 1P_START | 17 | 2P_START | U |
| 1P_UP | 18 | 2P_UP | V |
| 1P_DOWN | 19 | 2P_DOWN | W |
| 1P_LEFT | 20 | 2P_LEFT | X |
| 1P_RIGHT | 21 | 2P_RIGHT | Y |
| 1P_BTN_1 | 22 | 2P_BTN_1 | Z |
| 1P_BTN_2 | 23 | 2P_BTN_2 | a |
| 1P_BTN_3 | 24 | 2P_BTN_3 | b |
| 1P_BTN_4 | 25 | 2P_BTN_4 | c |
| 1P_SELECT | 26 | 2P_SELECT | d |
| GND | 27 | GND | e |
| GND | 28 | GND | f |

PWR_FLAG  VCC
GND
PWR_FLAG
VCC
VSS
+12V

PWR_FLAG
VSS  VCC
+12V
PWR_FLAG

COIN_CNT_1  COIN_CNT_2
SPEAKER(+)  SPEAKER(-)

VSS
VSS

**3P** (CN1 Conn_01x15)

| 1 | COIN_3 |
| 2 | 3P_LEFT |
| 3 | |
| 4 | 3P_RIGHT |
| 5 | 3P_UP |
| 6 | 3P_DOWN |
| 7 | 3P_BTN_1 |
| 8 | 3P_BTN_2 |
| 9 | 3P_BTN_3 |
| 10 | 3P_START |
| 11 | 3P_SERV |
| 12 | |
| 13 | |
| 14 | |
| 15 | |

**4P** (CN2 Conn_01x15)

| 1 | COIN_4 |
| 2 | 4P_LEFT |
| 3 | |
| 4 | 4P_RIGHT |
| 5 | 4P_UP |
| 6 | 4P_DOWN |
| 7 | 4P_BTN_1 |
| 8 | 4P_BTN_2 |
| 9 | 4P_BTN_3 |
| 10 | 4P_START |
| 11 | 4P_SERV |
| 12 | |
| 13 | |
| 14 | |
| 15 | VSS |

CP2A
PAG. 19 SCH

**objects**

MAIN_A[15..0] → MAIN_A[12..0]
MAIN_DB[7..0] → MAIN_DB[7..0]
OBJCLK6
OBJCLK12
CRCS → CRCS
DS → DS
OBJCS → OBJCS
OBJREG → OBJREG
SYSRES → SYSRES
OBJCHA → OBJCHA
VBLANK → VBLANK
XSYNC → XSYNC
RD → RD

PAL[4..0] → PAL[4..0]
PRI[4..0] → PRI[4..0]
OCOL[3..0] → OCOL[3..0]
SHD[1..0] → SHD[1..0]
CPU → CPU

File: objects.kicad_sch

**scroll**

MAIN_A[15..0] → MAIN_A[15..0]
MAIN_DB[7..0] → MAIN_DB[7..0]
SCRCLK → SCRCLK
VBLK → VBLK
RD → RD
SYSRES → SYSRES
VRAMCS → VRAMCS
RMRD → RMRD

APAL[2..0]  BPAL[2..0]
BPAL[2..0]  BCOL[3..0]
BCOL[3..0]  FPAL[2..0]
FPAL[2..0]  ACOL[3..0]
ACOL[3..0]  FCOL[3..0]
FCOL[3..0]  OBJCLK12
OBJCLK12    OBJCLK6
OBJCLK6     XSYNC
XSYNC       MRESET
MRESET      PE
PED         PE
SNDRES      SNDRES
PQD         PQ
CPURES      CPURES
HSYNC       HSYNC
VBLANK      VBLANK
HBLANK      HBLANK
JP3         JP3
INT         INT
FIRQ        FIRQ
NMI         NMI

File: scroll.kicad_sch

**icu**

MAIN_A[15..0] → MAIN_A[4..0]
MAIN_DB[7..0] → MAIN_DB[7..0]
AFR → AFR
HIPCS → HIPCS
WR → WR
AS → AS
IOCS → IOCS
CPURES → CPURES
CCUCS → CCUCS
CCUCK → CCUCK
VLDI → VLDI
HLDI → HLDI
+12V → +12V

UWR  UWR
LWR  LWR
COIN_CNT_1  COIN_CNT_2
COIN_CNT_2  COIN_CNT_1
SCRCLK  SCRCLK
INT  INT
IRQEND  IRQEN
EEPDID  EEPDI
WOCOD  WOC0
WOC1D  WOC1
MSCHGD  MSCHG
EEPCSD  EEPCS
EEPCLKD  EEPCLK
COIN1D  COIN_1
COIN2D  COIN_2
OBJCHAD  OBJCHA
RMRDD  RMRD
BRAMBKD  BRAMBK
INITD  INIT
SYSRES  SYSRES
SOUNDON  SOUNDON
SOUNDREG  SOUNDREG
VBLKD  VBLK
CRCSD  CRCS
AFRD  AFR

File: icu.kicad_sch

These schematics cover the missing pages in the original ones published by the maker. Signal names existing in the original pages have been preserved. New signal names try to follow the maker style or are taken from similar schematics of the same maker.

JT

Title: Vendetta
KONAMI PWB352292A
Sheet: /scroll/
File: scroll.kicad_sch
JOTEGO
www.patreon.com/jotego
github.com/jotego/jtcores/tree/master/cores/simson/sch/vendetta
Size: A3   Date: 2023-08-09
KiCad E.D.A. 8.0.8   Id: 2/4
Rev: José Tejada G.

**Title: Vendetta**

Size: A3  Date: 2023-08-09  **Rev: José Tejada G.**
KiCad E.D.A. 8.0.8  Id: 3/4

KONAMI 053247 OBJECT DRAWING

KONAMI 053246 OBJECT GENERATOR

OBJECT ROM ADDRESS BUS

OBJECT ROM DATA BUS

COLOR PALETTE GENERATOR

TILE DRAWING

TILE DRAWING CLK

GENERATOR TO DRAWING

# INTERRUPT CONTROL UNIT

MAIN_DB[7..0]

**74LS174 — hex flip flop (9)**
- MAIN_DB5 3 D0 Q0 2 COIN1
- MAIN_DB4 4 D1 Q1 7 COIN2
- MAIN_DB3 6 D2 Q2 7 BRAMBK
- MAIN_DB2 11 D3 Q3 10 RMRD
- MAIN_DB1 13 D4 Q4 12 INIT
- MAIN_DB0 14 D5 Q5 15 OBJCHA
- Cp 9 / Mr 1 / D9

**E11 — flip flops (74LS273)**
- MAIN_DB7 3 D0 Q0 2 WOC0
- MAIN_DB6 4 D1 Q1 5 WOC1
- MAIN_DB5 7 D2 Q2 6 MSCHG
- MAIN_DB4 8 D3 Q3 9 EEPCS
- MAIN_DB3 13 D4 Q4 12 EEPCLK
- MAIN_DB2 14 D5 Q5 15 EEPDI
- MAIN_DB1 17 D6 Q6 16 IRQEN
- MAIN_DB0 18 D7 Q7 19
- Cp 11 / Mr 1

**F12 regulator**
- EN / VDD / OUT
- GND / VSS
- 24MHz

**F11D 74LS04** 9 → 8 → SCRCLK (24M)

**F11E 74LS04** 13 → 12 COIN_CNT_2

**G11B 74LS74**
- D / S / Q
- IR / Q

**U12 — KONAMI 051550** (Master/RESET generation for the whole system and watchdog timer)
- 1 CLK
- 4 VREF
- 5 RES
- 7 CT
- 11 VS
- 15 COIN1 OUT1
- 16 COIN2 OUT2
- 9 +5V / +12V 19
- 10 RESET
- 20 CNT2
- 21 CNT2
- 22 CNT1
- 23 CNT1
- N.C

AFRD, 2A11, CPC1, VCC, VSS, +12V, C20, EMI_Filter_CLK

COIN_CNT_2
COIN_CNT_1

**D11 — KONAMI 053252 — CTR Interrupt Control Unit**
- 39 VDD
- 17 VDD
- 4 VLDI
- 3 HLDI
- 7 RES
- 5 CLK
- 36 CCO
- 37 RW
- 38 AB3 (MAIN_A3)
- 40 AB2 (MAIN_A2)
- 41 AB1 (MAIN_A1)
- 42 AB0 (MAIN_A0)
- 21 DB7 (MAIN_DB0)
- 20 DB6 (MAIN_DB1)
- 19 DB5 (MAIN_DB2)
- 18 DB4 (MAIN_DB3)
- 16 DB3 (MAIN_DB4)
- 15 DB2 (MAIN_DB5)
- 14 DB1 (MAIN_DB6)
- 13 DB0 (MAIN_DB7)
- 43 SEL2
- 23 SEL1
- 22 SEL0
- 28 VSS
- 6 VSS
- 30 VLD
- 31 HLD
- 12 CRES
- 44 FCNT
- 35 INT2
- 34 INT1
- 25 CSY
- 24 CBK
- 26 VSY
- 27 VBK
- 29 HSY
- 33 HBK
- 32 HBS
- 11 CLK4
- 10 CLK3
- 9 CLK2
- 8 CLK1
- 1 PE
- 2 PQ

FOOTPRINT ONLY

SYSRES, VLDID, HLDID, CCUCKD, CCUCSD, WR, CCUCS

**G5B 74LS08** → CPURES, IRQEN → 6
**E9B 74LS04** → VBLK 9 → 8
**H11A 74LS74** → VSS, D, Q 5, IR, Q, VCC → INT

**C15 — KONAMI 054000**
- MAIN_A4 6 A4 D7 19 MAIN_DB7
- MAIN_A3 7 A3 D6 18 MAIN_DB6
- MAIN_A2 8 A2 D5 17 MAIN_DB5
- MAIN_A1 9 A1 D4 16 MAIN_DB4
- MAIN_A0 10 A0 D3 15 MAIN_DB3
- D2 13 MAIN_DB2
- D1 12 MAIN_DB1
- D0 11 MAIN_DB0
- WRD 27 P27
- 26 P26
- 22 P22
- VCC 20 P20
- N.C 1,2,3,4,5,21,23,24,25

**E9C 74LS04** HIPCSD 5 → 6

**D10 — 74LS138 — address decoder**
- MAIN_A1 1 A0 O0 15
- MAIN_A2 2 A1 O1 14
- MAIN_A3 3 A2 O2 13 SOUNDON
- O3 12 SOUNDREG
- O4 11 CRCS
- O5 10 AFR
- VCC 6 E3 O6 9
- ASD 5 E2 O7 7
- IOCSD 4 E1

**G4B 74LS04** MAIN_A0 3 → 4

**F10A 74LS32** 1, 2 → 3 UWR
**F9D 74LS32** 13, 12 → 11 LWR

**Title: Vendetta**
Size: A4 | Date: 2023-08-09 | Rev: José Tejada G.
KiCad E.D.A. 8.0.8 | Id: 4/4

JT