



CP2A PAG. 19 SC	MAIN_A[150] MAIN_DB[70] OBJCLK6 OBJCLK12 CRCS HDS OBJCS OBJREG SYSRES OBJCHA VBLANK XSYNC RD	objects  MAIN_A[120]  MAIN_DB[70]  >OBJCLK6  >OBJCLK12  >CRCS  >DS  >OBJCS  >OBJREG  >SYSRES  >OBJCHA  >VBLANK  XSYNC  >RD	PAL[40]D PRI[40]D OCOL[30]D SHD[10]D CPUD	PAL[40 PRI[40 OCOL[30 SHD[10
		File, objects kisad sch		

	scroll		
MAIN_A[150] MAIN_DB[70] SCRCLK VBLK RD SYSRES VRAMCS RMRD	scroll  MAIN_A[150]  MAIN_DB[70]  >SCRČIK  >VBLK  >RD  >SYSES  >VRAMČS  >RMRD	APAL[20]D BPAL[20]D BCOL[30]D FPAL[20]D ACOL[30]D OBJCLK12D OBJCLK6D XSYNCD MRESETD PED SNDRESD PQQ CPURESD	APAL[20] BPAL[20] BPAL[20] BCOL[30] FPAL[20] ACOL[30] OBJCLK12 OBJCLK6 XSYNC MRESET PE SNDRES PQ CPURES
	File: scroll.kicad_sch		

	icu		
MAIN_A[150]	→MAIN_A[40]	<u>UWR</u> D	UWR
MAIN_DB[70]	→ MAIN_DB[70]	LWRD	LWR
AFR	AFR	COIN CNT 1D	COIN_CNT_2
HIPCS	HIPCS	COIN_CNT_2D	COIN_CNT_1
WR	- DMR	SCRCLKD	SCRCLK
AS	DAS	INTD	INT
IOCS	>10 <u>CS</u>	IRQEND	IRQEN
CPURES	CPURES	EEPDID	EEPDI
ccu <del>cs</del>	DCCUCS	WOCOD	WOCO
CCUCK	- DCCUCK	WOC1D	WOC1
VLDI	- DVLDI	MSCHGD	MSCHG
HLDI	DHLDI	EEPCSD	EEPCS
+12V	1+12V	EEPCLKD	EEPCLK
	J+121	COIN1D	COIN_1
		COIN2D	COIN_2
		OBJCHAD	OBJCHA
		RMRDD	RMRD
		BRAMBKD	BRAMBK
		INITD	INIT
		SYSRESD	SYSRES
		SOUNDOND	SOUNDON
		SOUNDREGD	SOUNDREG
		VBLKD	VBLK
		CRCSD	CRCS
		ĀFRD	ĀFR
		AFRD	
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These schematics cover the missing pages in the original ones published by the maker. Signal names existing in the original pages have been preserved. New signal names try to follow the maker style or are taken from similar schematics of the same maker.

www.patreon.com/jotego
github.com/jotego/jtcores/tree/master/cores/simson/sch/vendetta
JOTEGO
KONAMI PWB352292A
Sheet: /
File: vendetta.kicad\_sch

Title: Vendetta
Size: A3 Date: 2023-08-09
KiCad E.D.A. 8.0.6
Id: 1/4





