

These schematics cover the missing pages in the original ones published by the maker. Signal names existing in the original pages have been preserved. New signal names try to follow the maker style or are taken from similar schematics of the same maker.

The schematic shows a complex circuit with multiple integrated circuits (ICs) and passive components. Key components include:

- 74LS244** (D29): A hex invert buffer with inputs I0a-I3a, I0b-I3b, and outputs O0a-O3a, O0b-O3b.
- 74LS257** (A26, B26): 4-to-1 multiplexers with inputs I0a-I3a, I0b-I3b, I0c-I3c, I0d-I3d and outputs Za, Zb, Zc, Zd.
- 74LS253** (D25, D24, D23, D22): 4-bit binary counters with inputs I0a-I3a, I0b-I3b, I0c-I3c, I0d-I3d and outputs O0a-O3a, O0b-O3b, O0c-O3c, O0d-O3d.
- 74LS174** (B22): A hex flip-flop with inputs D0-D5, Q0-Q5, and outputs OUT1, OUT2.
- 051550** (U1): A master/reset generation IC for the whole system and watchdog timer.
- JAMMA_CONN** (JM1): A connector with pins for power, ground, and various control signals.

The title block contains the following information:

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 www.patreon.com/jotego
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