



XSYNC >XSYNC RD >RD	OBJCHA OBJCHA	OBJREG OBJREG
OBJREG SYSRES OBJCHA OBJCHA OBJCHA OBJCHA	OBJREG OBJREG	
SYSRES DUBJECTS OBJCHA OBJCHA OBJCHA	PAG. 19 SCH DS DS DS OBJCS OBJREG OBJREG OBJREG	PAG. 19 SCHDS DS

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MAIN_A[150]	>MAIN_A[40]	<u>uwr</u> d-	UWR
MAIN_DB[70]		LWRD-	LWR
AFR	→ MAIN_DB[70] → DAFR	COIN CNT 1D	COIN_CNT_2
HIPCS	- DHIPCS	COIN_CNT_2D	COIN_CNT_1
WR	→ WR	SCRCLKD	SCRCLK
AS	DAS	INTD	INT
IOCS	010 <u>CS</u>	IRQEND	IRQEN
CPURES	CPURES	EEPDID	EEPDI
ccu <del>cs</del>	-DCCUCS	WOCOD	WOCO
CCUCK	->ccuck	WOC1D	WOC1
VLDI	- DUCUCK - DVLDI	MSCHGD	MSCHG
HLDI	- DHLDI	EEPCSD	EEPCS
+12V	7+12V	EEPCLKD	EEPCLK
		COIN1D	COIN_1
		COIN2D	COIN_2
		OBJCHAD	OBJCHA
		RMRDD	RMRD
		BRAMBKD	BRAMBK
		INITD	INIT
		SYSRESD	SYSRES
		SOUNDOND	SOUNDON
		SOUNDREGD	SOUNDREG
		VBLKD	VBLK
		CRCSD	CRCS
		AFRD-	ĀFR
		AFRD	
	File: icu.kicad_sch		

These schematics cover the missing pages in the original ones published by the maker. Signal names existing in the original pages have been preserved. New signal names try to follow the maker style or are taken from similar schematics of the same maker.

www.patreon.com/jotego
github.com/jotego/jtcores/tree/master/cores/simson/sch/vendetta
JOTEGO
KONAMI PWB352292A
Sheet: /
File: vendetta.kicad\_sch

Title: Vendetta
Size: A3 Date: 2023-08-09
KiCad E.D.A. 8.0.4

Rev: José Tejada G





