

VSYNC GFX_CLK BGTRNSP VD[150] GDA[70] GDB[70] GDC[70] GDD[70] H1_BUF HBIF	>VD[70] >GDA[70] >GDB[70] >GDC[70] >GDD[07] >H1_BUF	SYNCD REDD GREEND BLUED	SYNC RED GREEN BLUE

WATCHDOG	watchdog WATCHDOG	RESETD	RESET
+12V	7+12V	COIN_LOCKD	COIN_LOCK
P26_DRQ	→P26_DRQ	COIN_LOCKD	
CNN1B	>CNN1A		
CNN1A	->CNN1B		
	File: watchdog.kicad_sch		

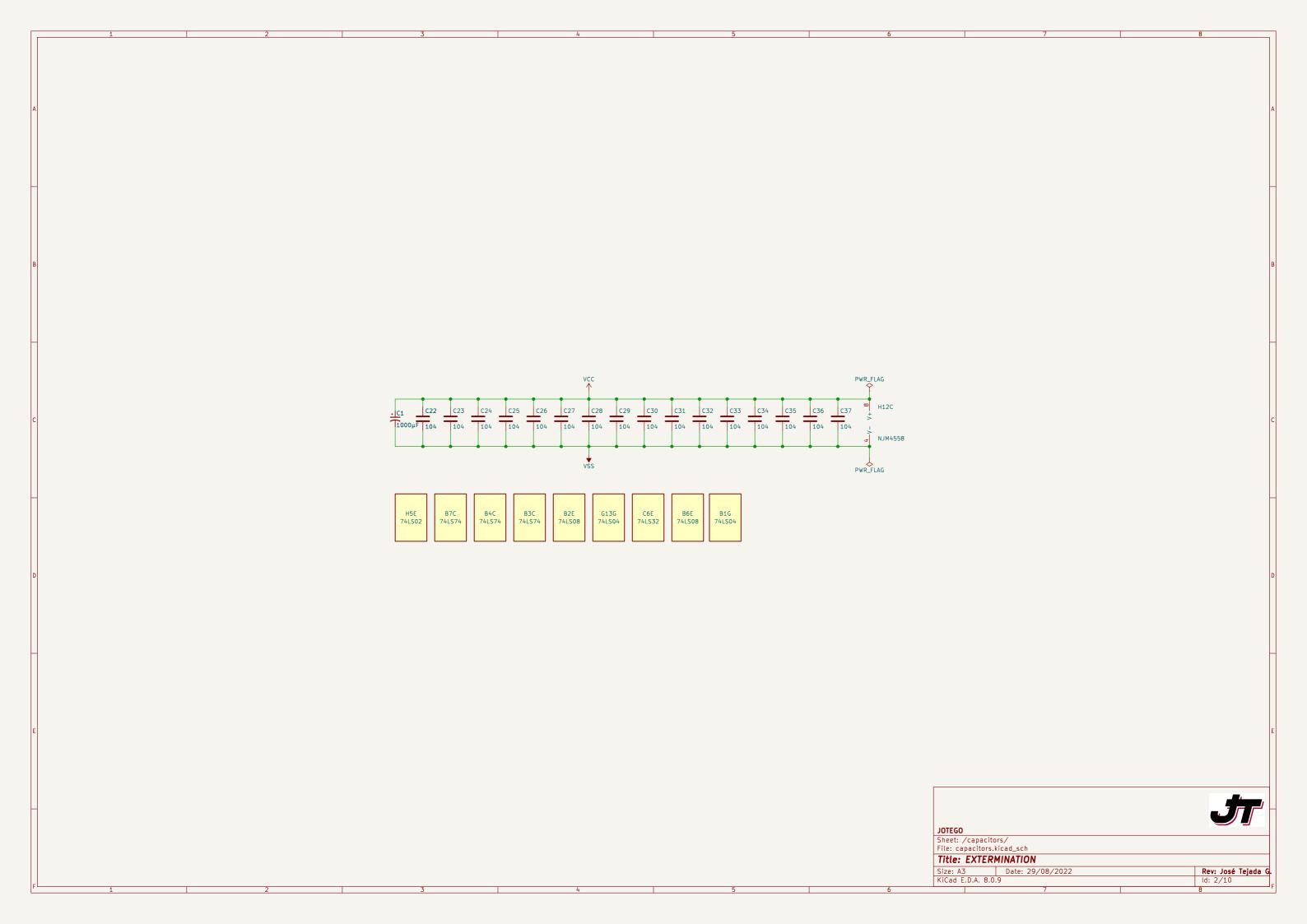


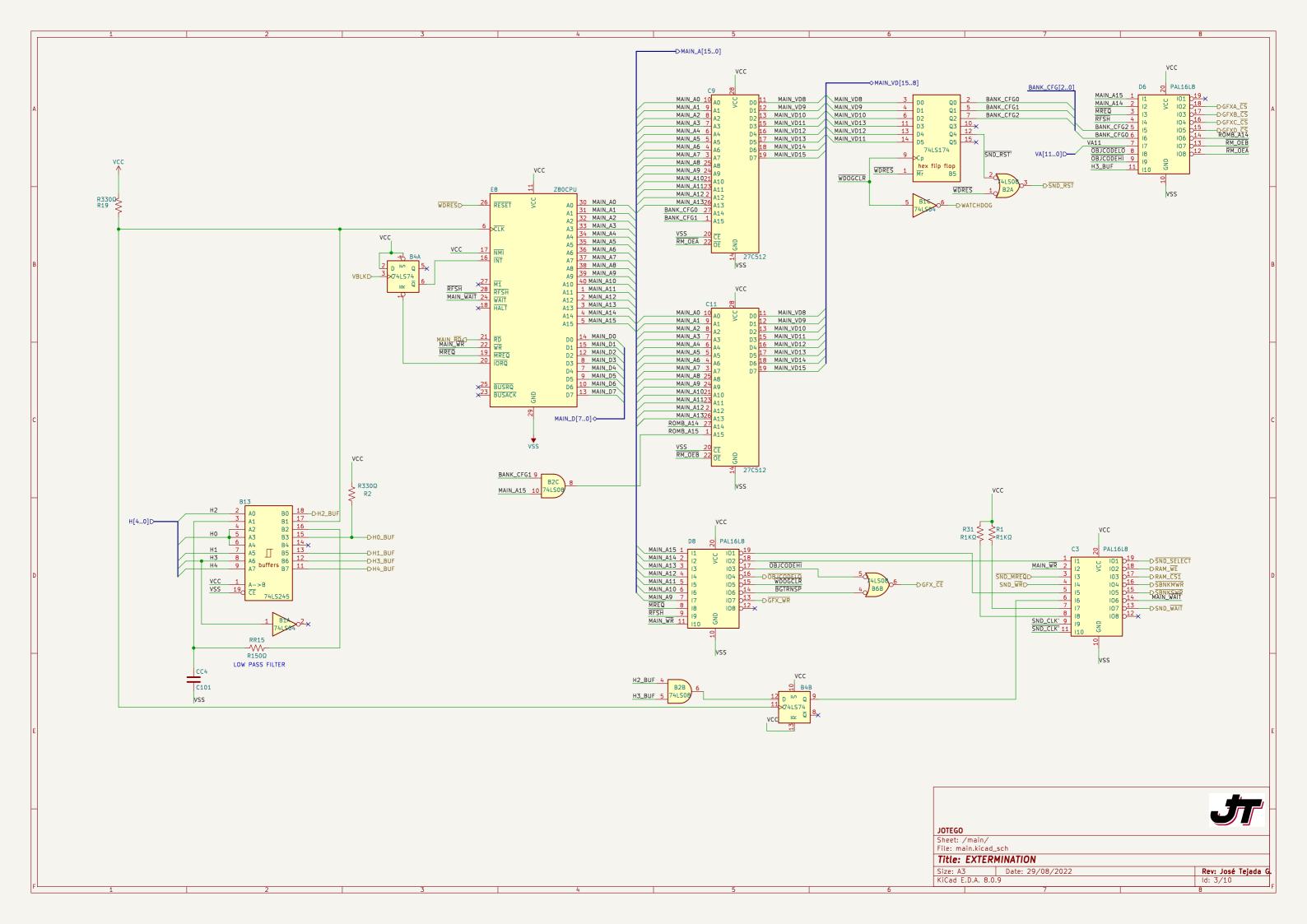
RESET SND_CS	mcu >RESET >SND_CS	SND_D[70]D	SND_D[70] SND_RD
COIN_LOCK	>COIN_LOCK	P26_DRQD	P26_DRQ
CI/01	DCI/01	. 2020.140	
CI/02	DCI/02		
H1_BUF	DH1 BUF	SND WRD	SND_WR
	D111_001	CNN1AD	CNN1A
N.CO	⊓N.C0	CNN1BD	CNN1B
N.C3	IN.C3	COIN_1D	COIN_1
MCU_RST			COIN_2
SND_A[150]	>MCU_RST	COIN_2D	COIN CNT 1
P[2310]	>SND_A[150] >P[2310]	COIN_CNT_1D	COIN_CNT_2
	File: mcu.kicad sc	h	

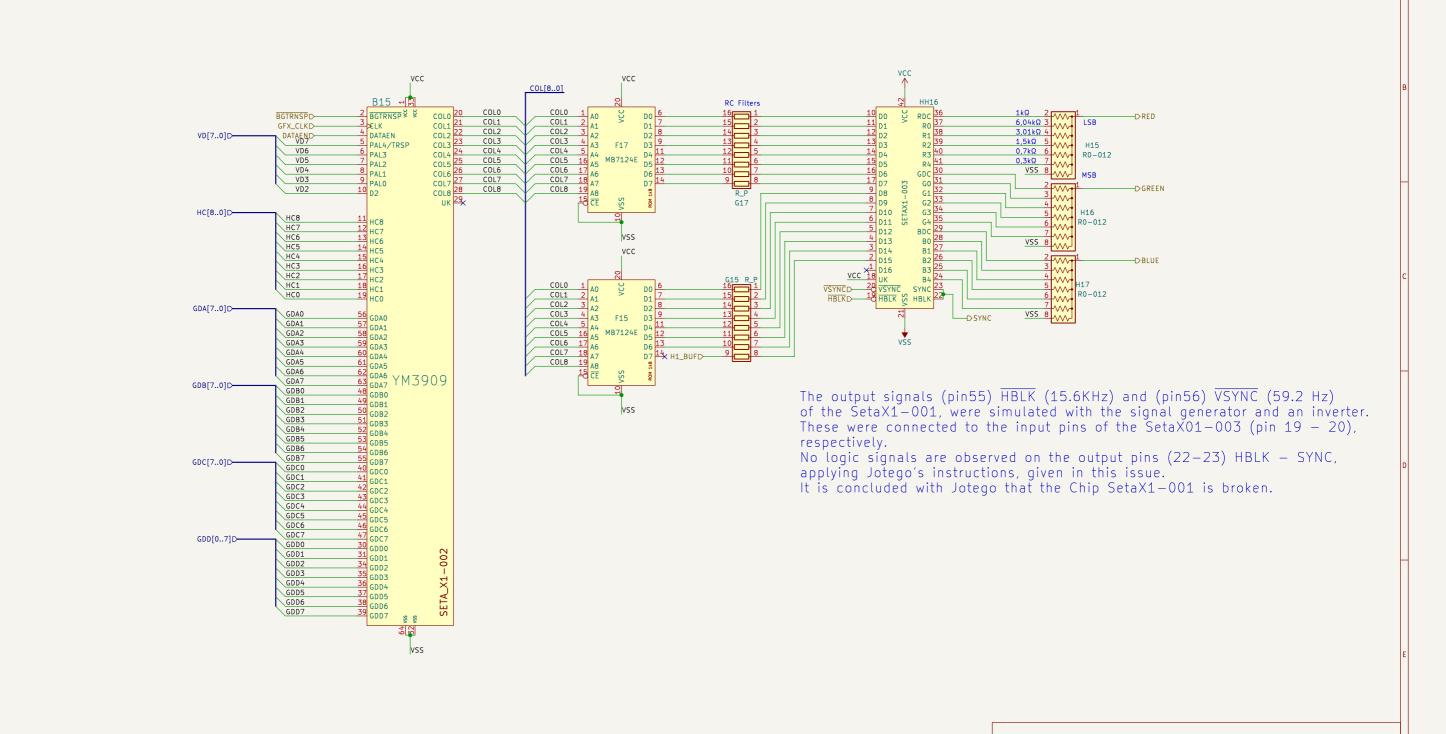
gfx GFX CE	
cry cr	
GFX_CE DGFX_CE	
OBJCODELO VSYNCD VSYNCD	IC.
GFX_WR DATAEND DATAE	N
GFXA_CS GFX_CLKD GFX_CL	K
GFXB_CS VBLKD VBL	K
	0]
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	0]
	01
CDD[7 (01
GDB[70] GDC[70] GDC[70]	01
H4_BUF GDC[70] GDC[70] GDD[70	01
MAIN ALAE OI PH4_BUF GDD[/U]	
MAIN_D[70] MAIN_A[150] HBLKD HBLKD	
WDRE 0 WDRE 0 WDRE	S
VA[110] WDRESO UCIO	01
H3_BUF HC[80]D	_

	sound		
ND_A[150]	OSND_A[150]	MAIN_VD[158] ♦	MAIN_VD[158]
ND_D[70]	♦SND_D[70]		
ND_SELECT	SND_SELECT	SND_MREQD	SND_MREQ
ND_WR	SND_WR	SHD_HILLED	
ND_RD	⇒SND_RD		
0_BUF	DHO_BUF	SND_CSD	SND_CS
ND_RST		MCU_RSTD	MCU_RST
ND_WAIT	SND_RST	MCU_RSID	
	⇒SND_WAIT		
AIN_RD			
.C1	→MAIN_RD		
PEAKER(-)	□N.C1		
PEAKER(+)	□SPEAKER(-)		
AIN_A[130]	□SPEAKER(+)		
2_BUF	→MAIN_A[130]		
AM_WE	⊃H2_BUF		
AM_CS1	⊃RAM_WE		
	⊃RAM_CS1		
BNKSWR	SBNKSWR		
BNKMWR	SBNKMWR		
	File: sound.kica	d sch	

JOTEGO		
Sheet: /		
File: exterm.kica	d_sch	
Title: EXTERI	MINATION	
Size: A3	Date: 29/08/2022	Rev: José Tejada
KiCad E.D.A. 8.0.	9	ld: 1/10







Sheet: /colmix/ File: colmix.kicad_sch

Title: EXTERMINATION

Size: A3 Date: 29/08/2022 KiCad E.D.A. 8.0.9

