

VSYNC  GFX_CLK  BGTRNSP  VD[150]  GDA[70]  GDB[70]  GDC[70]  GDD[70]  H1_BUF  HBLK  DATAEN  HC[80]	>VD[70] >GDA[70] >GDB[70] >GDC[70] >GDC[70] >H1_BUF >HBLK >DATAEN	SYNCD REDD GREEND BLUED	SYNC RED GREEN BLUE
nc[ou]	HC[80]		
	rite: CotiiiiX.KICad_SCI		

WATCHDOG +12V	watchdog >WATCHDOG	RESETD	RESET COIN LOCK
P26_DRQ	—□+12V	COIN_LOCKD	COIN_LOCK
CNN1B	P26_DRQ		
CNN1A	CNN1A CNN1B		
	File: watchdog	.kicad_sch	

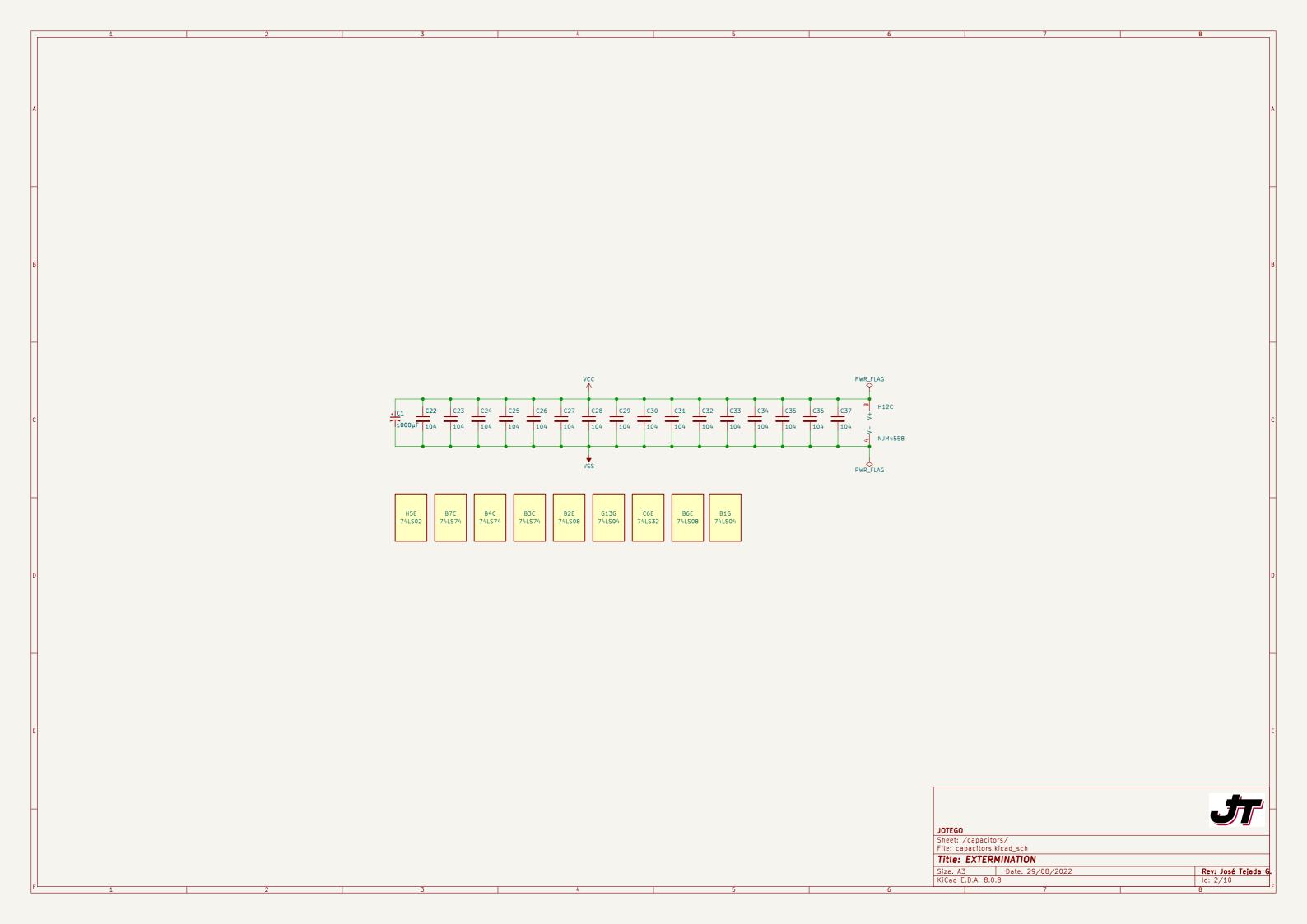


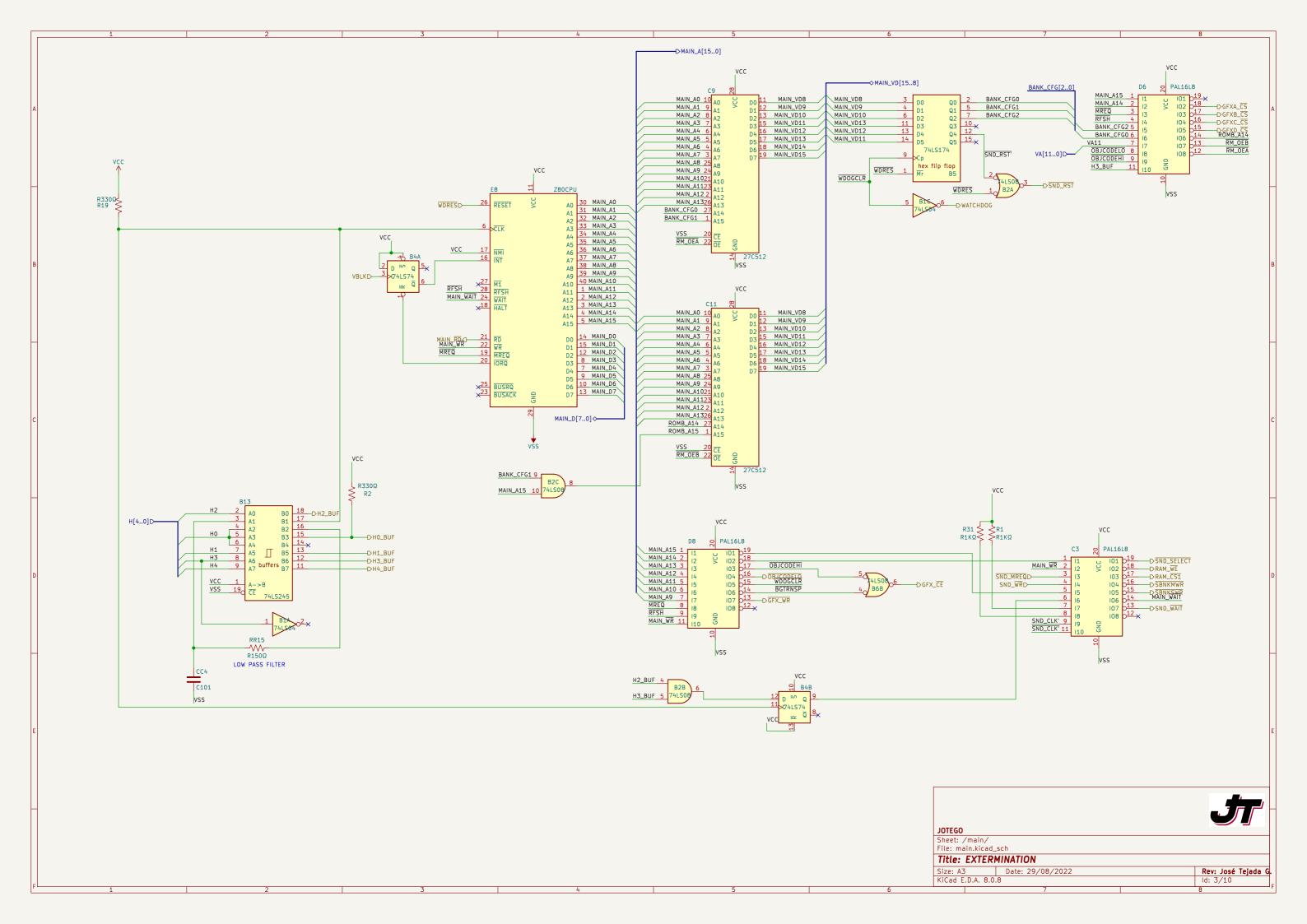
RESET SND_CS	mcu >RESET >SND_CS	SND_D[70]D	SND_D[70] SND_RD
COIN_LOCK	>COIN_LOCK	P26_DRQD	P26_DRQ
CI/01	DCI/01	1 2 0 _ 5 11 Q B	
CI/02	DCI/02		
H1_BUF	DH1 BUF	SND WRD	SND_WR
	D111_001	CNN1AD	CNN1A
N.CO	□N.C0	CNN1BD	CNN1B
N.C3	IN.C3	COIN_1D	COIN_1
MCU_RST			COIN_2
SND_A[150]	DMCU_RST	COIN_2D	COIN_CNT_1
P[2310]	OSND_A[150]	COIN_CNT_1D	COIN_CNT_2
	>P[2310]	COIN_CNT_2D	
	File: mcu kicad sc	h	

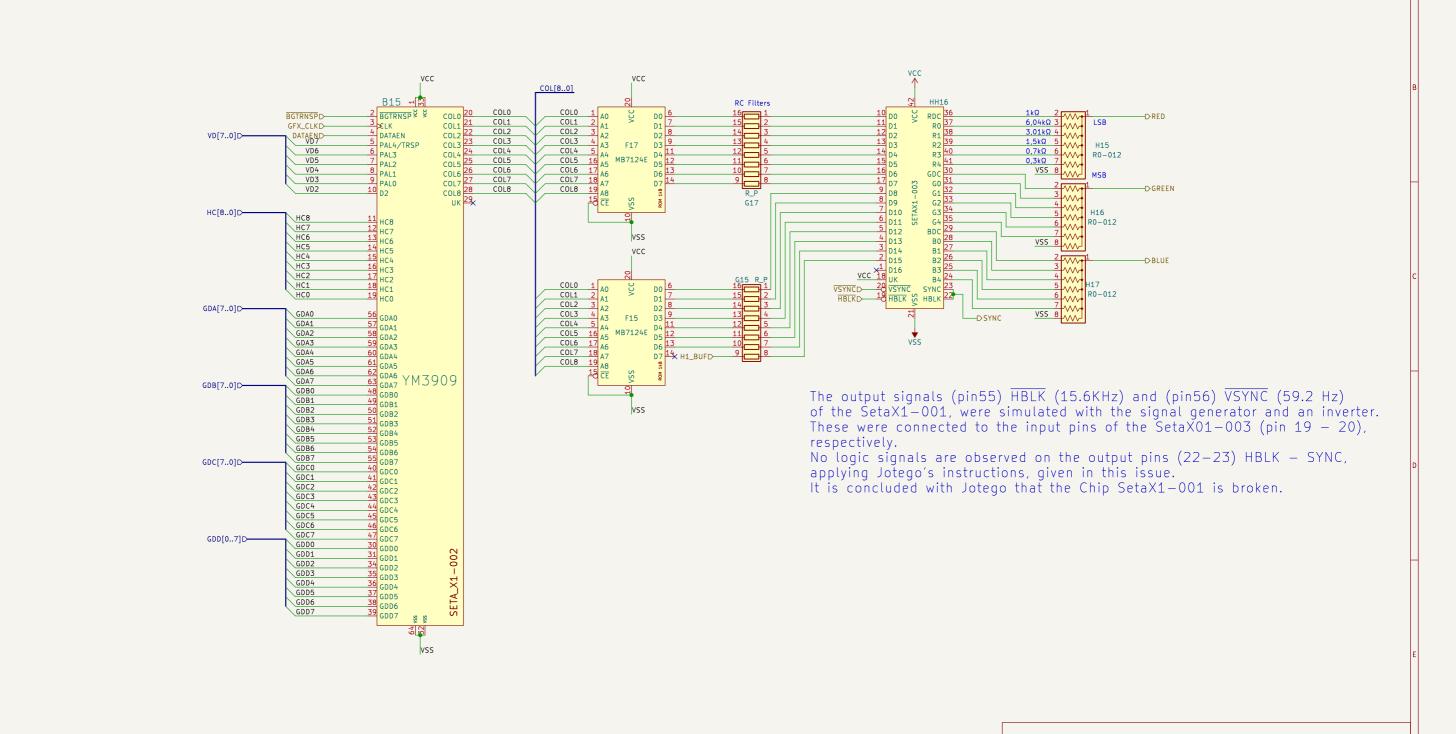
	gfx		
GFX_CE	->GFX_CE		
<b>OBJCODELO</b>	OBJCODELO	VSYNCD	VSYNC
GFX_WR	OGFX WR	DATAEND	DATAEN
GFXA_CS	GFXA_CS	GFX_CLKD	GFX_CLK
GFXB_CS	OGFXB_CS	VBLKD	VBLK
GFXC_CS	OGFXC_CS	H[40]D	H[40]
GFXD_CS	OGFXD_CS	VD[150]◊	VD[150]
MAIN_RD	->MAIN_RD	VD[150]♦	GDA[70]
	DMAIN_RU	GDA[70]♦	GDB[70]
			GDC[70]
H4_BUF	DU/ DUE	GDC[70]♦	GDD[70]
MAIN_A[150]	⊃H4_BUF ⊃MAIN_A[150]	GDD[70]♦ HBLKD	HBLK
MAIN_D[70]	DMAIN_A[15U]	HBLKD	
VA[110]	♦MAIN_D[70]	WDRES	WDRES
H3_BUF	OVA[110]		HC[80]
	H3_BUF	HC[80]D	

SND_D  70      SND_SELECT	SOUND	MAIN_VD[158]  SND_MREQD SND_CSD MCU_RSTD	SND_MREQ SND_CS MCU_RST
SND_RST			MCU_RST
SND_WAIT		TTCO_KSTD	
N.C1 SPEAKER(-) SPEAKER(+) MAIN_A[130] H2_BUF RAM_WE RAM_CS1	>MAIN_RD	d sch	

OTEGO		
Sheet: /		
ile: exterm.kica	d_sch	
Title: EXTERI	MINATION	
Size: A3	Date: 29/08/2022	Rev: José Tejada
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Sheet: /colmix/ File: colmix.kicad\_sch

Title: EXTERMINATION

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