

The frequency of CCLK is the frequency of P18M divided by 2.
 $f_{CCLK} = f_{P18M} / 2 = 18.432\text{Mhz} / 2 = 9.216\text{MHz}$.

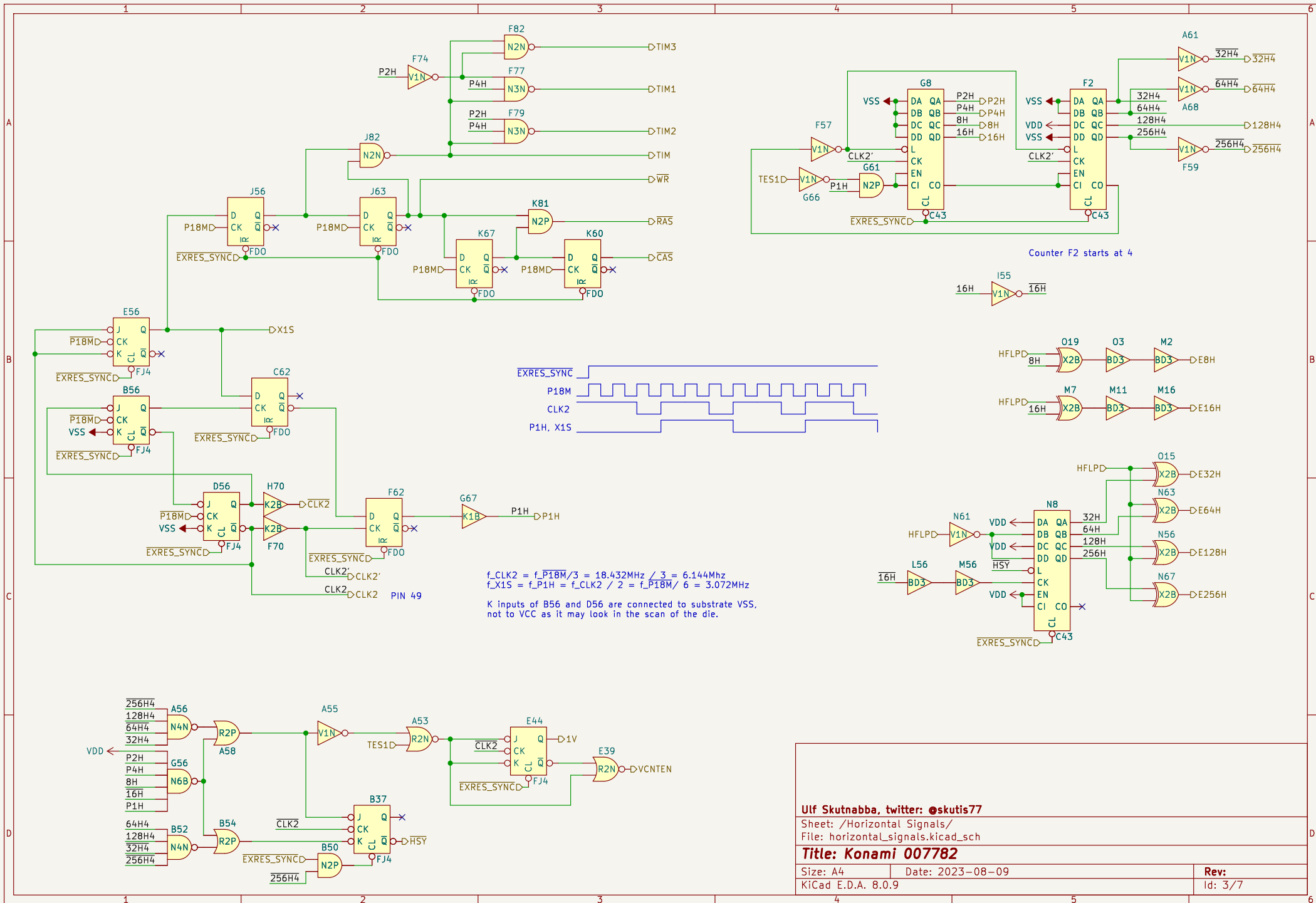


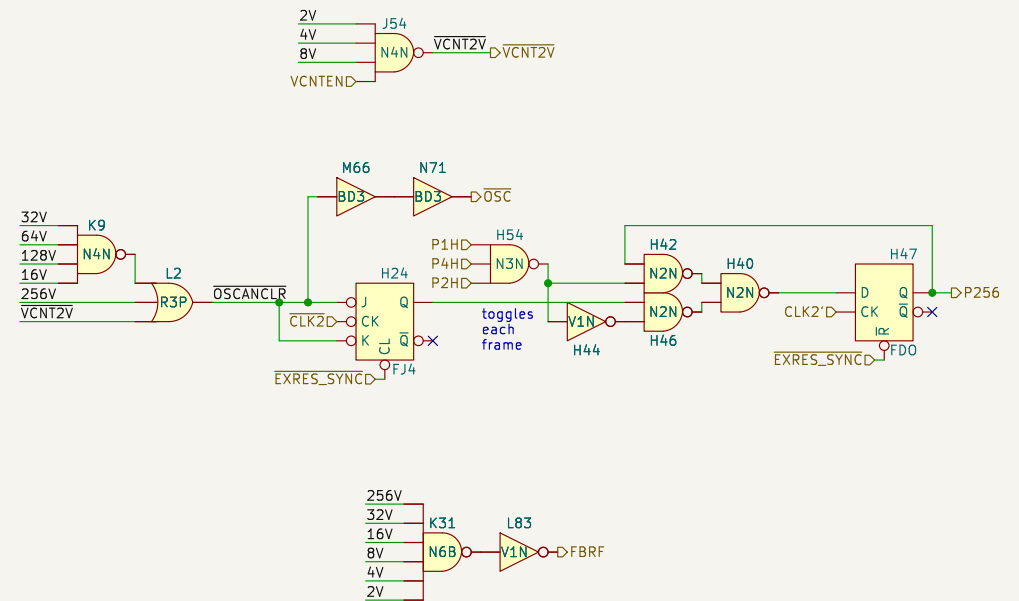
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 File: clocks_and_reset.kicad_sch

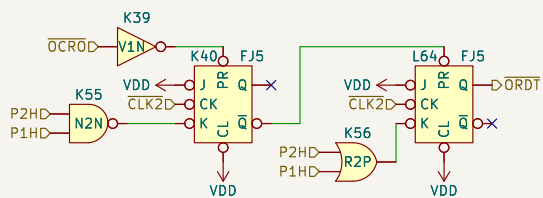
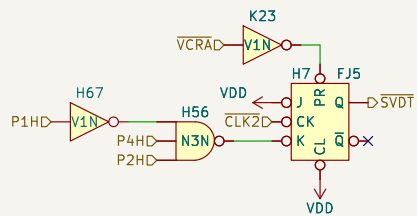
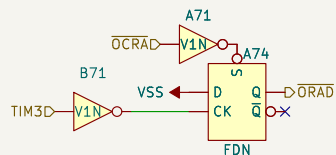
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File: data_acknowledge.kicad_sch

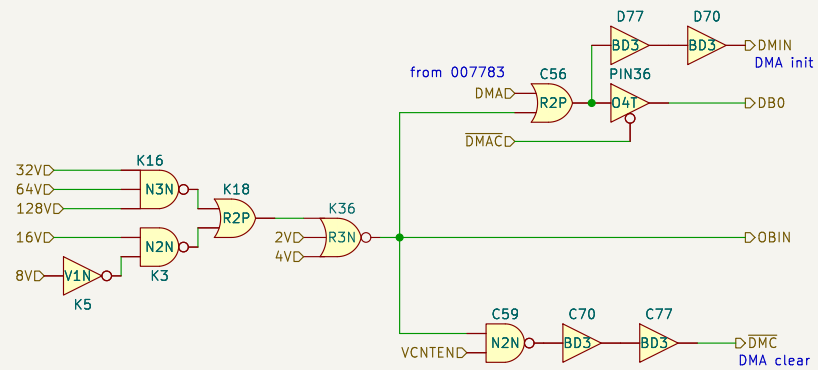
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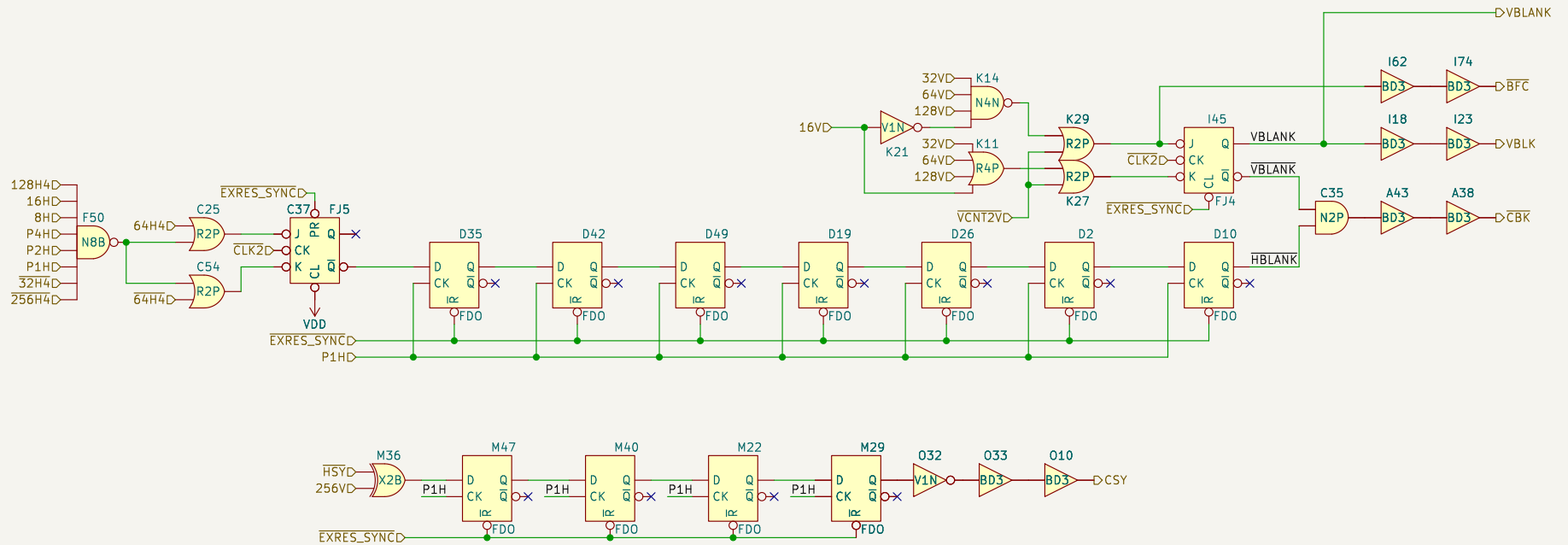
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Sheet: /Sync and Blanking/
File: sync_and_blanking.kicad_sch

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