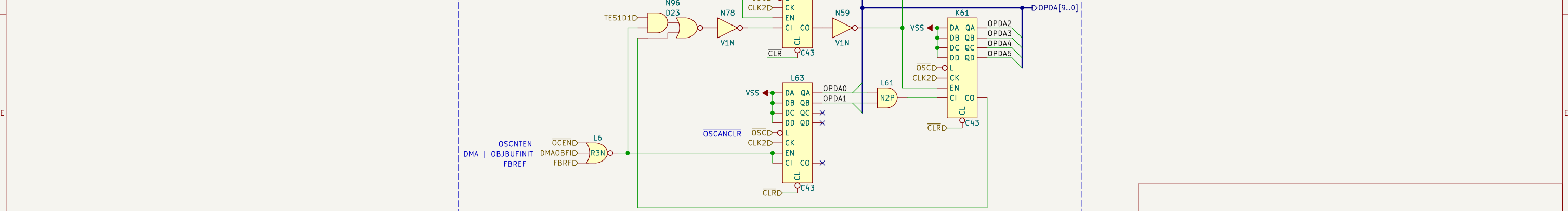


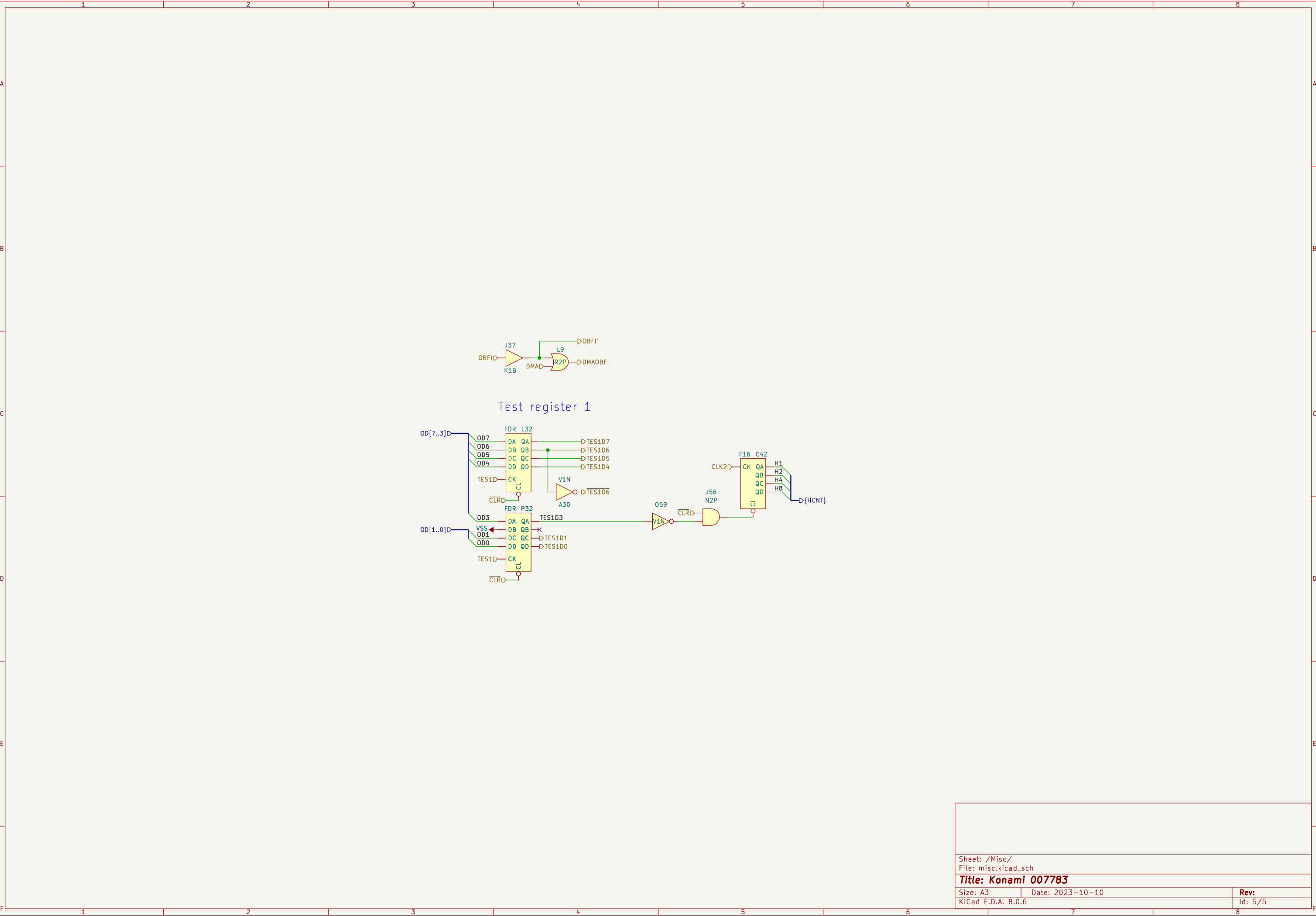
Addressing starts when \overline{OSC} is activated.
 OPDA[12:10] = 110 \rightarrow 0x1800..0x1BFF

When DMA, OBJBUFINIT and FBREF are not active the address is incremented when \overline{OCN} is deactivated.



Title: Konami 007783

Size: A3	Date: 2023-10-10	Rev:
KiCad E.D.A. 8.0.6		Id: 3/5



Four time slots available all the time for addressing.
0: - OBJ/BUF/INIT high: Writing zeros to all data in the Object Data Buffer.
- DMA high: Data copying from Object RAM to Object Buffer RAM.
1: Parsed object data copy to the frame buffers.
2: Master CPU access
3: Slave CPU access

OBFI	DMA	OH2	OH2'	OH4	OH4'
1	X	0	1	0	1
X	1	0	1	0	1

