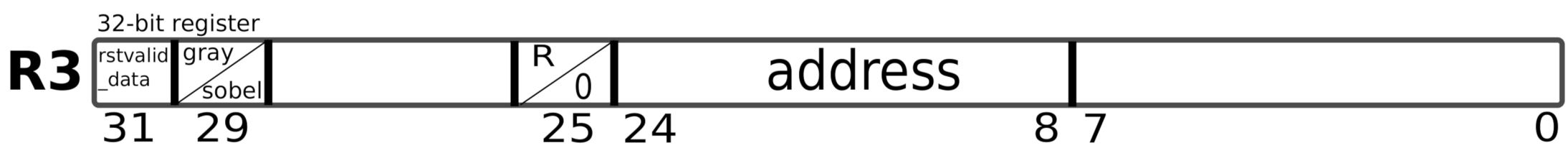
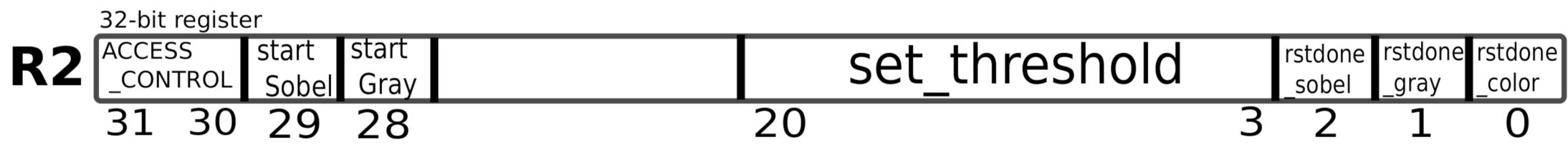
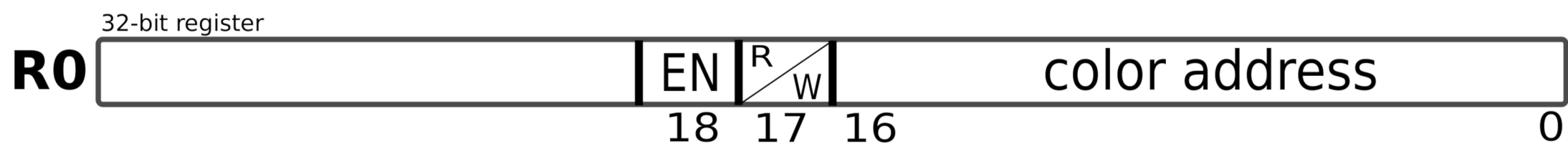
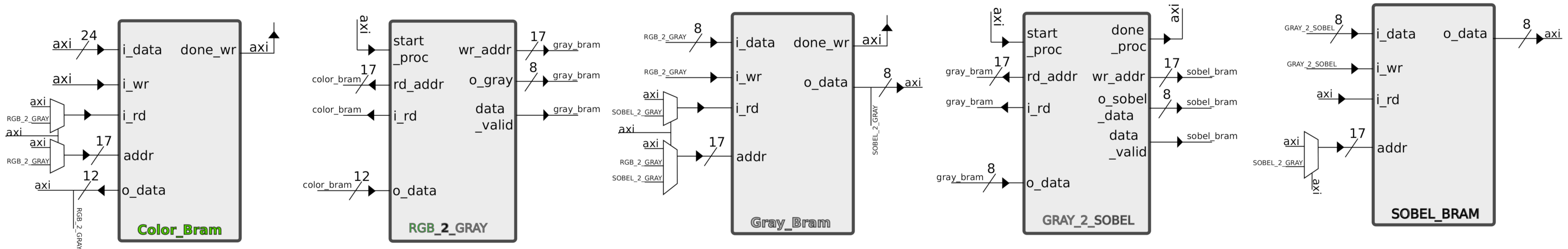
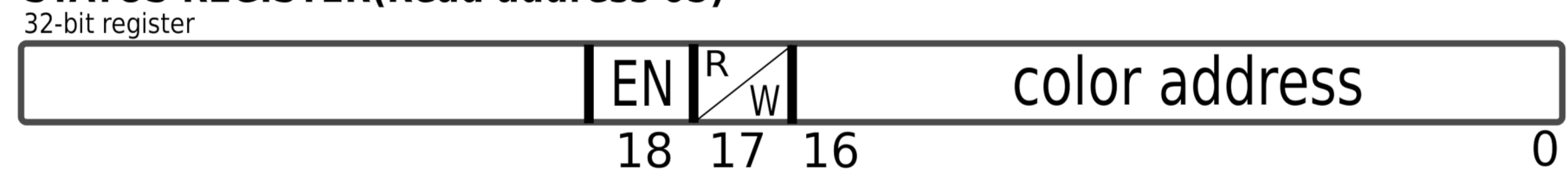


# Processor ARM

## AXI BUS



### STATUS REGISTER(Read address 05)



### READ DATA REGISTER(Read address 04)

