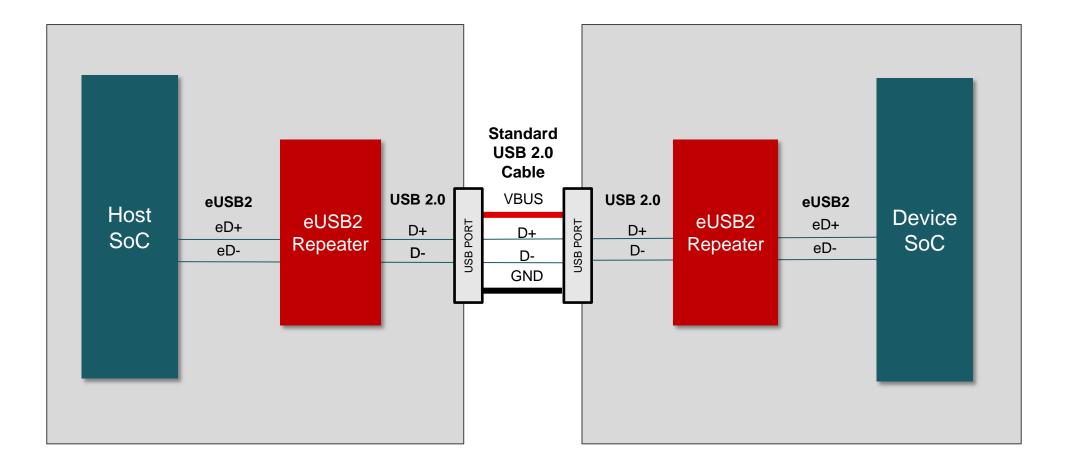
What is embedded USB 2.0 (eUSB2)? TI Precision Labs – USB

Prepared by Julie Nirchi

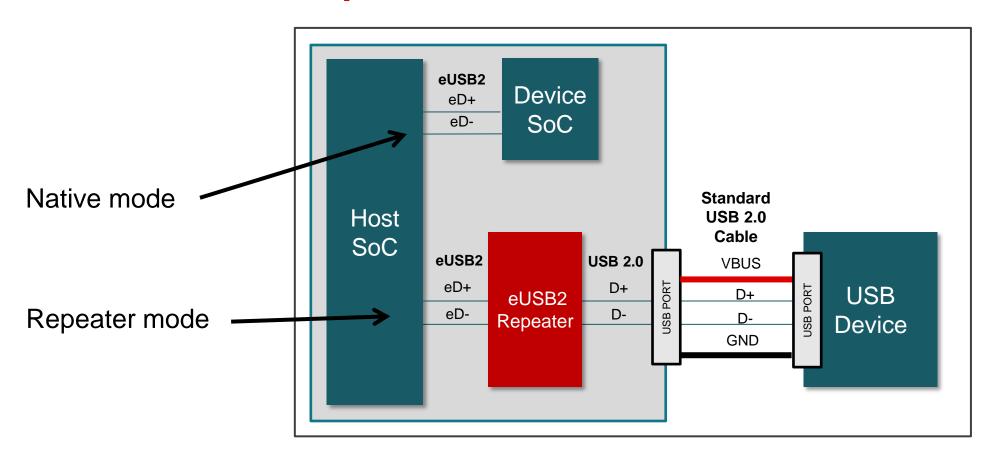
Presented by Nicholaus Malone



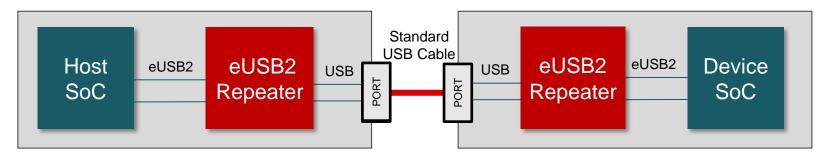
eUSB2 was created to bring USB 2.0 to small process nodes

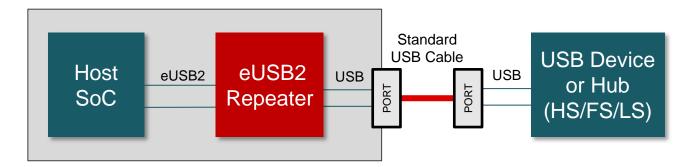


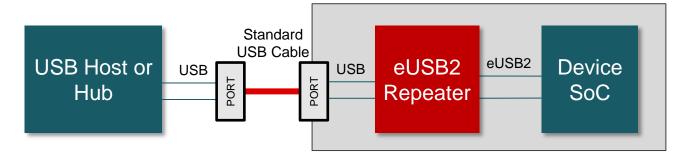
eUSB2 has two operational modes



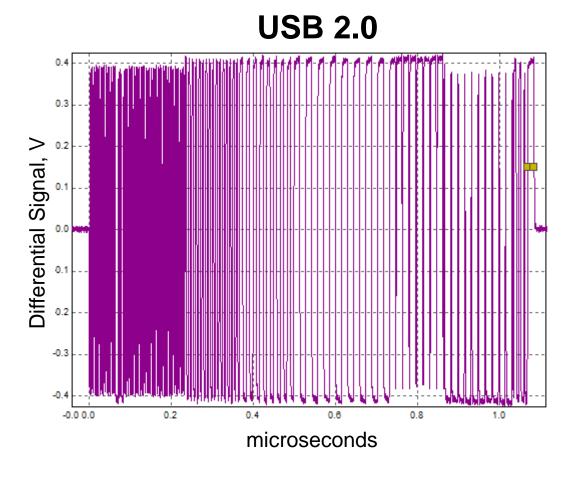
eUSB2 repeater mode examples



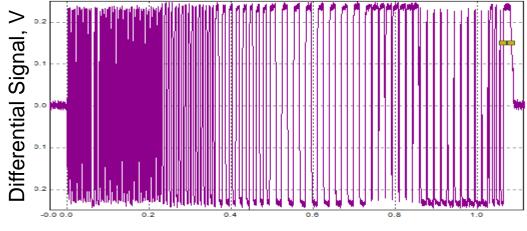




USB 2.0 and eUSB2 high-speed test packets

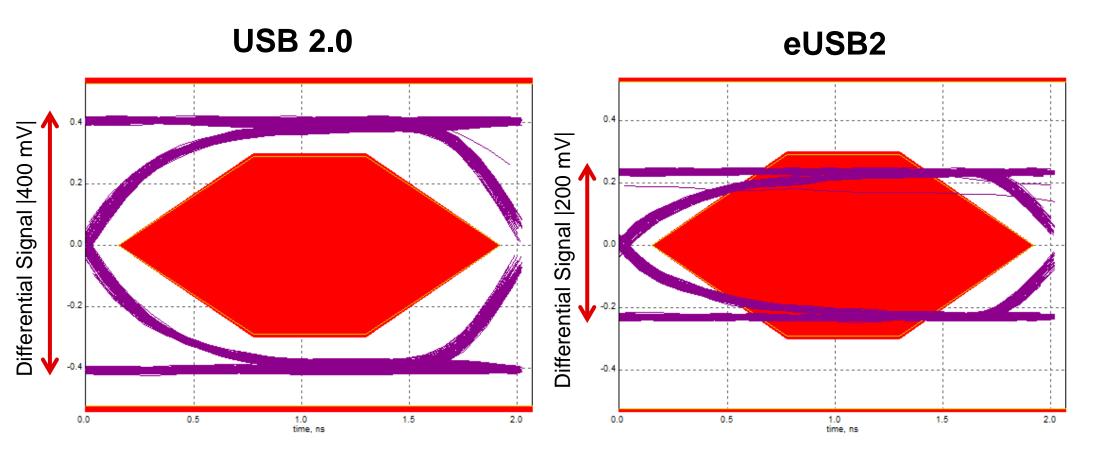


eUSB2

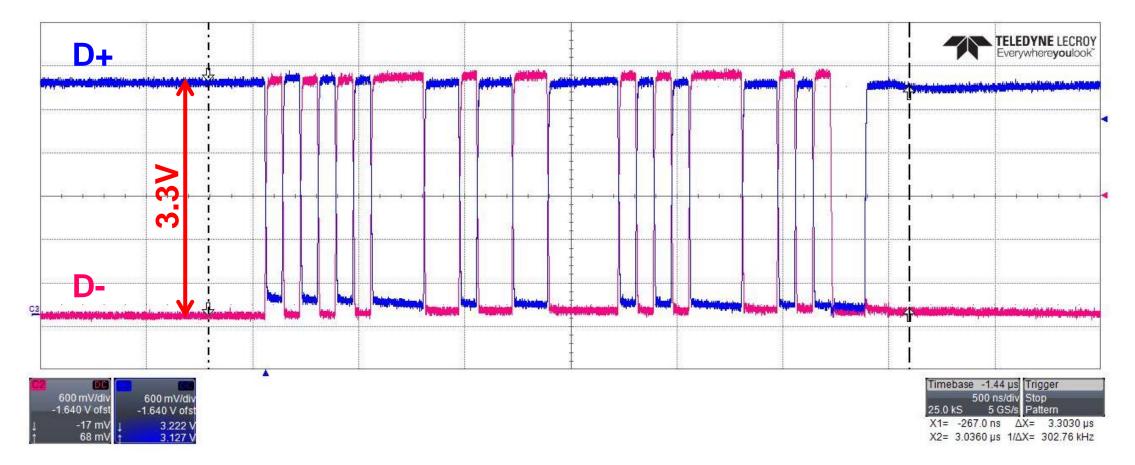


microseconds

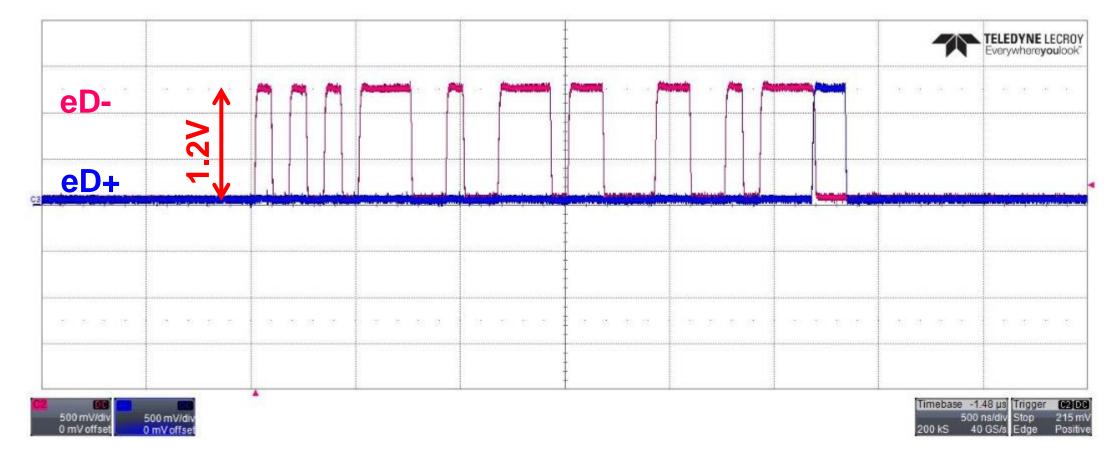
USB 2.0 and eUSB2 high-speed eye diagrams



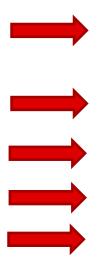
USB 2.0 full speed packet - differential



eUSB2 full speed packet – single ended

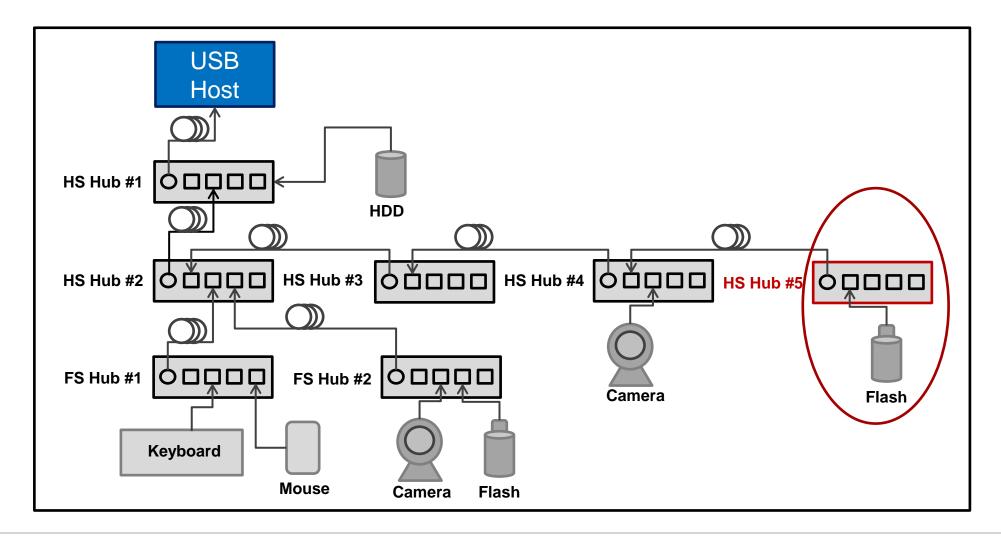


eUSB2 control messages - in-band signaling

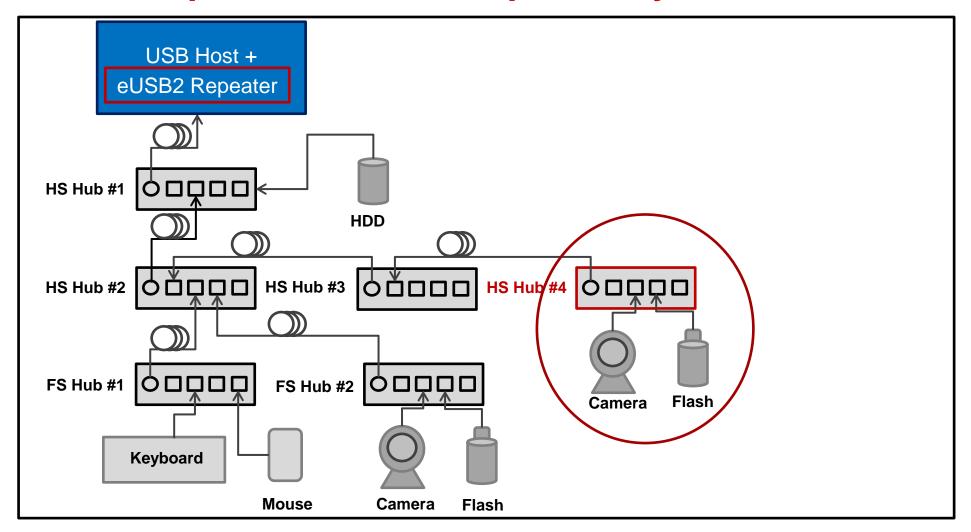


Control Message Name	Description	Usage Cases
CM.FS	HS Host: Enter FS terminations and	
	enter L1 power state	
	HS Peripheral: Enter FS	Repeater mode only
	terminations	
CM.L1	FS/LS: Enter L1 power state	
CM.L2	Enter lowest physical power state	Repeater mode only
CM.Reset	USB2.0 Bus Reset	Repeater mode only
	Enable HS terminations without	Repeater mode only, for compliance
CM.Test	enumeration (Test Mode)	use.
		Native mode / Repeater mode - to
CM.RAP	Start of register access	access register space.

USB 2.0 compliance and interoperability

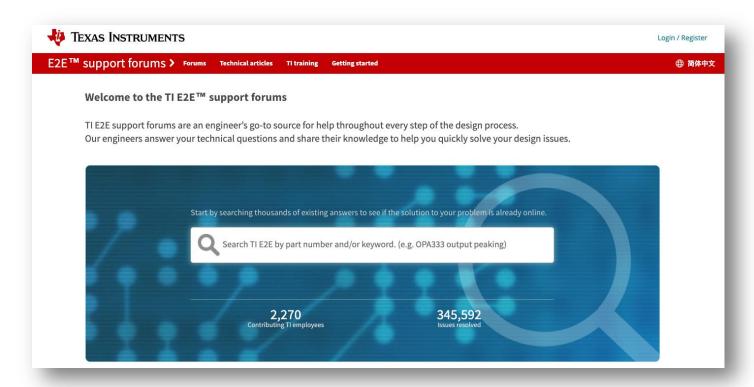


eUSB2 compliance and interoperability



Thank you

- <u>TI Precision Labs What is an Eye Diagram?</u>
- TI Precision Labs Layout Basics for USB Designs





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True or false: eUSB2 supports the same speeds as USB 2.0.





True or false: eUSB2 supports the same speeds as USB 2.0.

True. eUSB2 supports high speed, full speed and low speed operation.

True or false: eUSB2 can be connected to external connectors.



True or false: eUSB2 can be connected to external connectors.

False. eUSB2 is an inter chip interconnect and it requires an eUSB2 repeater to support external connectors.

True or false: eUSB2 repeaters are just level shifting redrivers.



True or false: eUSB2 repeaters are just level shifting redrivers.

False. eUSB2 repeaters not only level shift high speed signaling, they support differential to single ended translation for full speed and low speed as well as in-band signaling.

True or false: eUSB2 allows SoCs to use lower voltage IO cells.



True or false: eUSB2 allows SoCs to use lower voltage IO cells.

True. eUSB2 is designed to run at 1.2V or lower instead of 3.3V.