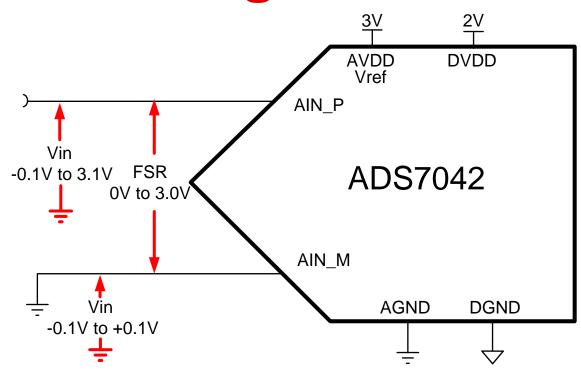
SAR ADC Input Types

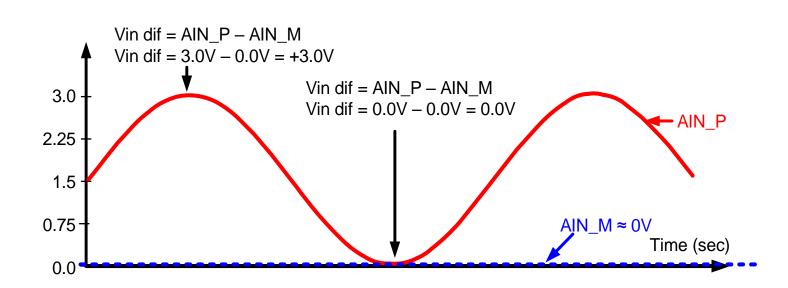
TIPL 4003 TI Precision Labs – ADCs

Created by Art Kay
Presented by Peggy Liska

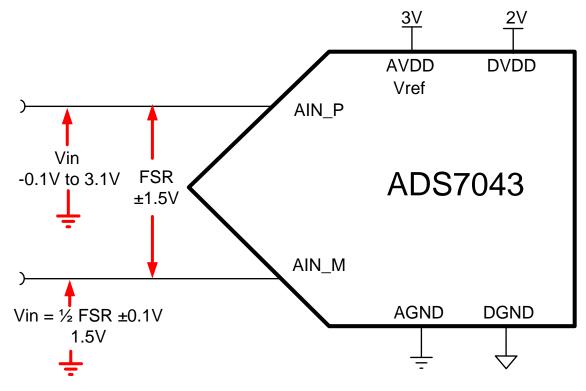


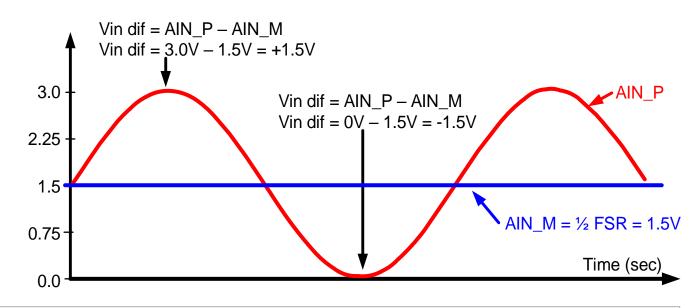
Single Ended



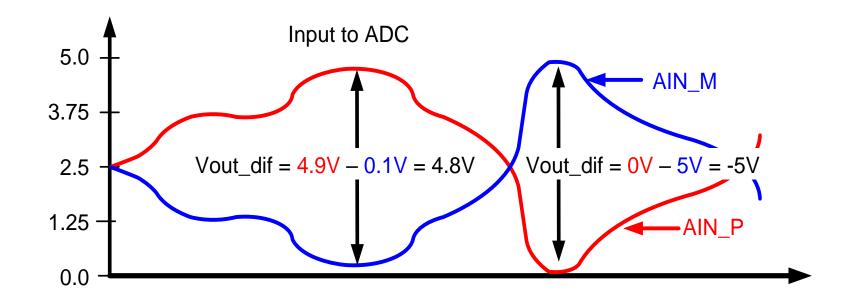


Pseudo-differential



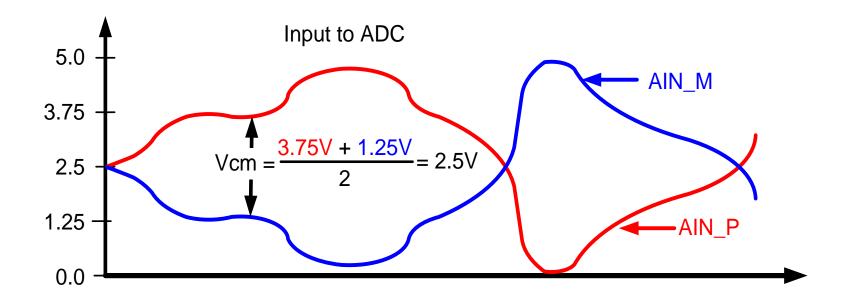


ADC Input Swing and Common Mode



$$V_{dif} = AIN_P - AIN_M$$

- Differential of ±5V (10Vpp)
- Single ended equivalent 0V to 5V (5Vpp)
- Double the range of single ended
- Negative differential output can occur when absolute output voltage of each output is positive (unipolar single supply)



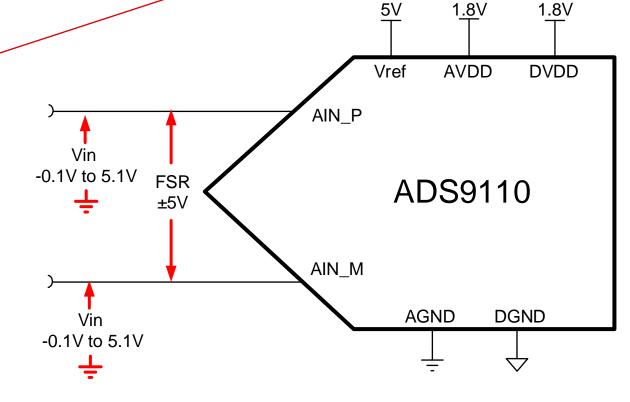
$$Vcm = \frac{AIN_P + AIN_M}{2}$$

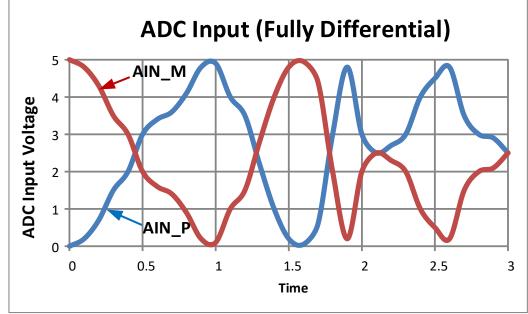
In this example common mode is always 2.5V

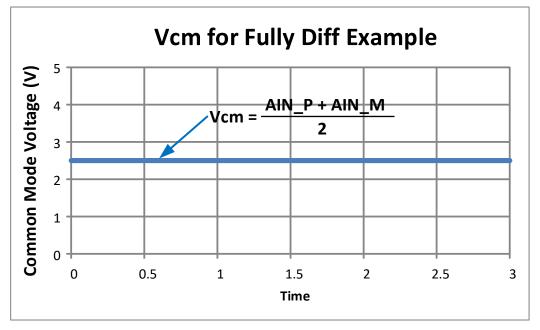
Fully Differential Input

PARAMETER 9110	MIN	TYP	MAX	UNIT				
ANALOG INPUT								
Full-scale input voltage span		-Vref		Vref				
Absolute Input voltage range	AIN_P to GND	-0.1		Vref + 0.1	V			
	AIN_M to GND	-0.1		Vref + 0.1				
Common-mode voltage range		(Vref/2)-0.1	Vref/2	(Vref/2)+0.1				
(AIN_P + AIN_M)/2			7					

Vcm must be constant at Vref/2



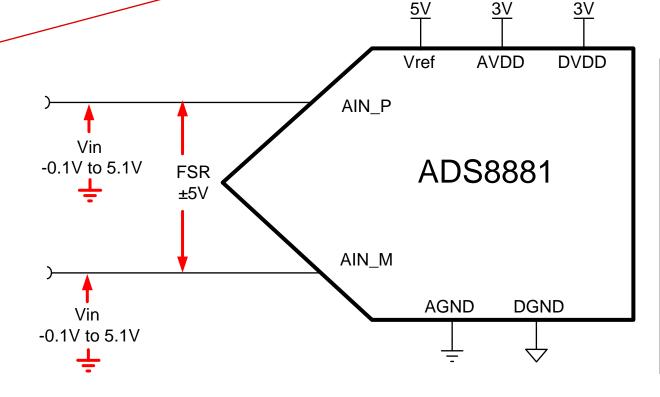


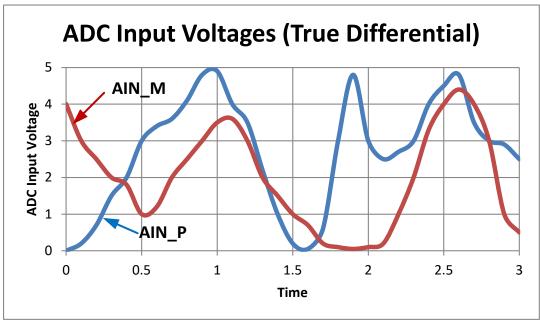


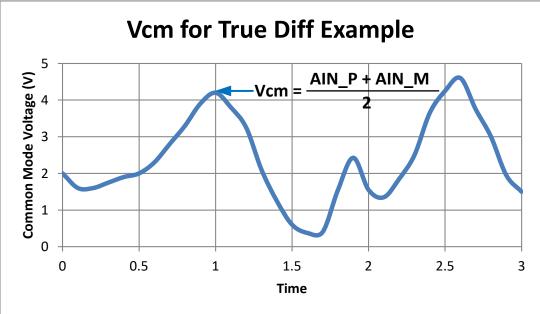
True Differential Input

PARAMETER ADS8881	MIN	TYP	MAX	UNIT				
ANALOG INPUT								
Full-scale input voltage span		-Vref		Vref				
Absolute Input voltage range	AIN_P to GND	-0.1		Vref + 0.1	V			
	AIN_M to GND	-0.1		Vref + 0.1				
Common-mode voltage range (AIN_P + AIN_M)/2		0.0	Vref/2 ₹	Vref				

Vcm has a wide voltage range.



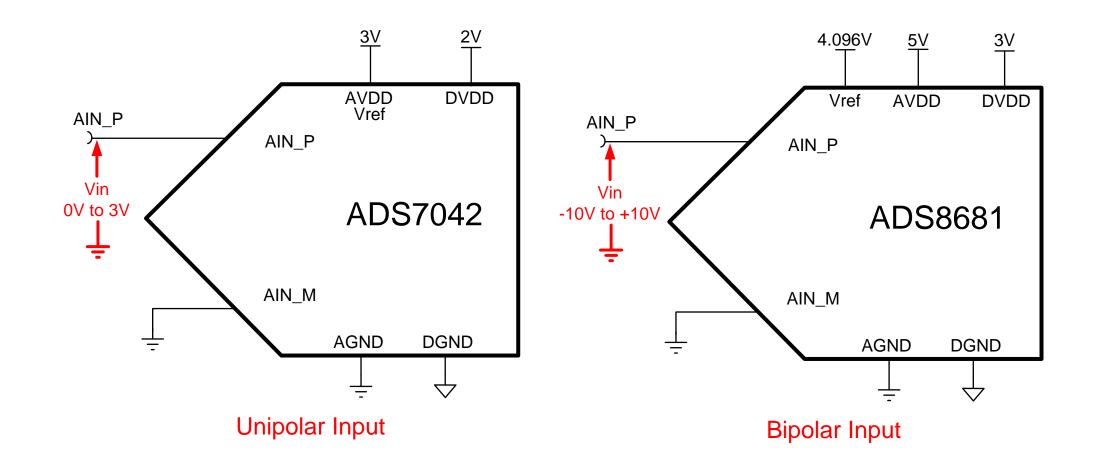




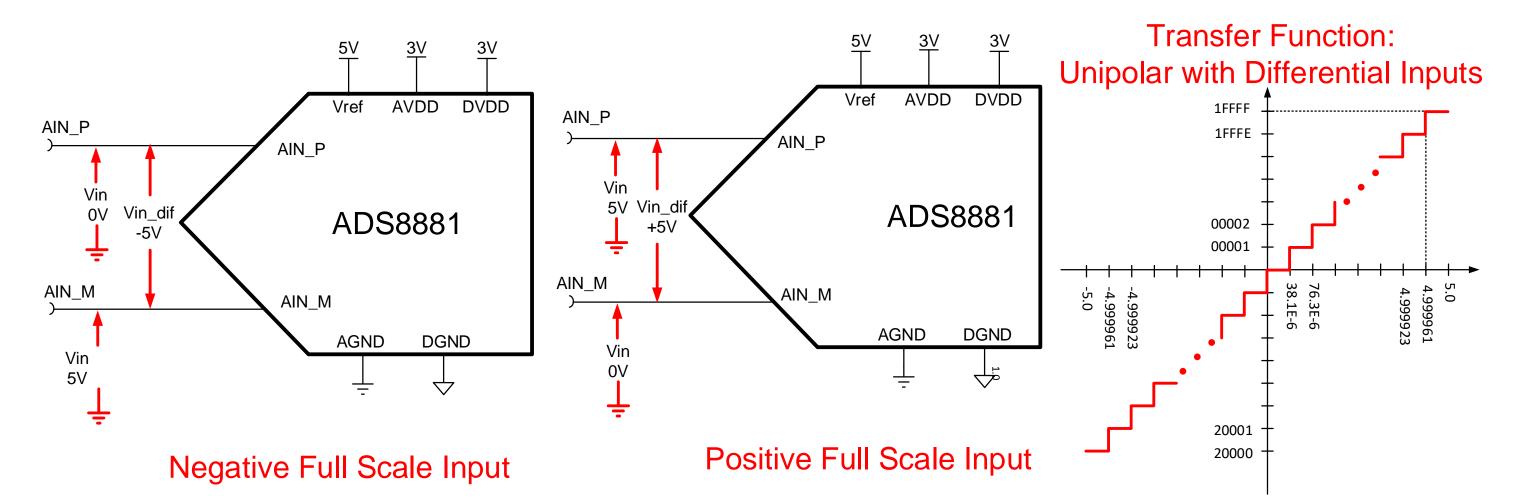
Summary of ADC input types

Input Type	Vref	Ain_P	Ain_M	Vcm	FSR	Example 16 Bit Output
Single Ended	5V	0V to 5V	GND (+/- 100mV)	n/a	0V to 5V	0000 = 0V FFFF = FSR = 5V
Pseudo	5V	0V to 5V	Set ½ * Range (2.5V)	n/a	-2.5V to +2.5V	8000 = -2.5V
Differential	O V		Oct /2 Range (2.0v)	11/4	2.5 0 10 12.5 0	7FFF = +2.5V
Fully	5V	0V to 5V	0V to 5V	Set ½ * Range	-5V to +5V	8000 = -5V
Differential				(2.5V)		7FFF = +5V
True Differential	5V	0V to 5V	0V to 5V	Can vary from –FS to +FS	-5V to +5V	8000 = -5V 7FFF = +5V

Unipolar vs Bipolar



Unipolar with Differential Input



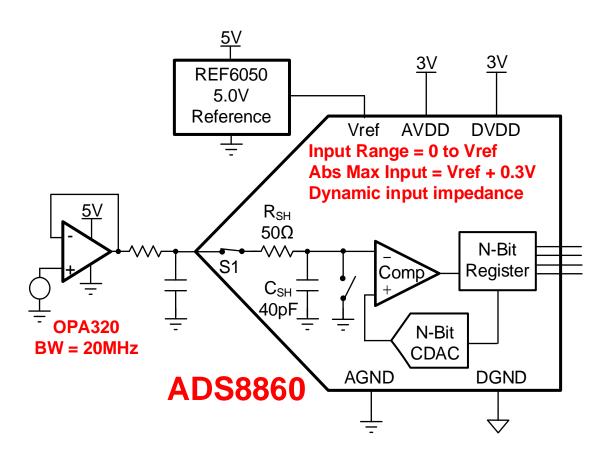
$$Vin_dif = AIN_P - AIN_M$$

 $Vin_dif = 0V - 5V = -5V$

Vin_dif = AIN_P - AIN_M
Vin dif =
$$5V - 0V = +5V$$

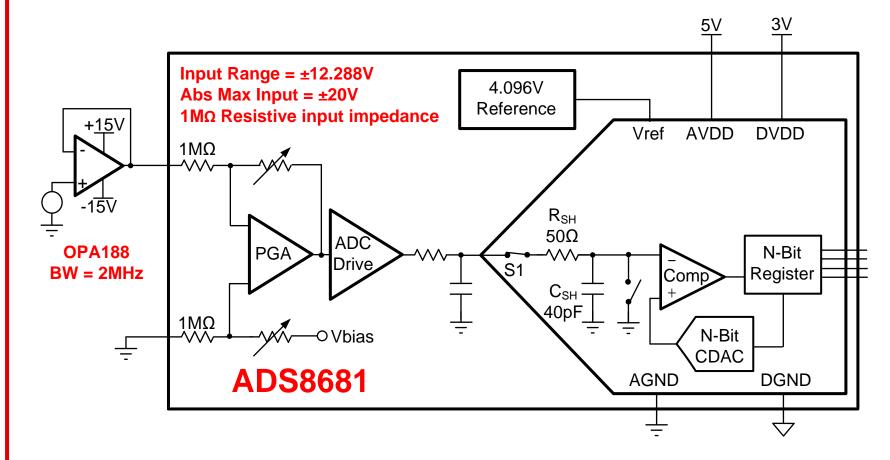
Input Impedance: Resistive vs. Switched Capacitor

Switched Capacitor Input



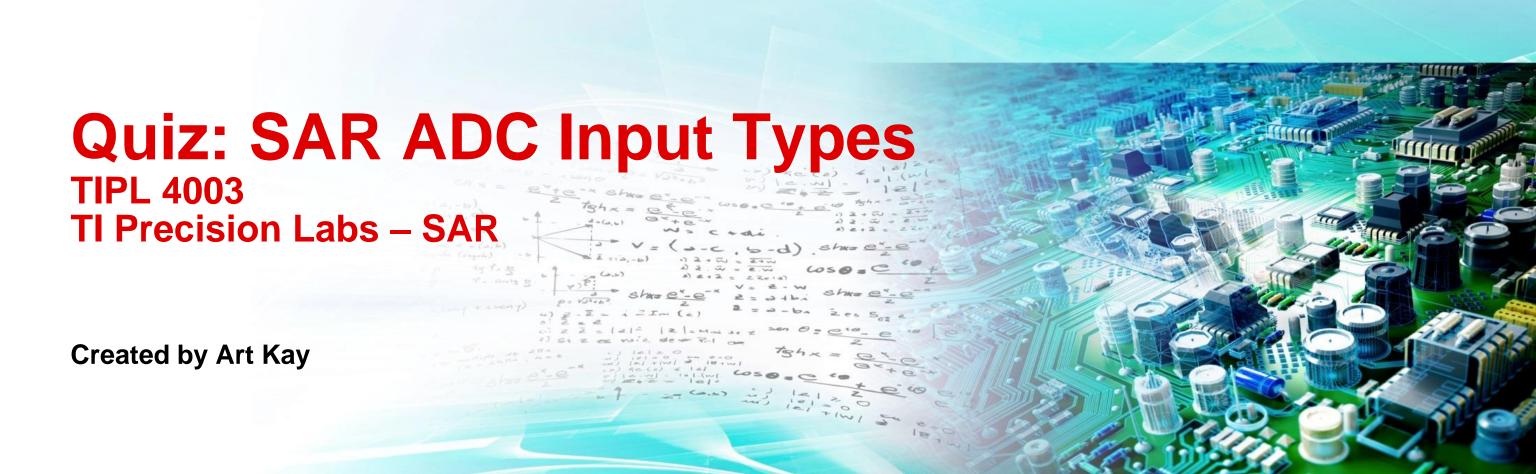
- Wide bandwidth external amp required
- Dynamic input impedance
- Input voltage range set by reference

Resistive, High Voltage, PGA input

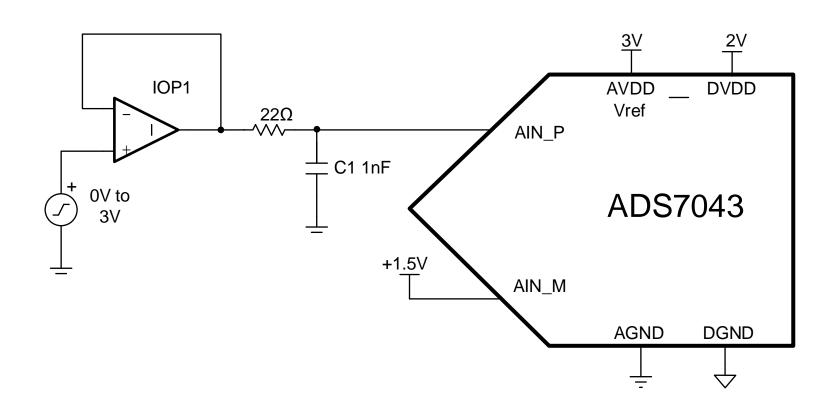


- External amplifier bandwidth not critical
- Internal PGA, ADC driver, and reference
- High voltage input (±12.288V) with 5V supply

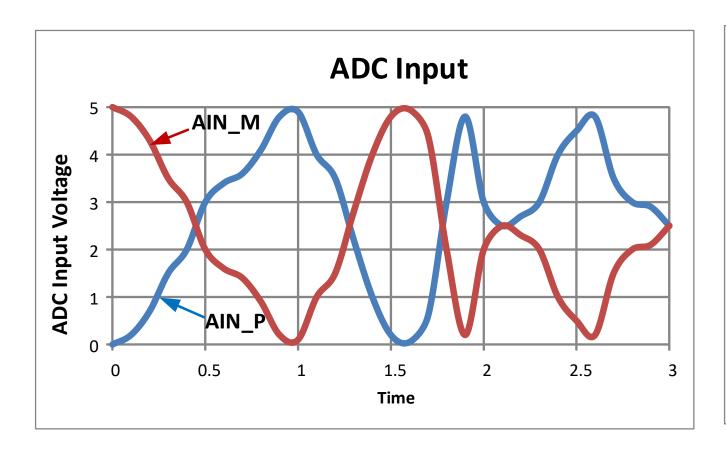
Thanks for your time! Please try the quiz.

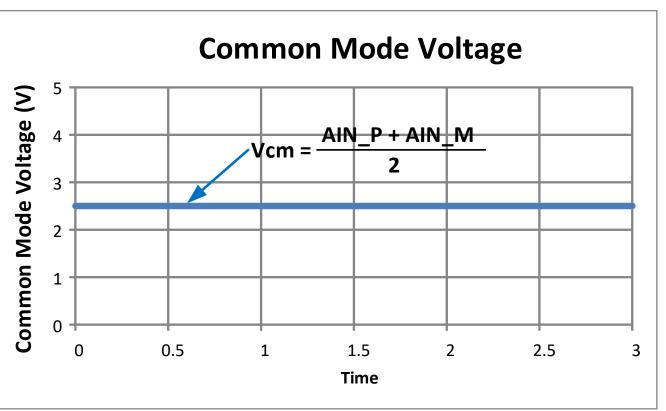


- 1. The circuit in the figure below is a _____.
 - a. Single ended ADC.
 - b. Pseudo-differential ADC.
 - c. Fully Differential ADC.
 - d. True Differential ADC.

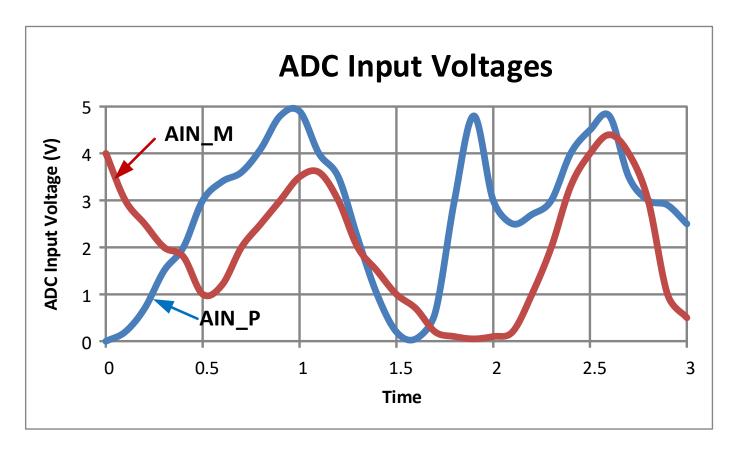


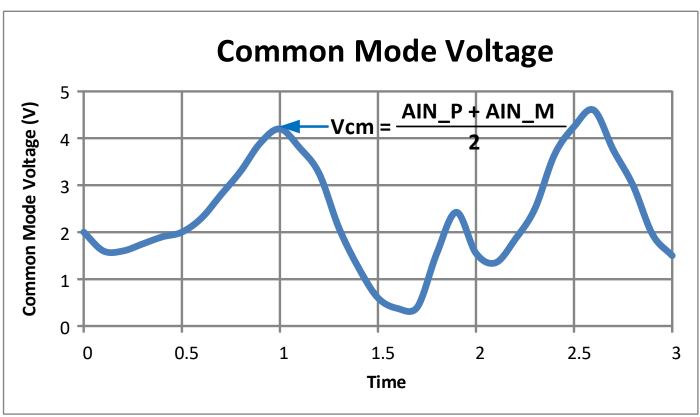
- 2. (T/F) The signal below can be directly applied to an ADC with a fully differential input.
 - a. True.
 - b. False.





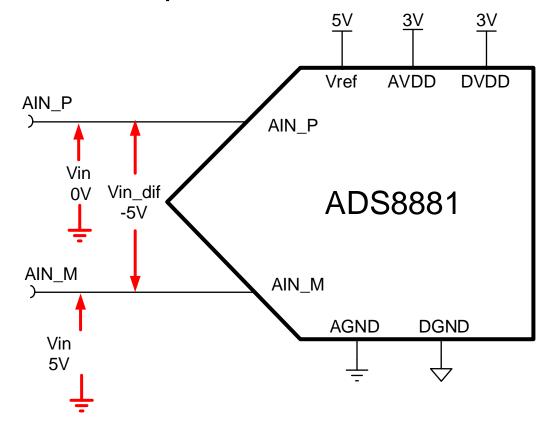
- 3. (T/F) The signal below can be directly applied to an ADC with a fully differential input.
 - a. True.
 - b. False.



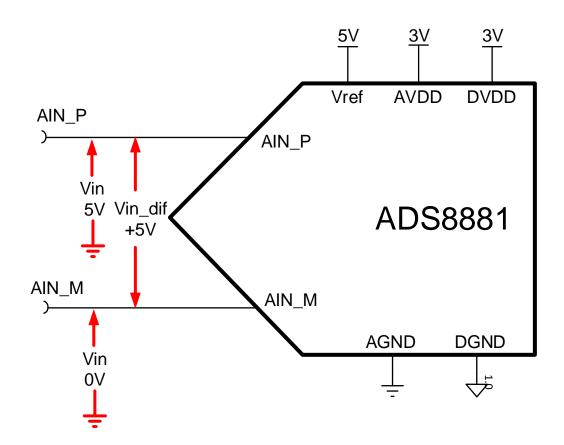


- 4. Which type of ADC would require a wide bandwidth amplifier and a RC charge bucket filter?
 - a. A high voltage SAR with an internal PGA.
 - b. A switched capacitor input SAR ADC.
- 5. (T/F) In general, a fully differential input has double the range of a single ended input assuming the same reference voltage.
 - a. True.
 - b. False.

- 6. The circuit in the figure below has a _____.
 - a. Unipolar, differential input.
 - b. Bipolar, differential input.



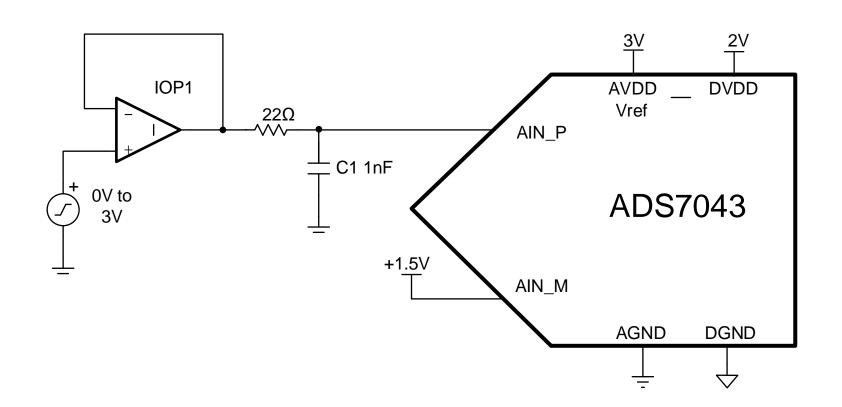
Negative Full Scale Input



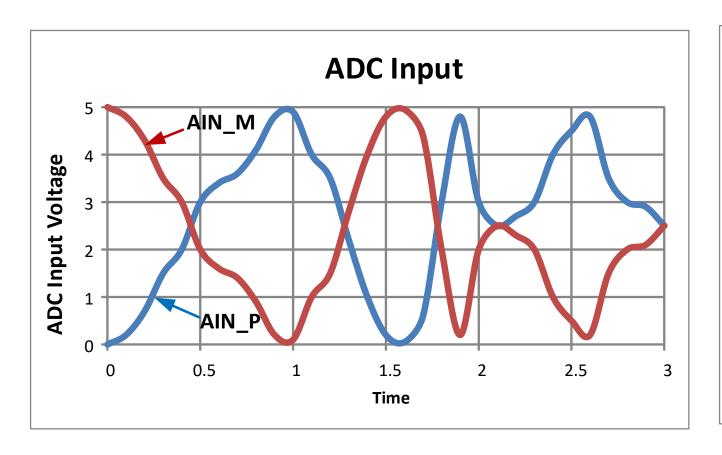
Positive Full Scale Input

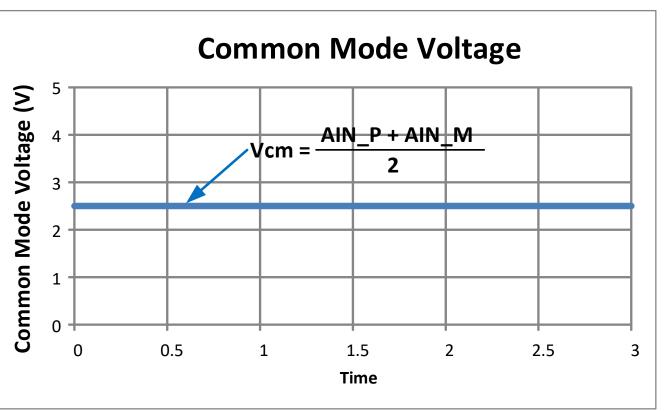
Solutions

- 1. The circuit in the figure below is a _____.
 - a. Single ended ADC.
 - b. Pseudo-differential ADC.
 - c. Fully Differential ADC.
 - d. True Differential ADC.

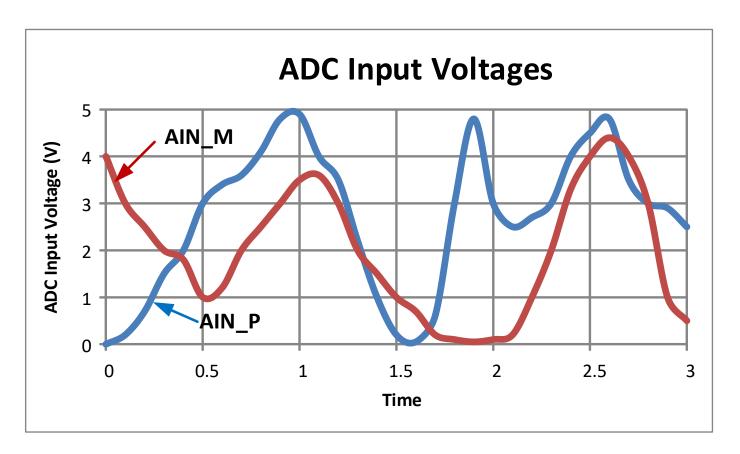


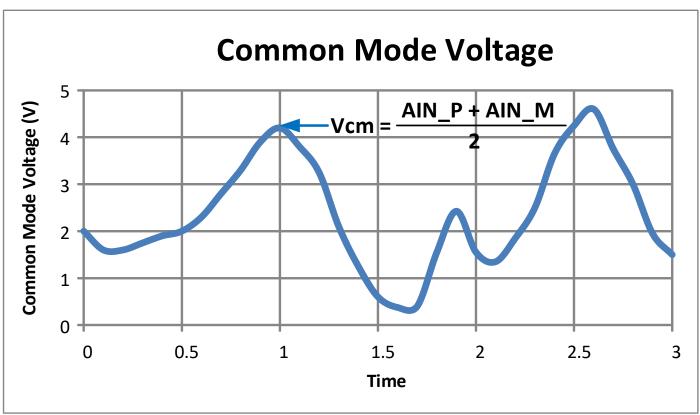
- 2. (T/F) The signal below can be directly applied to an ADC with a fully differential input.
 - a. True.
 - b. False.





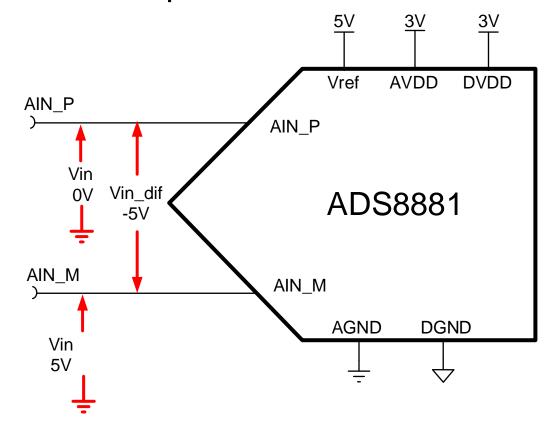
- 3. (T/F) The signal below can be directly applied to an ADC with a fully differential input.
 - a. True.
 - b. False.



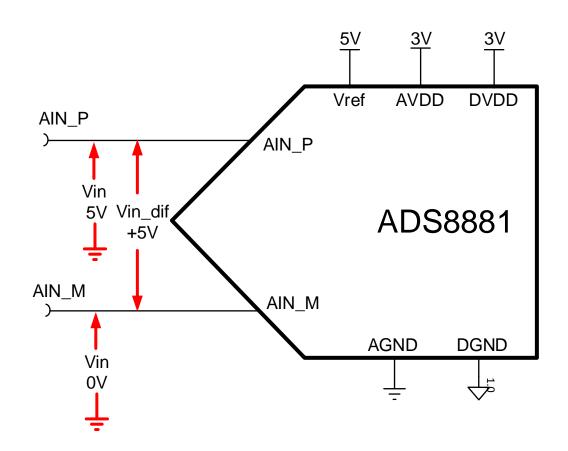


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 - b. Bipolar, differential input.



Negative Full Scale Input



Positive Full Scale Input