University of Engineering and Management
Institute of Engineering & Management, New Town Campus
Department of Computer Science & Engineering

Computer Organization & Architecture Laboratory
PCCCS492

EXPERIMENT NO.:

TITLE: Realization of Boolean Expressions Using Basic Gates (IC Chips).

OBJECTIVE: To realize of Boolean Expressions Using Basic Gates (IC Chips).

THEORY:

Logic expressions:

Using Basic Gates (IC Chips).

OBJECTIVE: To realize of Boolean Expressions Using Basic Gates (IC Chips).

THEORY:

Logic expressions:

i) $Y = A'B + C$

ii) $Y = AB' + C(A'+D)$

LOGIC DIAGRAM:

i)

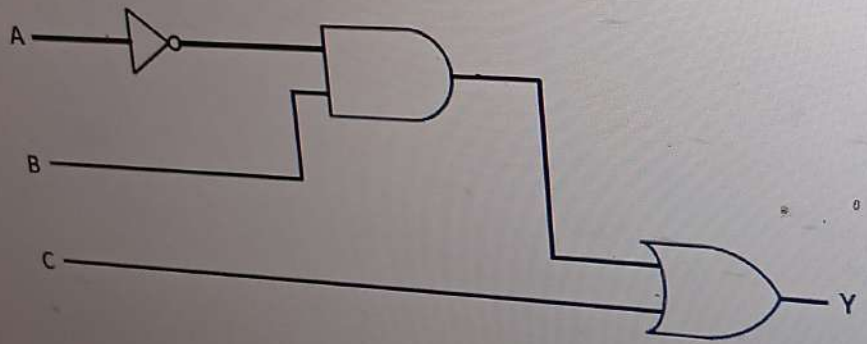

Fig : Logic Diagram for the expression of $Y = A'B + C$

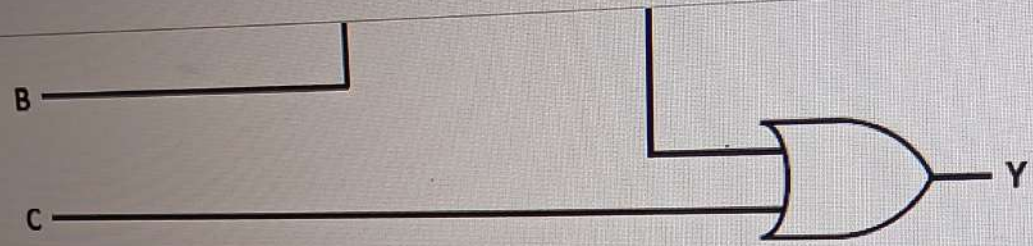

Fig : Logic Diagram for the expression of $Y = A'B + C$

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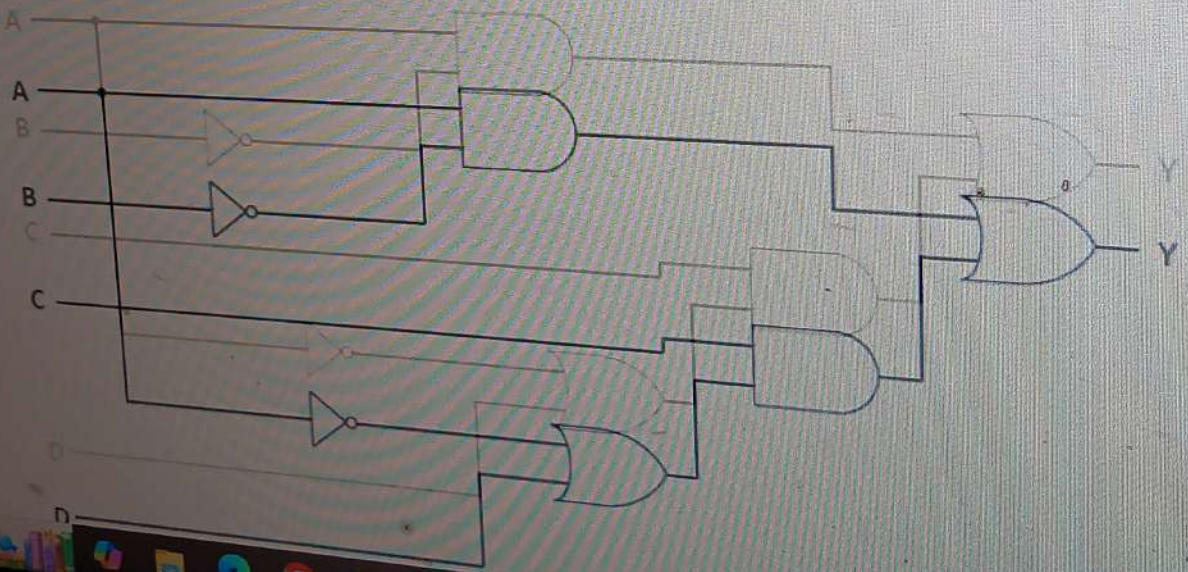
ii)



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ii)

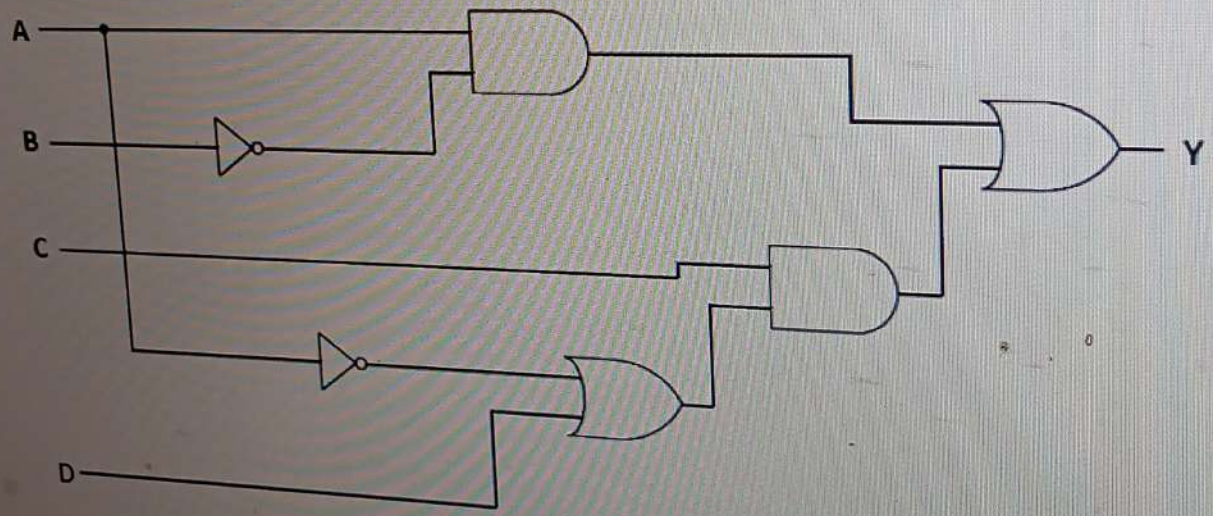

Fig : Logic Diagram for the expression of $Y = AB' + C(A'+D)$

INSTRUMENT & COMPONENT REQUIRED:

ii)

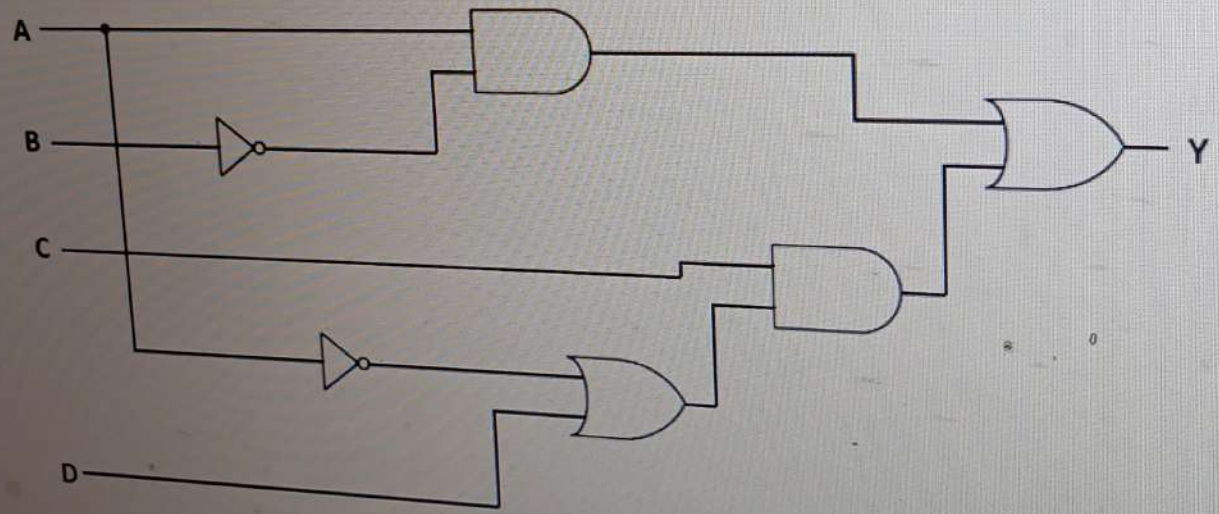

Fig : Logic Diagram for the expression of $Y = AB' + C(A'+D)$

INSTRUMENT & COMPONENT REQUIRED:

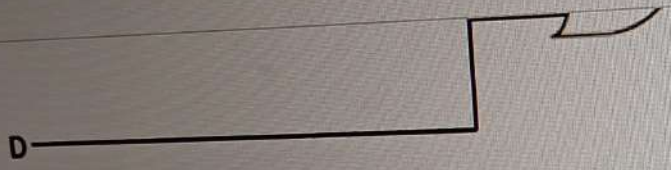

Fig : Logic Diagram for the expression of $Y = AB' + C(A'+D)$

INSTRUMENT & COMPONENT REQUIRED:

Sl. No.	Item	Specification	Qty.

VERIFICATION TABLE:

i) for the expression of $Y = A'B + C$

Input			Output
A	B	C	Y

ii) for the expression of Y = AB' + C(A'+D)

Input				Output
A	B	C	D	Y

CONCLUSION:

CONCLUSION: