Assignment - 1	0
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OIX Continue al 11 1	
8.1) Familiarization of Handware assembling for a or	igital.
Ans: - Digital handware assembly theory involves unders	fanding digital
logie, Boolean algebra, number systems logie cir	cuit, negisters,
microprocessors, circuit design using HDLs, clocking	synchronization.
Ans: - Digital handware assembly theory involves underest logie, Boolean algebra, number systems logic circuit design using HDLs, clocking FSMs, DSP, testing, and components. Learn the E	vasies before
practical assembly emphasizing logic gates, memon	y and sequential
W/CMT3	
Esmiliaguization of hardware assembling fun a digitale a computer, here's basic gate guide!	device, such as
a coumputer, here's basic gate quide!	
1. Components:	
· Processon (CPU): The brain of the computer	
· Mother hourd: The main circuit board connection	ng all components
· Mother hand: The main cineuis board connection · RAM (Random Access Memory): Tempory storage for	go na gunning
· Storage (HDD/SSD): Permanent storage for the	-1.00 / /
1 11	operating system
and data.	, /
Tower Supply Unit (PSU). Supplies power to the	Components.
· Graphics (and (GTV): Handles graphies proces	sing.
· Cooling System: Reeps components from overhear	ing.
· Powen Supply Unit (PSU): Supplies power to the one of the Graphies Cand (GPU)! Handles graphies process of Cooling System: Reeps components from overhead of Casing! Encloses and protects the components.	V
2. Took:	
· Serewarivers (Phillips and flathead).	
· Andistatic wrist stap strap to prevent static elec	micity damage.
· Cable ties for cable management.	0 0
· Theamal paste for the CPU.	

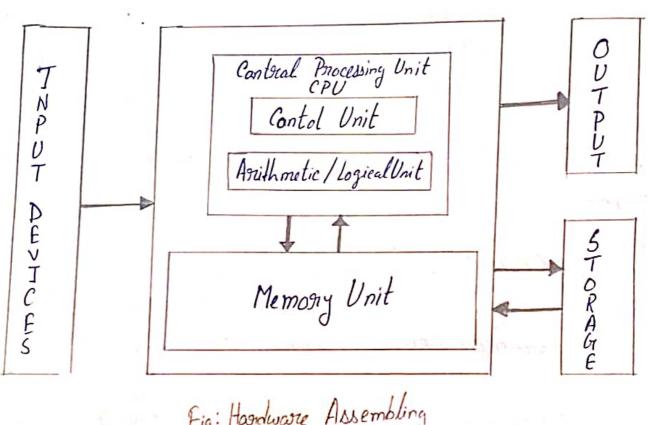


Fig: Handware Assembling

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3. Assembly Steps:	
· Propore the Workpace: Work on a clean, well-lit	and datic-free
swiface.	, con o Sport o
· Install CPU: Align and insert the CPU into the mother of the Mount RAM: Intall RAM into the designated slots · Install Stonage: Connect HDD/SSD to the mother back · Install GIPU: If you have a separate graphics car into the PCI slot.	en board socket.
· Mount RAM: Intall RAM into the designated slots	
· Install Stonage: Connect HDD/SSD to the motherhood	and.
· Install GIPU: If you have a separate graphics cas	rd install it
into the PCI slot.	
Connect Power Supply: Attach power rubles to the	mother bownd.
CIU, OIIU, and Agrical	
· Connect Front Panel Cables: Connect power button,	neset button, LEDs
and USB posts to the mether board.	
· Install Cooling System: Attach the CPU cooler as	dany additional
case forms.	A2
· Double-check Connections: Ensure all componer	to are securely
Connections.	0
· Cable Maragement: Organize and secure cables	for good
ainflow are assthetics.	V
· Close the Case: Securely close the computer	case.
4. RINS/HEET CHILL!	
· Poiges on the computer and enter the BIOS/L	VEF/.
· Configure boot onder, Later, time, and other	se Hirys.
· Pougest on the computer and enter the BIOS/L · Configure be of order, Later, time, and other 5. OPerating System Trustallation:	U
Insent the OS installation media.	
· Follow the on-sorein instructions to ind install	the operating
	7

Topic Date: / 7. Select -> Preferred Language [VHDL] and Select -> Next. 8. Cliek on Now Sowice 3. Select -> NHDL Module 10. Click > Next 11. Click > Finish 12. Click - Next - Finish 13. Now program window willopen and elick on Synthesis -XST. 14. For simulation, test bench is required. Right click on program file in "Sowice - Pane" and click on "New Sowice".

15. Give a file name and click on VHDL Test Bench. Click > Noxt. 16. Click → Next → Finish.

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8.3) Familiarization of HDL & Xilinx TSE Simulator.	
Ans: - THardware Description Language (HDL) is a specialized programming	
language for describing the structure and behavior of digital cines	<u>/</u>
Commonly used HDLs, like Verilog and VHDL, enable engineers to mod	1.0,
electronic system at various abstraction levels, from basic gates to	
Complex modules. HDL facilitates simulation, allowing designers to verify	
functionality before physical implementation. It plays a crucial grole	: 11
desgin and synthesis of digital circuit, supporting the oceation of scale	16
efficient, and ennon-free desigons. HDL rock can be synthesized to ge	nerata
nettiste from FPGA on ASIC devices contributing to the development of adv	anal
electronics and integrated circuits.	
Wiking ISE is a software took produced by Xilinex for synthesis and	,
analysis of HLD design which enable the developer to synthesize their design	
performing timing analysis, examine RTL diagrams, simulate a design with	1/
different set of data and configure the target device with the program	n
different set of data and configure the target device with the program 5-teps nequired from Xilinx IIS Simulaton:	,
1. Double click on icon Xilinx 9.1;	
2. Press OK.	
3. Go to FILF → New Project.	
4.(i) Put a Project Name and assign a Project Location	
4.(i) Put a Project Name and assign a Project Location (ii) Top-level Source Type must be HDL.	
→ Noxt.	
5. Select > Family, Device, Package, Speed as per avaliable Xiling R	and
5. Select → Family, Device, Package, Speed as per avaliable Xilinx B. 6. Select > Simulator [ISE Simulator (VHDL/Verilag)].	
	-

	PIONEER
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System.	
(Day of T 1 1/1 /:	
· Install Soiveres for mothershood GPU a 1 1	1
Install drivers for motherboard, GPU, and other T. Testing:	il Components.
· Power on the suder and clark has	ora o made
· Bun exclem disconcilia 1 / 1 / for any E	toton messages.
· Power on the system and check for any es · Run system diagnostic tools to ensure everythe conceptly.	ing is furetioning
The specific andelines constal in the	- 000/ 0.1 /
The specific quidelines provided in the manuals for and refer to any additional nesources available. If a specific type to digital device, place provide more more tailored quide.	neach component
a specific Libe to divided dies blow it	111.
more failored guide.	districts for a
with July	
8.2) Familiarization of SPEC Benchmark Application	fon CPU.
Ans: - 5PEC (Standard Performance Evaluation Compose	wion) Benchmark
Applications involves understanding industy-standard	bertumanee Lest
SPEC benehman Rs measure be porformance of competer.	51stems and
components. Explore Specific benchmarks like SPECint, Si	PECE SPECium
components. Explore Specific benchmarks like SPECint, Si and SPECivels to evalute CPU, floating point, Java, and we	b seven partenmence
nespectively.	1-7
5PEC (PU Benchmanks: . 5PEC provides two main CPU be no	hmark suites:
SPECint (fun integer performance) and SPECffe fun floating of SPECint: Measures integer performance, simulating of integer -based or obstations.	g-point performed
SPECint: Measures integer performance, simulating of	polications with
integer-based remembers.	r/ -
· Repriesents a narge of neal-world awaklands.	including scientific.
financial, and media application.	U