

Curriculum Vitae

Shoaib Akram
Australian National University
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Education

Ph.D. Computer Science Engineering, Ghent University, 2019.
Advisor: Lieven Eeckhout
Thesis Topic: Exploiting Managed Language Semantics to Optimize for Hardware Heterogeneity

M.S. Electrical and Computer Engineering, University of Illinois at Urbana-Champaign, 2009.
Advisor: Deming Chen
Thesis Topic: Workload Adaptive Shared Memory Multicore Processors with Reconfigurable Interconnects

B.Sc. Electrical Engineering, University of Engineering & Technology, Lahore, Pakistan, 2006.
Advisor: Shahid H. Bokhari
Thesis Topic: Implementation of Suffix Trees on FPGAs

Research Interests

Computer architecture; Memory and storage systems; Performance analysis

Employment

January 2020 - Current

Lecturer (Assistant Professor) at The Australian National University

July 2019 - December 2019

Post-doctoral Researcher at PerfLab - Ghent University

July 2012 - June 2019

Ph.D. student at PerfLab - Ghent University
Research Focus: Computer architecture, runtime systems, memory management

March 2010 - June 2012

Junior Researcher at FORTH-ICS, Greece (with Angelos Bilas)
Research Focus: Storage systems

March 2006 - May 2007

Research Associate, Al-Khwarizmi Institute of Computer Science (KICS), Lahore

Memberships

- Professional Member, Association for Computing Machinery (ACM)

Refereed Publications

Journals

1. **S. Akram**, “Performance Evaluation of Intel Optane Memory for Managed Workloads,” ACM Transactions on Architecture and Code Optimization (TACO), 2021.
2. W. Liu, **S. Akram**, and L. Eeckhout, “Reliability-Aware Garbage Collection for Hybrid DRAM-HBM Memories,” ACM Transactions on Architecture and Code Optimization (TACO), 2021.
3. S. Pestel, S. Steen, **S. Akram**, and L. Eeckhout, “RPPM: Rapid Performance Prediction of Multithreaded Applications on Multicore Hardware,” IEEE Computer Architecture Letters (CAL), 2018.
4. **S. Akram**, J. Sartor, and L. Eeckhout, “DEP+BURST: Online DVFS Performance Prediction for Energy-Efficient Managed Language Execution,” IEEE Transactions on Computers (TC), 2017.
5. **S. Akram**, J. Sartor, K. Van Craeynest, W. Heirman, and L. Eeckhout, “Boosting the Priority of Garbage: Scheduling Collection on Heterogeneous Multicore Processors,” ACM Transactions on Architecture and Code Optimization (TACO), 2016.
6. **S. Akram**, A. Papakonstantinou, R. Kumar, D. Chen, “S. Akram, A. Papakonstantinou, R. Kumar, D. Chen, “A Workload-adaptive and Reconfigurable Bus Architecture for Multicore Processors,” International Journal of Reconfigurable Computing (IJRC), 2010.

Conferences

1. **S. Akram**, “Exploiting Intel Optane Persistent Memory for Full Text Search,” ACM SIGPLAN International Symposium on Memory Management (ISMM), 2021. [Acceptance Rate: 8/14](#)
2. S. Pestel, S. Steen, **S. Akram**, and L. Eeckhout, “RPPM: Rapid Performance Prediction of Multithreaded Workloads on Multicore Processors,” IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS), 2019. [Acceptance Rate: 26/88](#)
3. **S. Akram**, J. Sartor, K. McKinley and L. Eeckhout, “Emulating and Evaluating Hybrid Memory for Managed Languages on NUMA Hardware,” IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS), 2019. [Acceptance Rate: 26/88](#)
4. **S. Akram**, J. Sartor, K. McKinley and L. Eeckhout, “Crystal Gazer: A Profile-Driven Garbage Collector to Manage Hybrid Memories,” ACM International Conference on Measurement and Modeling of Computer Systems (SIGMETRICS), 2019. [Acceptance Rate: 50/317](#)
5. **S. Akram**, J. Sartor, K. McKinley and L. Eeckhout, “Write-Rationing Garbage Collection for Hybrid Memories,” ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI), 2018. [Acceptance Rate: 55/245](#)
→ *NVMW Memorable Paper Award*
6. **S. Akram**, J. Sartor, and L. Eeckhout, “DVFS Performance Prediction for Managed Multi-Threaded Applications,” IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS), 2016. [Acceptance Rate: 27/77](#)
→ *Best Paper Nomination*

7. K. Van Craeynest, **S. Akram**, W. Heirman, A. Jaleel, and L. Eeckhout, "Fairness-aware Scheduling on Single-ISA Heterogeneous Multicores," International Conference on Parallel Architectures and Compilation Techniques (PACT), 2013. [Acceptance Rate: 36/208](#)
8. **S. Akram**, M. Marazakis, and A. Bilas, "Understanding Scalability and Performance Requirements of I/O-intensive Applications on Future Multicore Servers," IEEE International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (MASCOTS), 2012. [Acceptance Rate: 49/134](#)
9. **S. Akram**, M. Marazakis, and A. Bilas, "Understanding and Improving the Cost of Scaling Distributed Event Processing," ACM International Conference on Distributed Event-Based Systems (DEBS), 2012. [Acceptance Rate: 17/103](#)
10. **S. Akram**, R. Kumar, D. Chen, "Workload Adaptive Shared Memory Multicore Processors with Reconfigurable Interconnects," IEEE Symposium on Application Specific Processors (SASP), 2009.

Workshops

1. **S. Akram** and A. Bilas, "A Sleep-based Communication Mechanism to Save Processor Utilization in Distributed Streaming Systems," Computer Architecture and Operating System Co-design (CAOS), held alongside HiPEAC, 2011.
2. **S. Akram**, M. Marazakis, and A. Bilas, "NUMA Implications for Storage I/O Throughput in Modern Servers," Computer Architecture and Operating System Co-design (CAOS), held alongside HiPEAC, 2012.
3. **S. Akram**, M. Marazakis, and A. Bilas, "Energy Inefficiency of Operating System Layers for Data-centric Infrastructures," Systems for Future Multi-core Architectures (SFMA), held alongside EuroSys, 2012.
4. **S. Akram**, K. McKinley, J. Sartor, and L. Eeckhout, "Managing Hybrid Memories by Predicting Object Write Intensity," Programming Across the System Stack (PASS), held alongside <programming>, 2018.
5. **S. Akram**, "To Expose, or Not to Expose, Hardware Heterogeneity to Runtimes!" Modern Language Runtimes, Ecosystems, and VMs (MoreVMs), held alongside <programming>, 2019.

Invited Publications

1. **S. Akram**, J. Sartor, K. McKinley and L. Eeckhout, "Kingsguard: Write-Rationing Garbage Collection for Hybrid Memories," Annual Non-Volatile Memories Workshop (NVMW), 2019.
2. **S. Akram**, S. Cromar, G. Lucas, A. Papakonstantinou, and D. Chen, "VEBoC: Variation and Error-Aware Design for Billions of Devices on a Chip," IEEE/ACM Asia and South Pacific Design Automation Conference (ASPDAC), 2008.

Invited Talks

1. "Exploiting Managed Language Semantics to Mitigate Wear-Out in Persistent Memory," Flash Memory Summit, 2019, Santa Clara.

2. "Exploiting Managed Language Semantics to Optimize for Heterogeneous Hardware," Australian National University, 2019, Canberra.
3. "Kingsguard: Write-Rationing Garbage Collection for Hybrid Memories," Annual Non-Volatile Memories Workshop (NVMW), 2019, San Diego.
4. "Profile-Driven Write-Rationing Garbage Collection for Hybrid Memories," The 5th Virtual Machine Meetup (VMM), 2018, Linz.
5. "Write-Rationing Garbage Collection for Hybrid Memories," Swiss Federal Institute of Technology, 2018, Lausanne.
6. "Write-Rationing Garbage Collection for Hybrid Memories," The 4th Virtual Machine Meetup, 2017, Prague.
7. "Managed Language Runtimes on Heterogeneous Hardware: Optimizations for Performance, Efficiency and Lifetime Improvement," Workshop on Programming Across the System Stack (PASS), 2017, Brussels.
8. "DVFS Performance Prediction for Managed Multithreaded Applications," VSSAD seminar, Sep. 15, 2017, Intel, Portland.
9. "Energy-Efficient Managed Language Execution on Modern Hardware," The 3rd Virtual Machine Meetup (VMM), 2016, Lugano.

Achievements and Honors

- NVMW Memorable Paper Award 2019
- HiPEAC Paper Award for PLDI 2018
- Nominated for Best Paper Award at ISPASS 2016
- Marie Curie Initial Training Networks Fellowship (2010-2012)
- Fulbright Scholarship (2007-2009)

Service (ANU)

- Ph.D. Scholarship Ranking Committee (RSCS, August 2020)
- HDR Convener (SOCO, Foundations cluster, 2021)

Ph.D. Supervisory Panel (ANU)

- Wenyu Zhao (Advisor: Steve Blackburn)

Ph.D. Advisees

University of Crete, Department of Computer Science, 2020
 Iacovos G. Kolokasis (Co-Advisor and Ph.D. committee)
 Advisor: Angelos Bilas
 Thesis Topic: Efficient Caching for Big Data Analytics

Honours Students

- Aditya Chilukuri (2021-2022) Engineering and Advanced Computing (Honours)

B.S. and M.S. Project Students

- Aditya Chilukuri (2021, ANU, COMP3770, 6 units)
- Peixiao Zhao, Master of Computing (2021, ANU, COMP8755, 12 units)
- Hengjia Zhang, Master of Computing (2020, ANU, COMP8755, 12 units)
- Ruben Peter Vervaeke, Master of Science (2017, Free University Brussels, Thesis)

Examiner (ANU)

- COMP8800 Zixian Cai u5937495 2020 (24 units)
- COMP3770 Calum Snowdon u6044174 2020 (6 units)
- COMP4560 Benjamin Chamberlain Gray u6677379 2021 (12 units)
- COMP4550 Jack Hendrick de Kleuver u5740954 2021 (24 units)
- COMP4560 Yiluo Wei u6227375 2021 (12 units)
- COMP4550 Kunal Sareen u6509424 2021 (24 units)
- COMP8755 Zicheng Liu u6924878 2021 (12 units)
- COMP4550 Alexander Horvat u6048805 2021 (24 units)
- COMP4550 Matthew Law u6699654 2021 (24 units)

Peer Reviewing and Program Committees

- Program Committee, ISPASS 2022
- External Review Committee, ISCA 2022
- External Review Committee, MICRO 2021
- Program Committee, ISMM 2021
- External Review Committee, ISCA 2021
- External Review Committee, HPCA 2021
- External Review Committee, OOPSLA 2020
- Shadow Program Committee, EuroSys 2020
- External Review Committee, ISCA 2020
- External Review Committee, ASPLOS 2020

- Program Committee, VMIL 2019
- Session Chair, Programming Across the System Stack session, MoreVMs'19
- Student Volunteer Co-Chair, PLDI 2019
- Program Committee, ISMM 2019
- Program Committee, PASS 2018, 2019 (held alongside <Programming>)
- Artifact Evaluation Committee, OOPSLA 2016, 2017
- Artifact Evaluation Committee, PLDI 2018
- Reviewer, J. Parallel Distrib. Comput. 72 (2012)

Teaching

- Convener, ANU, 2021, Computer Architecture and Simulation (ENGN2219)
- Convener, ANU, 2021, Computer Microarchitecture (COMP5483)

References

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