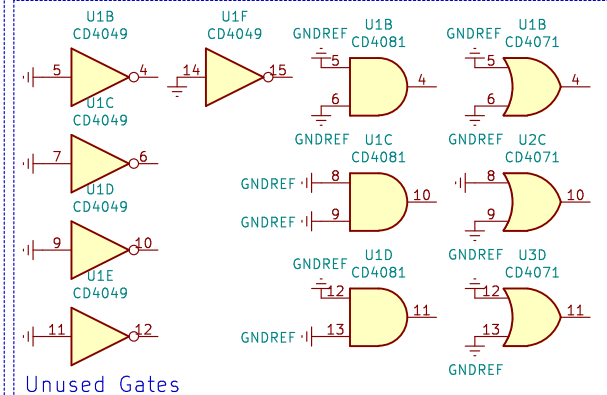
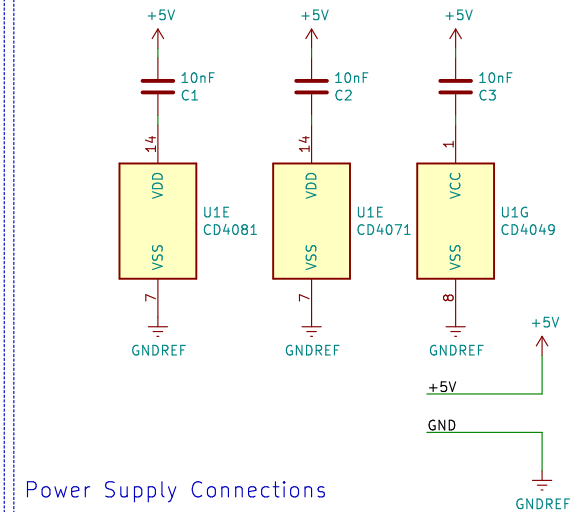


## Simplified Boolean Logic Gate Array



University of Cape Town  
Created by Duy Anh Nguyen, Student ID: NGYANH002

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File: EEE2047F-2.sch

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