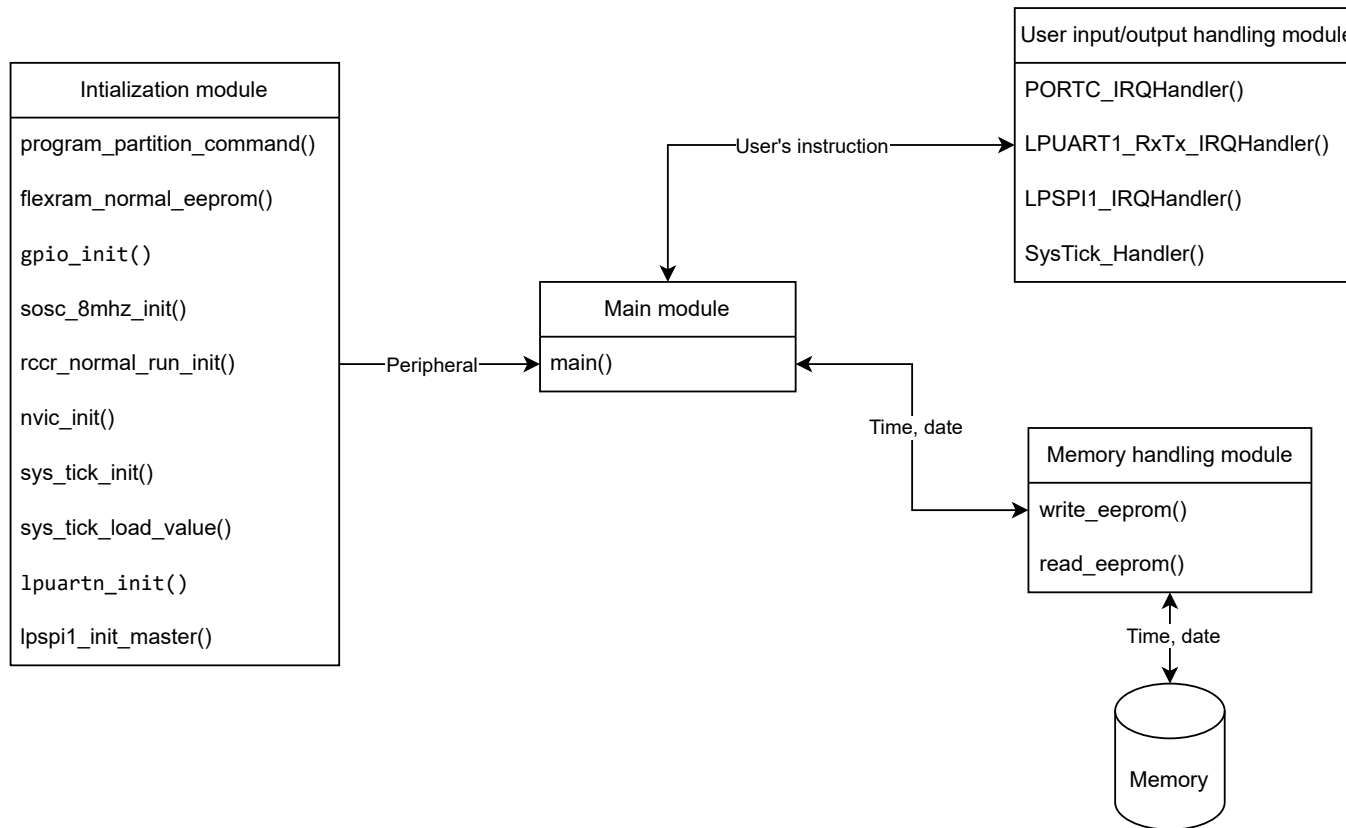
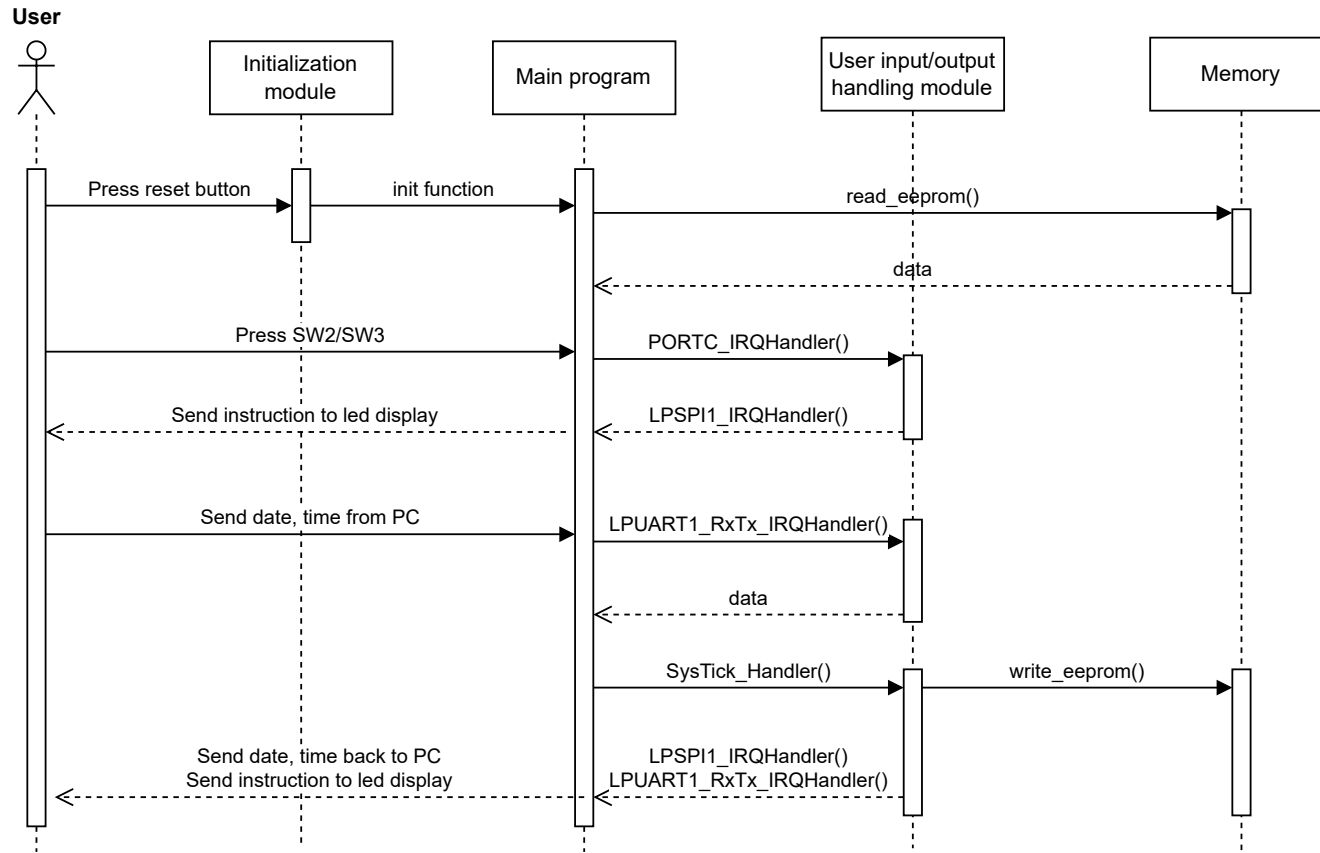


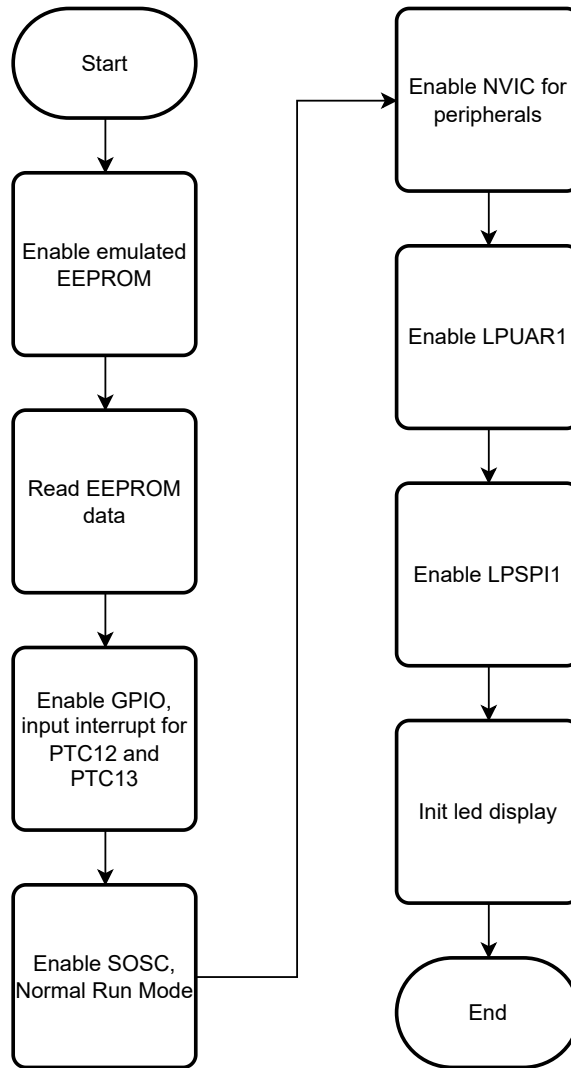
## ARCHITECT DESIGN FOR MOCK EMBEDDED



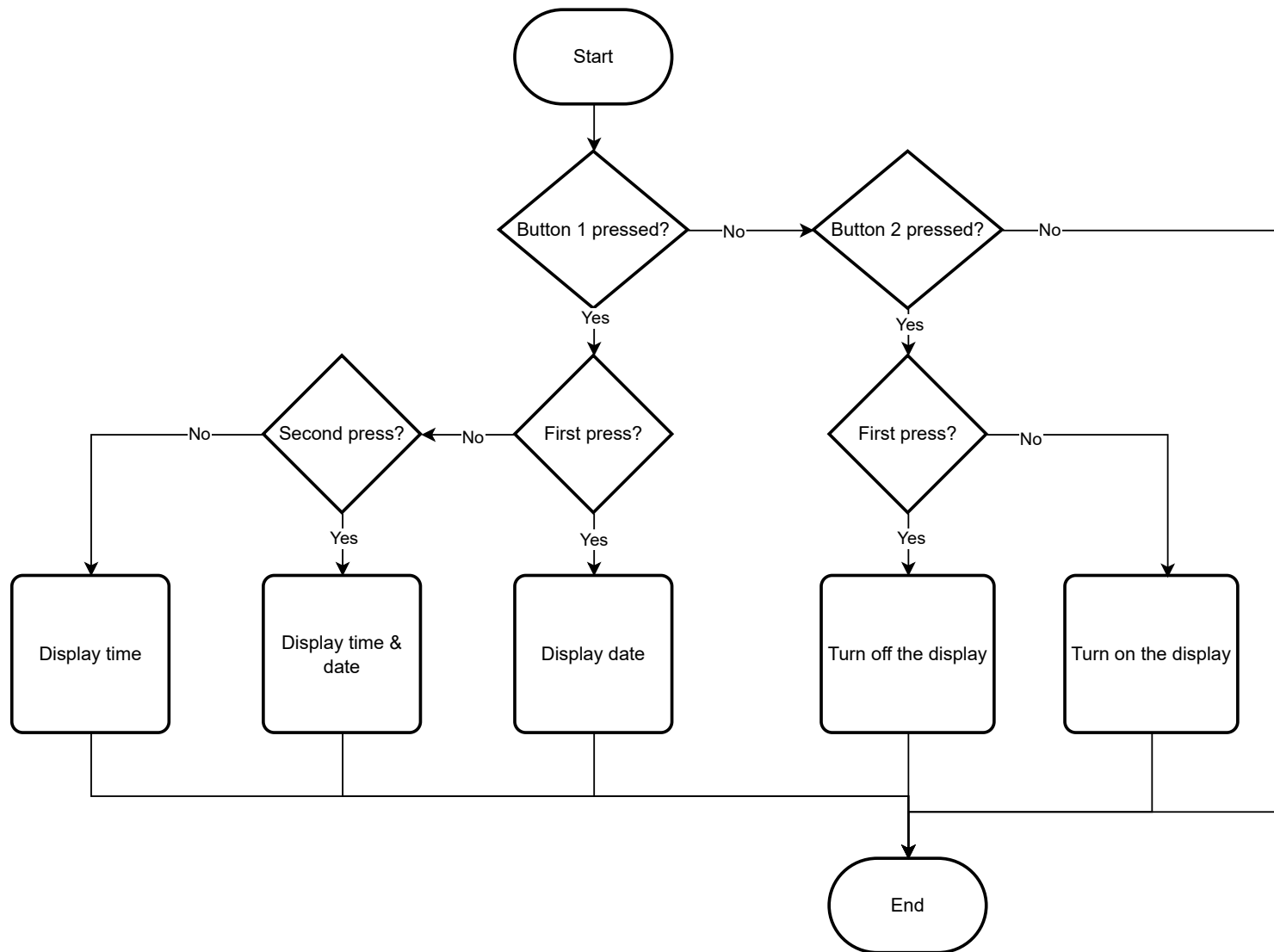
# SEQUENCE DIAGRAM FOR MOCK EMBEDDED



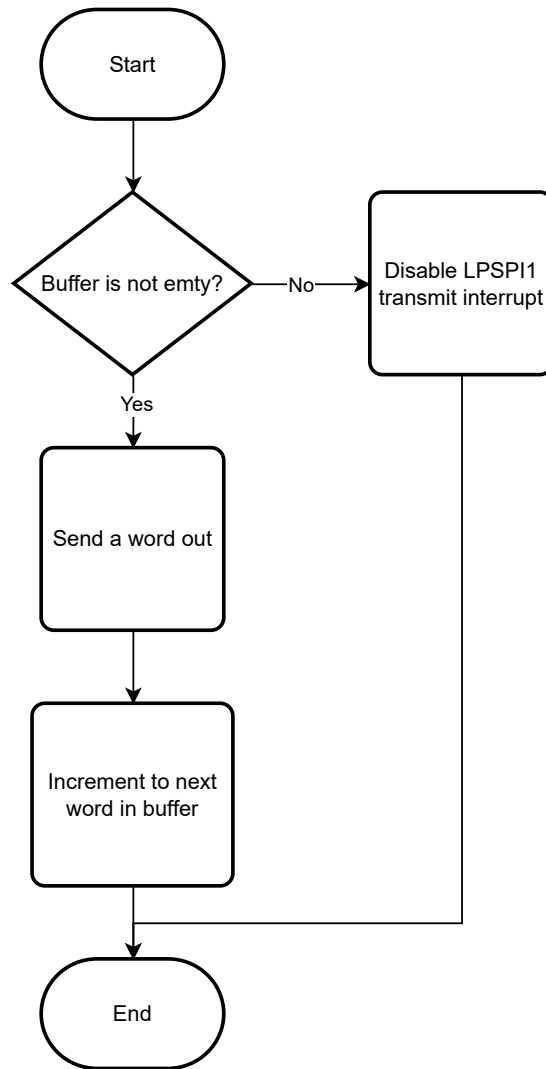
MAIN MODULE DETAIL DESIGN  
main()



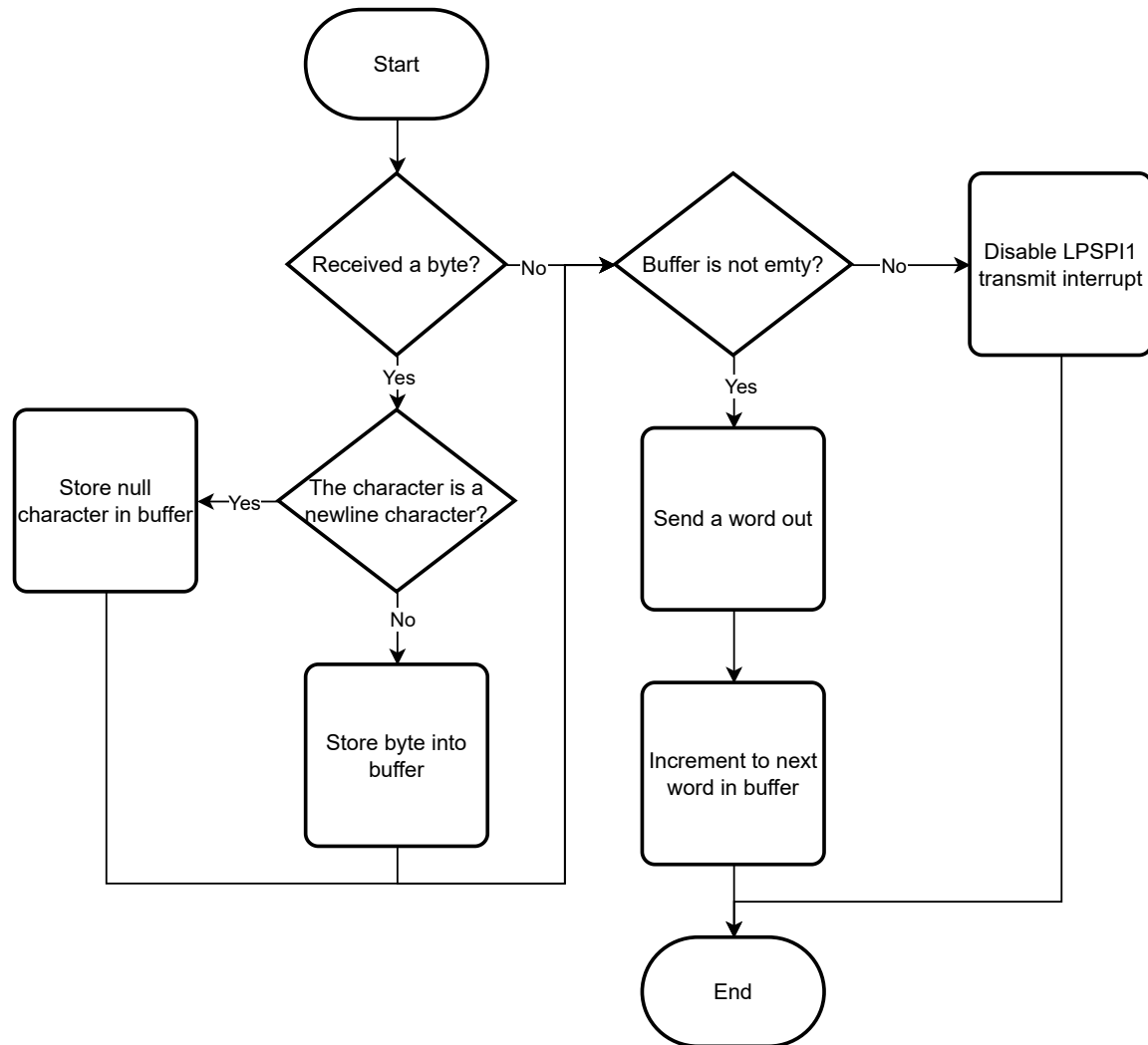
USER INPUT/OUTPUT MODULE DETAIL DESIGN  
PORTC\_IRQHandler()



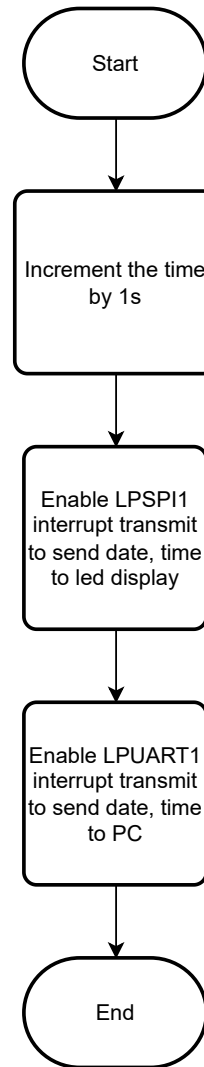
USER INPUT/OUTPUT MODULE DETAIL DESIGN  
LPSP11\_IRQHandler()



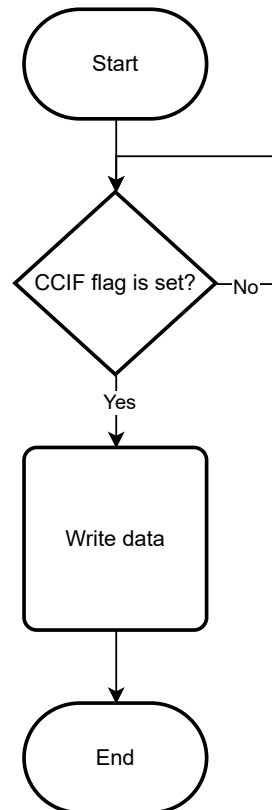
USER INPUT/OUTPUT MODULE DETAIL DESIGN  
LPUART1\_RxTx\_IRQHandler()



USER INPUT/OUTPUT MODULE DETAIL DESIGN  
SysTick\_Handler()



MEMORY HANDLING MODULE DETAIL DESIGN  
write\_eeprom()





MEMORY HANDLING MODULE DETAIL DESIGN  
read\_eeprom()

