



B.TECH. COMPUTER SCIENCE AND ENGINEERING,
SEMESTER III
GNDU, AMRITSAR

Practical File

Anu Sharma

Roll No: 17032000314

Subject: Digital Circuit Logic Design

Submitted To: Ms. Jaspreet Kaur

2021

GURU NANAK DEV UNIVERSITY, AMRITSAR

ECL-119: DIGITAL CIRCUIT LOGIC DESIGN