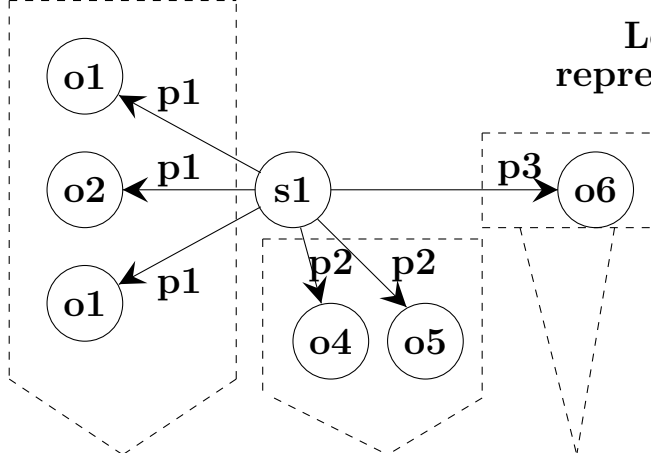


Logical  
representation



Physical  
layout

**s1 p1 o1 o2 o3 ; p2 o4 o5 ; p3 o6 .**