The University of Queensland – School of Information Technology and Electrical Engineering Semester 1, 2018 – CSSE2010 / CSSE7201 Project – Feature Summary

| Student Number | | | | Family Name | Given Names |
|----------------|--|--|--|-------------|-------------|
| | | | | | |

An electronic version of this form will be provided. You must complete the form and include it (as a PDF) in your submission.

You must specify which IO devices you've used and how they are connected to your ATmega324A.

| Port | Pin 7 | Pin 6 | Pin 5 | Pin 4 | Pin 3 | Pin 2 | Pin 1 | Pin 0 |
|------|------------------------------|-------|-------|-------|-----------|-----------|----------------------|------------------------|
| A | | | | | | | | |
| В | SPI connection to LED matrix | | | | Button B3 | Button B2 | Button B1 | Button B0 |
| С | | | | | | | | |
| D | | | | | | | Serial RX Baud ra | Serial TX te: 19200 |

| (Anything you want the marker to consider or know?) | | |
|---|--------|-------------------|
| | /4 | |
| | | |
| | | |
| | | |
| | | /53 |
| | | /33 |
| | /8 | |
| | /8 | |
| | /8 | /32 |
| | /5 | |
| | /5 | |
| | /5 | |
| | /5 | |
| | /5 max | /15 max |
| | | /8 /8 /8 /5 /5 /5 |

| Total: | (out of | 100, | max | 100) |
|--------|---------|------|-----|------|
|--------|---------|------|-----|------|

Penalties: (code compilation, incorrect submission files, etc. Does not include late penalty)

Final Mark: (excluding any late penalty which will be calculated separately)