

Vietnam: (084)-905-257-768

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OBJECTIVE

Seeking a new opportunity to develop my knowledge and advance my career.

SUMMARY OF QUALIFICATIONS

- In-depth knowledge of Deep Learning and Artificial Intelligence, especially of Video Classification, Real-time Object Recognition and Natural Language Processing.
- In-depth knowledge of Signal Processing and Analysis, especially of Image/Video Processing and Representation, Image/Video Search and Retrieval.
- Python, Matlab and C/C++ programming language for Signal Processing, Deep Learning.
- In-depth knowledge of Electronic and Telecommunication principles and practices.
- Effective communication and team skills.

EDUCATION

- 2007-2012:
Bachelor of Telecommunication, Danang University of Technology (Vietnam)
Faculty of Electronic and Telecommunication
Average learning capacity (CPGA): 3.47/4
- 2013-2017:
PhD Candidate, Nanyang Technological University (Singapore)
Interdisciplinary Graduate School, Rapid-Rich Object Search Laboratory (RoseLab)
Average learning capacity (CPGA): 4.63/5

JOB EXPERIENCE

Global Cybersoft Vietnam, Inc.

October 2017 – Now

Artificial Intelligence and Deep Learning Engineer

- Image/Video and Speech Analysis using Deep Learning.
- Real-time object detection using YOLOv3 technique.
- Predictive Maintenance using Deep Neural Network.

Acronics System, Inc.

July 2012 – February 2013

FPGA and Embedded System Design Engineer

- FPGA design using Verilog VHDL.
- Embedded systems design using C/C++ programming language on Microchip MCUs (PIC16 and PIC32 family).

ACADEMIC PROJECTS

Two Novel Extensions for VLAD Based Image Representation (Research Project) Fall 2016

- Propose a novel extension for the most successful hand-crafted global image representation, VLAD, to significantly improve its performance in image retrieval.
- Unpublished work: write a technical report.

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Dominant SIFT: A Novel Compact Descriptor (Research Project)

Spring 2015

- Propose a novel compact local descriptor, which is based on the famous SIFT descriptor, for image representation and mobile visual search.
- Publish a conference paper at the 40th IEEE International Conference on Acoustics, Speech and Signal Processing 2015.

Design a simple MIPS 32 Microprocessor using Verilog VHDL (Group Project)

Fall 2011

- Design a simple microprocessor model based in MIPS 32 structure using Verilog VHDL.
- Simulation the model in Modelsim.

Multi-hop communication system and Two-way Relay (Individual Project)

Spring 2012

- Research multi-hop communication system and build the mathematical model for two-way relay communication system.
- Build and simulation the model of two-way relay communication system in Matlab.

AWARD AND ACHIVEMENT

- Coursera Deep Learning Specialization Certificate – DeepLearning.AI (Feb 2018-Now).
- Annual Scholarship of Danang University of Technology for Excellent Student (2007-2012).
- First prize for Mathematical Analysis in Competition for Danang University Excellent Student (2009).
- Consolation prize for Mathematical Algebra in Competition for Danang University Excellent Student (2009).
- First prize for Mathematical Algebra in Competition for Danang University Excellent Student (2012).
- Nanyang Technological University Research Scholarship (2013-2017).

ADDITIONAL INFORMATION

- *Languages*: Advanced Vietnamese, Advanced English, Advanced Python, Advanced Matlab, Intermediate C/C++, Basic JavaScript, Basic Java.
- *Others*: Microsoft Office, LaTeX language, Adobe Photoshop CC.