**Description**

Release is subjected to verify Tenneco Library blocks at .c level from microcontroller generated auto code on target specific. Verification checks out for code optimization, and memory utilization.

**Target Specifications**

* AUTOSAR.tlc
* Version 4.2

**Microcontroller Environment**

* Infineon Tri-core, C1, C2xx
* Freescale – 32-bit Power PC

**TEN LIB VERSION**

* V1.13.1

**Input test case to blocks**

Input test cases are the ones that fed during Model-Simulation. Order to maintain consistence the same test cases were used to perform software-execution.

**Attachments**

* S-function (.mex file) --- Ten\_Lib\_Test\_sbs.mexw64
* Model file (.slx) ---- Rate\_limiter
* Model file (.slx) ---- Ten\_Lib\_Test
* Library Report ---- Ten\_LIB\_Report
* Image File (.jpg) ---- Results
* Library Release(.slx) --- tenneco\_lib
* Release notes (TEN\_LIB\_SW\_ReleaseNotes) ---- This document

**Limitations**

None