1. MAC address is also called \_\_\_\_\_\_.

Answers

1. Physical address (ans)

2. Logical address

3. Source address

4. Destination address

2. Which is main function of transport layer?

Answers

1. Node to node delivery

2. End to end delivery (ans)

3. Synchronization

4. updating and maintaining routing table

3. The difference between memory and storage is that memory is \_\_\_\_ and storage is \_\_\_\_\_

Answers

1. Temporary, Permanent (ans)

2. Permanent, Temporary

3. Slow, fast

4. All of the above

4. At the \_\_\_\_\_\_ Layer of OSI model the data unit is transformed into the corresponding electromagnetic signal.

Answers

1. Application

2. Physical (ans)

3. Session

4. Data link layer

5. Which of the following task is not done by data link layer?

Answers

1. Framing

2. Error Control

3. Flow Control

4. Channel Coding (ans)

6. Minicomputers are also called …………… computers.

Answers

1. handheld

2. laptop

3. midrange (ans)

4. smart

7. Which part interprets program instructions and initiate control operations.

Answers

1. Input

2. Storage Unit

3. Logic Unit

4. Control Unit(ans)

8. The commonly used protocol for webpage transfer is\_\_\_\_\_

Answers

1. HTML

2. HTTP (ans)

3. WML

4. WTTP

9. \_\_\_\_\_\_ is the example of full-duplex system.

Answers

1. Radio

2. the telephone network (ans)

3. The keyboard

4. the repeater

10. In ………………., the processor issues an I/O command, on behalf of a process, to an I/O module.

Answers

1. Programmed I/O (ans)

2. Interrupt driven I/O

3. Direct Memory Access

4. Virtual Memory Access

11. What is the high speed memory between the main memory and the CPU called?

Answers

1. Register Memory

2. Cache Memory (ans)

3. Storage Memory

4. Virtual Memory

12. The data link layer divides the stream of bits received from the network layer into data units called \_\_\_\_\_\_\_\_

Answers

1. Segment

2. Frames(ans)

3. Datagram

4. Messege

13. Where is a hub specified in the OSI model?

Answers

1. Session Layer

2. Physical layer (ans)

3. Data Link layer

4. Application Layer

14. The CPU and memory are located on the:

Answers

1. Expansion Board

2. Motherboard (ans)

3. Storage Device

4. Output Devic

15. Pentium is related with which part of Computer

Answers

1. DVD

2. Hard Disk

3. Micro Processor (ans)

4. Mouse

16. CPU gets the address of next instruction to be processed from

Answers

1. Instruction Register

2. Memory Address Register

3. Program Counter (ans)

4. Index Register

17. Which of the following is invalid type of memory

Answers

1. RAM

2. ROM

3. PRAM (ans)

4. EPROM

18. A \_\_\_\_\_\_ is usually privately owned and links the devices in a single office, building or campus.

Answers

1. LAN (ans)

2. MAN

3. WAN

4. all of the above

19. Transit time and response time measure the \_\_\_\_\_\_\_ of a network.

Answers

1. Performance (ans)

2. Reliability

3. Security

4. Longevity

20. Class C range lies within range

Answers

1. 0 to 127

2. 128 to 191

3. 192 to 223 (ans)

4. 240 to 255