

#### **GENERAL GUIDELINES**

#### Do's:-

- Students should be on time for every lecture.
- Students are advised to show due respect to all faculty members.
- Students should keep the Classrooms, Laboratories and Workshops clean and tidy.
- Students must maintain absolute discipline and decorum, while on campus.
- Students should come prepared with algorithm / flowchart / program / procedure for all the experiments before attending the laboratory session.
- Students should bring the data sheets and laboratory records completed in all respects to the laboratory.
- Students are advised to clarify their doubts in the respective courses with the faculty.
- Students have to inform their parents that they should follow up the progress of their wards by being in touch with the institution authorities at regular intervals.
- Students are advised to be present for the mentor meetings conducted by their respective Faculty Advisors, failing which appropriate disciplinary action will be taken.

#### Don'ts:-

- Students are not permitted to attend the class without the identity card, once issued.
- Ragging is strictly prohibited because it is punishable under Karnataka Education Act. Any student involved in ragging, will be severely punished which includes handing over the case to Police, rustication from the college etc.
- Writing on desks and walls is strictly prohibited, failing which the students will be fined heavily.
   If the identity of the individual is not established the entire class / students in the block will be fined.
- Students must not use their cell phones during class hours. If any student is found using their cell phone during class hours it will be confiscated.
- Students are not supposed to alter the configuration of the system / any software on the systems.



### **IV SEMESTER (2018-2022)**

Sl.	Course Code	Course Title	Н	Hours per week			Credits	Course Type
No.			L	Т	P	S		-
1	UE18MA251	Linear Algebra and Its Applications	4	0	0	0	4	FC
2	UE18CS251*	Design and Analysis of Algorithms	4	0	0	0	4	CC
3	UE18CS252	Database Management Systems	3	0	0	0	3	CC
4	UE18CS253	Microprocessor and Computer Architecture	4	0	0	0	4	FC
5	UE18CS254	Theory of Computation	3	0	0	0	3	CC
6	UE18CS255	Design and Analysis of Algorithms Laboratory	0	0	2	0	1	CC
7	UE18CS256	Microprocessor and Computer Architecture Laboratory	0	0	2	0	1	FC
8	UE18CS257 X	Special Topic II	0 /2	0	0 /4	0/8	2	PW
9	UE19MA151D	Engineering Mathematics –II (Applicable to Lateral Entry Students)	2	0	0	0	2	FC
Total			20/22	0	4/8	0/8	22/24	
Note: Prerequisite courses * UE18CS151								



### **UE18MA251 - LINEAR ALGEBRA (4-0-0-0-4)**

#### # of Credits: 4

Class No.	Portions to be covered	Percentage of Syllabus Covered
1	Introduction to Linear Algebra	
2-3	The Geometry of Linear Equations – Row and Column Pictures	
4	Singular cases in two and three dimensions	
5-6	Gaussian Elimination, The breakdown of elimination	
7	Scilab Class Number1 – Gaussian Elimination	
8	Elementary Matrices	20
9-10	Triangular Factors and Row Exchanges	
11-12	Inverse by Gauss -Jordan Method, Transposes	
13-14	Scilab Class Number 2&3- LU Decomposition and Inverses	
15-17	Vector Spaces and Subspaces ( Definition only ), Column Space and Null Space, Examples	
18-20	Echelon Form, Row Reduced Form, Pivot Variables , Free variables	
21- 24	Linear Independence, Basis and Dimensions	40
25	Scilab Class Number 4 – Span of Column Space of A	
26-27	The Four Fundamental Subspaces	
28	Existence of Inverses	
29	Scilab Class Number 5 –Four Fundamental Subspaces of A	
30-31	Linear Transformations, Examples	
32-33	Transformations Represented by Matrices	
34-35	Rotations, Reflections and Projections	
36-38	Orthogonal Vectors and Subspaces	60
39-40	Cosines and Projections onto Lines	
41-42	Projections and Least Squares	
43	Scilab Class Number 6-Projections by Least Squares	
44-	Orthogonal Bases- Orthogonal Matrices, Properties, Rectangular	
46	Matrices with orthonormal columns	
47-50	The Gram- Schmidt Orthogonalization, $A = QR$ Factorization	
51	Scilab Class Number 7- The Gram- Schmidt process	
52-54	Introduction to Eigen values and Eigenvectors, Properties of eigenvalues and eigenvectors, Power Method to compute the largest eigenvalue	80
55-56	Diagonalization of a Matrix, Powers and Products of Matrices	



57-58	Scilab Class Number 8&9- Eigen Values and Eigen Vectors,	
	The Power Method	
59-60	Positive Definite Matrices - Tests for Positive Definiteness	
61-	Positive Definite Matrices and Least Squares, Semi definite	100
62	Matrices	
63-	The Singular Value Decomposition of a Matrix, Examples	
67		
68	Applications of SVD	
69-70	Scilab – In Semester Assessment	

### **UE18CS251 - DESIGN AND ANALYSIS OF ALGORITHMS (4-0-0-0-4)**

# of Credits: 4 # of Hours: 56

Class	Chapter	Topics to be Covered		
#	Title /		% of Portion	o covered
	Reference		70 01 1 01 (101	reovered
	Literature			1
			% of	Cumulative
	_		Syllabus	%
1	Unit #1	The motivation for the course.		
		Scheme of the course.		
	T1:	Introduction to Algorithms.		
2	Chapters	Fundamentals of Algorithmic problem-solving.		
	1.1, 1.2, 1.3,	Important problem types – sorting, searching.		
3	2.1, 2.2, 2.3,	Important problem types – string processing,	16.07	16.07
	2.4	graph problems, Combinatorial, Geometrical,		
		numerical problems		
4		Analysis Framework, Orders of Growth		
5		Asymptotic Notations		
6		Basic Efficiency Classes		
7		Analysis of Non-recursive Algorithms		
8		Analysis of Recursive Algorithms		
9		Solving Recurrences of Recursive Algorithms		
10	Unit #2	Brute-Force approach and Sequential Search		
11	]	Brute-Force String Matching		
12	T1:	Selection Sort and Bubble Sort		
13	Chapters	Exhaustive Search – Travelling Salesman		
	3.1, 3.2,	Problem	23.22	39.29
14	5.2,	Knapsack Problem, Assignment Problem		
15	3.4,	Recursion		
16	5.1, 5.3, 5.4,	Decrease-and-Conquer approach - Insertion Sort		



	I	(Jan – May, 2020)		1
17	5.5	Depth First Search		
18		Breadth First Search		
19		Topological Sorting		
20		Algorithms for Generating Combinatorial		
		Objects		
21		Johnson Trotter Algorithm		
22		Decrease-by-a-Constant-Factor Algorithms		
23	Unit #3	Divide-and-Conquer approach with Mergesort		
24		Quicksort		
25	T1:	Binary Search		
26	Chapters	Multiplication of Large Integers		
27	4.1, 4.2, 4.3,	Strassen's Matrix Multiplication	19.64	58.93
28	4.4, 4.5,	Transform-and-Conquer approach - Presorting		
29	6.1, 6.3, 6.4,	Heapsort		
30	7.4	Balanced Search Trees - AVL Trees		
31	1	AVL Tree Construction		
32	1	2-3 Trees		
33	1	B Trees		
34	Unit #4	Space and Time Tradeoffs - Sorting by Counting		
35	1	Distribution Counting Sort		
36	T1: chapters	Input Enhancement in String Matching –		
	7.1, 7.2,	Horspool's algorithm		
37	12.1, 12.2,	Boyer-Moore Algorithm		
38	9.1, 9.2, 9.3,	Backtracking: N queens Problem, Hamiltonian	19.64	78.57
	9.4	circuit Problem		
39	1	Branch-and-Bound: Knapsack Problem,		
		Travelling Salesman Problem		
40	]	Greedy Approach		
41	1	Prim's Algorithm		
42	1	Kruskal's Algorithm		
43	]	Dijkstra's Algorithm		
44	1	Huffman trees		
45	Unit #5	Dynamic Programming approach		
46	1	Examples of Dynamic Programming		
47	T1: chapters	Computing a Binomial Coefficient		
48	8.1, 8.4,	Knapsack problem and Memory Functions		
49	8.2,	Problem: All-pairs Shortest-paths		
50	11.1, 11.2,	Floyd's Algorithm		
51	11.3	Problem: Finding Transitive Closure		
52	1	Warshall's Algorithm	21.43	100
53	1	Lower Bound Algorithms		
54	1	Decision Trees		
	1			
55		P, NP and NP Complete problems		



Coping with the limitation	of Algorithm Power
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#### Literature:

Publication Information					nation
Book Type	Code	Title & Author	Editio n	Publisher	Year
Text Book	T1	Introduction to The Design and Analysis of Algorithms Anany Levitin	2	Pearson	2011
Reference Book	R1	Introduction to Algorithms Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest and Clifford Stein	3	Prentice-Hall Indi	a 2009
Reference Book	R2	Horowitz, Sahni, Rajasekaran, Fundamentals of Computer Algorithms	2	Universities Press	2007
Reference Book	R3	Jon Kleinberg, Eva Tardos, Algorithm Design	1	Pearson Education	2006

### **UE18CS252 – DATABASE MANAGEMENT SYSTEMS (3-0-0-0-3)**

# of Credits: 3 # of Hours: 42

Class	Reference	Topics	% of Portion covered	
#	Literature		% of Syllabus	Cumulative %
Unit #1	- Introduction	n to Database and Conceptual Design		
using E	RD		19	19
1	T1: 1.1 -	Introduction to Databases		
	1.6			
	RA: T1 1.7			
	<b>– 1.8</b>			
2	T1: 2.1 –2.3	Data models, Three-Schema		
		Architecture and Data Independence,		
		Database Languages and Interfaces		
3	T1: 2.4 -	Database system environment,		
	2.6	Centralized and Client/Server		
		architectures, Classification of database		
		management system		
4	T1: 3.1 -	Conceptual Model, Entity types,		
	3.3	attributes and keys		
5	T1: 3.4	Relationship types, sets, roles		
6	T1: 3.5 -	Weak Entity, Refining the ER design,		



	1	(Jan – May, 2020)	T	ī
	3.7	ER diagrams		
7	T1: 3.9	Relationship Types of degree higher		
		than two		
8	T1: 3.10	Example: University database		
	Assignment	Laboratory exercise 3.34		
	1			
Unit #2	- Relational N	Model		
9	T1: 5.1	Relational Model concepts		
10	T1: 5.2	Relational model Constraints and		
		relational Database Schemas		
11	T1: 5.3	Update operations, Transactions and		
		dealing with constraint violations		
12	T1: 9.1	Relational Database Design Using ER-		
		to Relational Mapping		
13	T1: 8.1	Unary Operations - SELECT and	19	38
		PROJECT		
14	T1: 8.2	Set Theory Operations		
15	T1: 8.3	Binary Relational Operations - JOIN,		
		DIVISION		
16	T1: 8.4 –	Aggregate Functions and Grouping		
	8.5			
	Assignment	T1: Exercise 8.17		
	2			
Unit #3			24	62
17	T1: 6.1	SQL Data Definition, Primary Data		
		Types, Advanced Data Types like		
		CLOB, BLOB		
18	T1: 6.2	Specifying Constraints in SQL		
19	T1: 6.3 -	Basic Retrieval Queries, Insert, Delete,		
20	6.5	Update		
21	0.0			
22	T1: 7.1	Advanced SQL Queries, Other SQL		
23		Constructs : WITH, CASE, Outer joins		
24	T1: 7.2 -	Specifying General Constraints as		
	7.4	Assertions and Triggers, Views and		
		Schema Change Statements in SQL		
25	T1: 19.9	Additional Features of SQL –		
			İ	
	RA: T1	performance, execution plan		
	17.1, 17.6 -	performance, execution plan		
	17.1, 17.6 – 17.7			
26	17.1, 17.6 – 17.7 T1: 10.1 –	Database Programming		
26	17.1, 17.6 – 17.7			



		(Jan – May, 2020)		
	3			
Unit #4	- Database D	esign		
27	T1: 14.1	Informal Design Guidelines for		
		Schemas		
28	T1: 14.2	Functional Dependencies		
29	T1: 15.1	Inference Rules, Closure	_	
30	T1: 15.1.2 –	Equivalence, Minimal Cover		
	15.1.3			
31	T1: 15.2	Properties of Relational		
		Decompositions	19	81
32	T1: 14.3	Normal Forms Based on Primary Keys		
33	T1: 14.4	(1st, 2nd and 3rd NF), General		
		Definitions of Second and Third		
		Normal Forms		
34	T1: 14.5 -	,		
	14.7	of Higher Normal Forms		
	Assignment	Exercise T1: 14.32		
	4			
		nitecture and Database Security		
35		Database Security (CREATE USER,		
	30.3	ROLE, GRANT and REVOKE)		
36	T1: 20.1,	Transactions - ACID Properties,		
	20.3	schedules		
	RA – T1:			
	20.4 – 20.5			
37	T1: 20.6	SQL commands for database		
		transactions (BEGIN, END, COMMIT,		
20	FD4 04 4	SAVEPOINT, ROLLBACK)	19	100
38	T1: 21.1,	Concurrency, Locking, Deadlocks -		
20	21.2	Detection and Prevention		
39	T1: 22.1	Crash Recovery		
40	-22.2			
40	T1:23.1	Overview of parallel and distributed		
11	TT1. 24.1	databases		
41	T1: 24.1 -			
42	24.3	NoSQL databases		
42	T1: 24.4 -			
	24.6			



#### Literature:

			Publication Information			
Book Type	Code	Title & Author	Editio n	Publisher	Year	
Tout Dools	T1	Fundamentals of Database Systems - Ramez Elamsri, Shamkant B Navathe	7 <sup>th</sup>	Pearson	2017	
Text Book	T2	Database Management Systems - Johannes Gehrke, Raghu Ramakrishnan	3 <sup>rd</sup>	McGraw-Hill	2003	
Reference Book	R1	Database Systems: The Complete Book - Garcia-Molina, J D Ullman, Widom	2 <sup>nd</sup>	Prentice-Hall	2008	
	R2	Database System Concepts - Silberschatz, H Korth, S Sudarshan	6 <sup>th</sup>	McGraw-Hill	2010	

#### **UE18CS253 - MICROPROCESSOR AND COMPUTER ARCHITECTURE**

# of Credits: 4 # of Hours: 56

Class	Chapter Ti-		% of por	rtions covered
#	tle / Refer-	Topics to be Covered	Refer-	
	ence Liter-		ence	Cumulative
	ature		Chapter	
UNIT	1: Introducti	on to Microprocessor, & ISA (10 hours)		
1		Introduction -Overview of Microprocessor : Evo-		
		lution and Introduction to INTEL Processor		
2	1.6,2.3 of	ISA – Classification, RISC and CISC		
3	T2,	Memory Addressing-Interpreting memory ad-		
	A-3 of T1,	dresses, addressing modes		
4	pg no: 51-	Addressing Modes		
5	55 of T2	Type and size of operands	20%	20%
6	Chapter 3.1	Instruction set-operations		
7	to 3.5 of T3	Instruction set-operations		
8	6.8,5.6 of	Instruction set-operations		
9	T2	Instruction set-operations		
10		Instruction Encoding		
11		Instruction Encoding		
12		Case Study- ARM/MIPS/X86 Processor		
UNIT	2 Pipelining	( 12 hours)		

		(Jan – May, 2020)		
13		Introduction to Pipelining,		
		3 stage pipelining		
14		5 stage pipelining		
15	4.1,4.2 of	Pipeline hazards		
16	Text T2	Structural Hazards		
17		Data Hazards .		
18	Appendix	Data Hazards continued		
19	C-1, C-2,	Data hazards	20%	40%
20	Sec 1.1,	Control hazards- what is a control hazard, predic-		
	1.4, 1.5 of	tion algorithms- static, dynamic		
21	T1	Static branch prediction		
22		Dynamic branch prediction		
23		Dynamic branch prediction		
24		Dynamic Branch predication		
UNIT	3· Me	emory Hierarchy (10 Hours)		
25	1410	Trends in technology, power & energy in Inte-		
		grated Circuits		
26		Introduction to Memory Hierarchy		
27	-	Fully Associative Cache Memory		
28	-	Direct Map Cache Memory		
29	-	Set Associative Cache Memory		
30	Appendix	Cache Performance Problems		
31	B.1, B.2,	Cache Performance Problems	20%	60%
32	B.3 of T1	Basic cache Optimizations		
33		First & Second optimizations		
34	-	First & Second optimizations		
35	-	Third & Fourth Optimizations		
36	-	Third & Fourth Optimizations		
	4: Cache C	Optimizations and Parallel Architecture (10 Hours)	L	
37		cache Optimization 5		
38	1	Cache Optimization 5		
39	Appendix	Cache Optimization 6		
40	B.3 of T1	Cache Optimization 6		
41	( Referenc	Introduction to I /O	200/	000/
42	e Material	Introduction to parallel architecture	20%	80%
43	to be	Parallel programming models		
44	Provided)	Memory architecture		
45		Memory architecture		
46		Amdahl's Law		
47		Gustafson's Law		
		s in Architecture ( 10 Hours)		



48	Chapter 3	Introduction to Instruction Level Parallelism (ILP)		
49	of	Scheduling policies for ILP		
50	T1( 3.1,3.2	Scheduling policies for ILP		
51	,3.4,3.5	Dynamic Scheduling schemes		
52	and 3.10)	nd 3.10) Limitations of ILP		
53	( Refer-	Multicore architecure	20%	100%
54	ence ma-	ma- Multicore architecture continued		
55	terial to	Multicore architecure		
56	be pro-	Revision		
	vided)			

#### Literature:

Book	Code	Title & Author	Publication Info			
Type			Edition	Publisher	Year	
Text Book	T1	Hennessy Patterson	Fifth Edition	MK Morgan Kaufmann	2012	
Text Book	T2	ARM System on Chip, Steve Furber	Second Edi- tion,	Pearson Education	2000	
Text Book	Т3	ARM System Developer's Guide	Reprint 2009	Elsevier	2009	

#### **UE18CS254 – THEORY OF COMPUTATION**

# of Credits: 3 # of Hours: 42

Class #	Unit	Topic	Chapter &	% Cov	erage
Class #	Oilit	торіс	Section	Unit	Total
1	1		T1- 1.2	21.15	21.15
		Introduction: Computers, computation, computability	T2 - 1.1,		
		and Languages	1.2, 1.3,		
			1.7, 1.8		
2		Deterministic Finite Automata: Definition, start state,	T1 – 2.1		
3		final state, internal states, Language of DFA,	T2 - 2.1 -		
		construction of DFA and string acceptance	2.3, 2.9		
4			T1 – 2.2		
5		Non–Deterministic Finite Automata	T2 - 2.12,		
			3.1 - 3.2		
6		Equivalence of Deterministic and Non–Deterministic	T1 - 2.3		



7		Finite Automata			
			T2 2 F	-	
8		Minimizing Finite Automata	T2- 3.5		
9		Regular Expressions, Construction of Regular Expressions	T2- 4.1 – 4.4, 4.7, 4.9		
10		Equivalence of RegEx & Finite Automata	T2 - 4.5 -		
11		Equivalence of Negex & Finite Automata	4.6		
12	2	Regular Grammars	T2 - 5.1 – 5.3	10.22	40.38
13	_ 2		T2 - 5.4 -	19.23	40.38
14		Equivalence of Regular Grammar & Finite Automata	5.7		
15		Closure Properties of Regular Languages	T2 - 6.1 – 6.2		
16		Pumping Lemma and identifying Non–Regular Languages	T2 - 6.3 – 6.4		
17		Context–Free Languages	T2 - 7.1 – 7.2		
18		Constructing Context–Free Grammars: Linear & Non –	T2 - 7.3 -		
19		linear	7.5		
20		Darsing and Ambiguity	T2 - 7.6 -		
21	3	Parsing and Ambiguity	7.8	21.15	61.53
22		N. D. Link B. H. A. L. G. L. H.	T1 - 7.1		
23		Non–Deterministic Pushdown Automata, Constructing Pushdown Automata	T2 - 8.1 -		
24		T distinctivity and the state of the state o	8.4, 8.7		
25		Equivalence of Push Down Automata and Context Free Grammars, Conversion CFG to PDA	T1 - 7.2		
26		Deterministic Pushdown Automata and Deterministic Context–Free Languages	T1 – 7.3		
27		Conversion to Chomsky Normal Form, A Membership	T1 – 6.3	]	
28		Algorithm for Context–Free Languages	T2 - 7.10		
29	4	Greibach Normal Form	T2 - 7.11	21.15	82.68
30		Closure Properties Context Free Languages	T2 - 9.1 - 9.4	7 21.13	02.00
31			T2 - 9.5 –	1	
32		Pumping Lemma for Context–Free Languages	9.6		
	5	The Standard Turing Machine	T2 - 10.1 -	1	1



	(5411 1114), 1010)			
34		10.3		
35		FFD 40.4		
36	Constructing Turing Machines	T2 - 10.4 – 10.5		
37		10.5		
38	Church–Turing Thesis , Universal Turing Machine	T2 - 10.7, 10.9	17.32	100
39	Recursive and Recursively Enumerable Languages	T2 - 11.1 -		
40	Diagonalization, Chomsky hierarchy	11.8, 11.13		
41	Post Correspondence Problem, The Halting Problem of Turing Machines	T2 - 12.1, 12.3		
42	Undecidable Problems	T2 - 12.7		

#### **UE18CS255 – DESIGN AND ANALYSIS OF ALGORITHMS LABORATORY**

# of Credits: 1 # of Weeks: 13

Laboratory	Design and Analysis of Algorithms Laboratory (UE18CS255)
Title	
For the Class	B. Tech. 4 <sup>th</sup> Semester 2018-2022 batch
Preamble	Design and Analysis of algorithms is core to computer science and engineering. It is
	important in designing algorithms to solve different types of problems.
	The laboratory includes implementation of problem statements with respect to various
	algorithmic strategies such as Divide and Conquer, Decrease and Conquer, Greedy and
	Dynamic Programming techniques.
Objective	Understand the algorithm design techniques including Brute Force, Divide and Conquer,
	Decrease and Conquer, Transform and Conquer, Dynamic Programming and Greedy
	technique.
	Make Space and Time Tradeoffs while designing algorithms.
	Analyze the algorithms with absolute running time of the algorithm implementations and
	compare it with the asymptotic complexity classes of the algorithms.
Outcome	
	Design and implement algorithms of Brute Force Technique.
	Design and implement algorithms with Divide and Conquer technique.
	Design and implement algorithms with Decrease and Conquer and Transform and
	Conquer techniques.
	Design and implement algorithms with Space and Time Tradeoffs.
	Design and implement optimization algorithms using Dynamic Programming and Greedy
	technique.



Session	Tasks
1	Introduction to Algorithms laboratory and Implementation of Sequential search
2	Implementation of Naïve string matching algorithm
3	Implementation of Bubble sort and Selection sort algorithm with time computations
4	Implementation of exhaustive travelling sales person problem.
5	Implementation of insertion sort and comparison of sorting algorithms
6	Implementation of DFS and BFS related problems
7	Implementation of merge sort and binary search algorithm with time computations
8	Implementation of quick sort algorithm with time computations
9	Implementation of Heap sort with time computations
10	Implementation of horspool's string matching algorithm
11	Implementation of Backtracking and branch and bound based travelling sales person problem
12	Implementation of Dijkstra's algorithm
13	Implementation of floyd's and warshall's algorithm

### **UE18CS256 – MICROPROCESSOR AND COMPUTER ARCHITECTURE LABORATORY**

# of Credits: 1 # of Weeks: 13

0- 0-00-0	01 ((0015) 15
Laboratory Title	Microprocessor & Computer Architecture Laboratory (UE18CS256)
For the Class	B. Tech. 4 <sup>th</sup> Semester 2018-2022 batch
Preamble	Microprocessor & Computer Architecture Laboratory is a core course and complements the theory course in Microprocessor & Computer Architecture. In the theory course, the students study ARM and MIPS architecture and Instruction set. In the lab course, the students implement assembly language programs and sensor based project using Arduino microcontroller.
Objective	<ul> <li>Implement assembly language programs and develop strong competencies in contemporary ISAs.</li> <li>Develop, edit, compile and debug assembly language programs using present - day simulators.</li> <li>Know various addressing modes that are defined in a given instruction set architecture and illustrate how machine language instructions in that architecture identify the operand(s) of each instruction.</li> <li>Practice interfacing experiments using various sensors with Arduino board.</li> <li>Learner to imbibe the skills of formulation of a complex problem, design a suitable solution using Arduino/ Raspberry Pi processors and demonstrate the end results.</li> </ul>
Outcome	At the end of the course, the student will be able to:  • Inculcate the importance of instruction set architecture and their fundamental concepts using assembly language programming.  • Demonstrate editing, compiling, executing and debugging an assembly



language program of a contemporary microprocessor.

- Demonstrate the usage of subroutines and recursion supported by the ISA.
- Imbibe strong assembly language programming skills by implementing solutions to problems using simulators.
- Instilling the idea to formulate a complex problem definition, approach to solve the problem, methodology to apply and implement suitable algorithm and check for the final results.

Session	Tasks
1	Introduction to Instruction Set – ARM Processor. Sample programs using Simulator.
2	Programs on ARM/ x86 using Simulator.
3	Programs on ARM/ x86 using Simulator.
4	Demonstration of Arduino Microcontroller with various sensors, Project Discussion.
5	Project Discussion (Playing Previous year Video) for Half an Hour. Project Title confirmation.
6	Programs on ARM/ x86 using Simulator.
7	Introduction to 3 stage Pipeline using simulator and Pluggins.
8	Introduction to 5 stage Pipeline (case study: Hazards using Simulator - RAW, WAR, WAW).
9	Cache Memory Demonstration.
10	Mini Project
11	Mini Project
12	Mini Project
13	Project Evaluation

### **UE18CS257X - MATLAB FOR IMAGE PROCESSING (2-0-0-0-2)**

# of Credits: 2 # of Hours: 28

	CHAPTER TITLE/REFE	UNI			ORTIONS ERED
Class	RENCE LITERATUR E	T NO	TOPICS TO BE COVERED	REFER ENCE CHAPT ER	CUMUL LATIVE
1	Chapter 1:	I	MATLAB's Power of Computational Mathematics, Features of MATLAB Uses of MATLAB, Basic Syntax, variables,	17.85	17.85
2-3 4-5	Introduction and Fundamentals	1	commands data types, operators, m - files	17.03	17.03
6-7			Decision making, loops, matrix operations: scalar operations,		
8-9	Chapter 2: Programming	II	transpose, determinant, Inverse, concatenation, vectors, arrays, colon notation, strings, functions	21.42	39.27
10-11	using MATLAB		<b>Project</b> : Image Interpolations using loops, Image Smoothing		
12-13	Chapter		Acquiring and Importing Data, Image Enhancement		
14-15	3,10,11: Image	III	Image Deblurring, Edge Detection Image Segmentation	21.42	60.69
16-17	Processing		<b>Project</b> : Filling gaps in the text using Morphological Processing		
18-21	MathWorks, Compter	IV	Funtions for Video Processing using Computer Vision Toolbox VideoReader(), VideoWriter() etc.	21.42	82.11



	Vision Tool		Using Video Processing functions extracting		
	Box:		Frames from video		
	Video		<b>Project:</b> Face Feature Detection		
22.22	Processing				
22-23	functions				
	(Reference				
	Material/ Links				
	will be				
	provided)				
24-25			Object Detection and Recognition		
	MathWorks,		Object Tracking and Motion Estimation		
	Compter				
	Vision Tool				
	Box:	V		17.00	100
26-28	Case study	V		17.89	100
	(Reference				
	Material/ Links				
	will be				
	provided)				

#### Literature:

Reference	mid o A d	Publication Info		
books	Title & Author	Edition	Year	
1	Digital Image Processing using MATLAB, Rafael C. Gonzalez, Richard E. Woods, Steven L. Eddins	2 <sup>nd</sup> Edition	2009	

### **UE18CS257X - ANDROID APPLICATION DEVELOPMENT (2-0-0-0-2)**

# of Credits: 2 # of Hours: 28

	Chapter			% of Portion	ons Covered
Class #	Title/Ref erence Literatur e	Unit#	Topics to be Covered	% of Syllabus	Cumulative
1	Android Program ming,		Android Programming: Introduction to Android, Required Tools, Anatomy and Creation of First Android Application, Using the Android Emulator		
2	Activities		Activities: Understanding Activities		
3	,	I	<b>Intents</b> : Linking Activities Using Intents	20	20
4	Fragmen ts, and		Calling Built-In Applications Using Intents		
5	Intents:		Fragments		
6			Displaying Notifications		
7			<b>User Interface</b> : Understanding the Components of a Screen, Adapting to Display Orientation		
8	User		Managing Changes to Screen Orientation, Utilizing the Action Bar		
9	Interface , Designin	II	Creating the User Interface Programmatically, Listening for UI Notifications	21	41
10	g User Interface		<b>Designing User Interface with Views:</b> Using Basic Views, Using Picker Views		
11	with		Using List Views to Display Long Lists		
12	Views		Menu		
13			<b>Data Persistence</b> : Saving and Loading User Preferences		
14	]		Persisting Data to Files		
15	Data		Creating and Using Databases		
16	Persisten ce,	III	<b>Content Providers</b> : Sharing Data in Android	25	66
17	Content		Using a Content Provider		
18	Provider		Creating Your Own Content Providers		
19	s		Using the Content Provider		



20			Messaging: SMS Messaging, Sending E-mail Services in Android		
21	Messagin	IV	<b>Android Services</b> : Types of Services: Local Service, Remote Service, Intent Service.	19	85
22	g, Android Services		Broadcast Receivers, Types of Broadcasts		
23	Services		Creating a Broadcast Receivers Introducing Notifications		
25	Location Based		<b>Location Based Services</b> : Location-Based Services, Configuring the Emulator to Test Location-Based Services		
26	Services, Publishi	V	Updating Locations in Emulator Location Providers	15	100
27	ng Android		Selecting a Location Provider, Finding Your Location,		
28	Applicati ons		<b>Publishing Android Applications:</b> Preparing for Publishing, Deploying APK Files		

#### Literature:

Reference	TT'.1 0 A .1	Publication Info	
books	Title & Author	Edition	Year
1	Beginning Android Application development", Wei-Meng Lee, Wiley Publishing,	1	2011
2	Professional Android 2 Application Development by Reto Meier, Wiley Publishing, 2010	1	2010
3	Android 4.2 App Development Essentials, Neil Smyth	1	2013
4	The Busy Coder's Guide to Android Development, Mark L. Murphy	1	2009

### **UE18CS257X - SECURED PROGRAMMING WITH C (2-0-0-0-2)**

# of Credits: 2 # of Hours: 28

	Chapter		% of Portions Covered		
Class #	Title / Referenc e Literatu re	Topics to be covered	% of Syllabus	Cumulative %	
	Unit #1	Introduction			
1	Introduction	Need for Secure Programming,	_		
2	T1: Chapter 1:	Set- UID Programs: Set-UID Mechanism, what can go wrong,			
3	Set-UID Programs Chapter 2:	Attack Surfaces of Set-UID Programs, Invoking other programs, Principle of Least Privilege;	21.42	21.42	
4	Environmen	Environment variable and attacks:			
5	t Variables and Attacks	Attack surface, Attack via External Program,			
6		Case study			
		Integer Security			
7	Unit #2	Integer- Unsigned Integers			
8	Integer	Integer security, Representation,			
9	Security	Conversions, Operations;			
10	T2:	Signed Integers- Operations	17.86	39.28	
11	Chapter 5: Integers	Vulnerabilities, Mitigation strategies			
	Unit #3	String Security and Stack Buffer Overflow			
12	String	Strings: Common String Manipulation Errors,			
13	Security and Stack Buffer	String Vulnerabilities- Program Stacks,	25	64.28	



		(Jan – May, 2020)		
14	Overflow	Buffer Overflow attack- Program memory layout,		
15	T1: Chapter 4: Buffer	Stack and Function Invocation,		
16	Overflow Attack	Stack Buffer Overflow Attack,		
17	T2: Characters	Address Randomization,		
18	and Strings	StackGuard		
		Format String Vulnerability		
19	Unit #4 Format String	Format String Vulnerability- Functions with variable number of arguments,		
20	Vulnerability	Format string with Missing optional Arguments		
21	T1: Chapter 6: Format	Vulnerable program and Experiment Set up		
22	String Vulnerabilit	Exploiting format string Vulnerability-Attack 1, 2,3	17.86	82.13
23	У	Exploiting format string Vulnerability-Attack 4,5, Countermeasures		
	Unit #5	Return to libc and Race Condition Vulnerability		
24	Return to libc and Race	Arc Injection: Return-to-libc Attack		
25	Condition Vulnerability	Attack Experiment Setup,Launch attack		
26	T1: Chapter 5: Return to	Race Condition Vulnerability- Race condition problem,	17.86	100
27	Libc Attack	Vulnerability and		
28	Chapter 7 : Race condition Vulnerability	Countermeasures		



#### Literature

Pools Type	Code	Code Title & Author		Publication Information			
Book Type	Code	True & Audior	Edition	Publisher	Year		
Text Book	T1	Computer Security A	1	CreateSpac	2017		
	11	Hands-on Approach, - Wenliang Du	1	e	2017		
	T2	SEI CERT- C Coding Standard					
		Rules for Developing Safe, Reliable	1	SEI-CMU	2016		
		and Secure Systems					

### **UE18CS275X – WEB FRAMEWORKS (2-0-0-0-2)**

# of Credits: 2 # of Hours: 28

Class #	Chapter Title / Reference Literature	Topics to be Covered	% of Porti	on covered
			% of Syllabus	Cumulative %
1	Unit#1 Introduction	What are frameworks? Frameworks vs Libraries		
2		Classification of Frameworks, Selecting Frameworks,	45.05	
3		Client-Server Architecture, HTTP,	17.85	17.85
4		MVC Pattern		
5		MVC Pattern		
6	Unit#2	Django – Introduction & Installation		
7	Django - I	Django Models	-	
8		Django Admin	21.42	39.27
9		Django URLs		33,27
10		Django Views		
11		Django Views		
12		Django ORM	21.42	60.69
13		Dynamic Data in Templates		



		(Jan – May, 2020)		
14		Django Templates		
15		Extending Templates		
16		Django Forms		
17		Django Forms		
18	Unit#4	Introduction, Building a React App		
19	React.js	Components		
20		State		
21		Accessing DOM Elements	21.42	82.11
22		Events		
23		Redux		
24	Unit #5	Interaction between frameworks		
25	Misc.	Connecting React & Django		
26	frameworks	Connecting React & Django		
27		Adopting a Framework with ease (Self-learning Assignment)	17.89	100
28		Adopting a Framework with ease (Self-learning Assignment)		

### Literature

			Publication Information		
Book Type	Code	Title & Author	Editio n	Publisher	Year
Text Book	T1	Django Girls	1	Online Tutorial @ https://tutorial.djangogirls.org/en/	
Text Book	T2	Learning React, Kirupa Chinnathambi	1	Online Tutorial @  https://www.kirupa.com/react/	



### UE18CS208A - OPERATION LINUX/UNIX : \*nix (2-0-0-0-2)

(Start your own internet company with \*nix)

Class #	Chapter Title / Reference Titles	Topics to be Covered	Coverage	Cumulative %
Unit 1 In		NIX : Textbook 1: Chapters 2,4,5,6, 9		
1	Introduction to UNIX	Salient Features, Layered Architecture, Concept of Shell and Kernel, File System, Concept of Process		
2	File System Related Commands	Creating and Removing Directories and Files, View the Content of a File, Copying/Moving Files, Hard Links/ Symbolic Links, Inode Structure, View the content of the directory		
3	Different Types of Files	Regular Files, Directories, Character and Block Device Files, Named Pipes		
4		Socket Files, File Permissions - Effective User ID, Sticky Bit.	200/	
5	Modifying File Permissions	Effect of File Permissions on Different Commands of UNIX.	20%	20%
6	Time Stamp	Access Time, Modification Time and Change Time, Effect of Different Commands on These Times.		
7	Process	Process Related Commands		
8		Concept of Process, Process Status, Child and Parent Process, Process ID		
9		Orphan and Zombie, Background and Foreground Processes, Executing process at a particular time	-	
	Uni	t 2 Shell and Scripting T1:Chapters 8,12,13.1-13.	3, 14	
12	Shell, Filters and Shell Scripting	Meta Characters, Redirection and Piping, Filters – sort, uniq, grep, wc, tail and head, Variables.		
13		Input/ Output and Assignment, Quoting – Single Quotes, Double Quotes and Back Quotes, Shell Scripts – Executing Shell Programs in Different Ways,	30%	50%
14		Environment Variables – Export Command, Relational and Logical Operators – Test Command		
15		Selection – if and case Commands		
16		Looping – while, until and for Loops.		



		(Jan – May, 2020)		
17		Command Line Arguments and Shift Command		
18		Arithmetic in Shell Programs		
19		Calling Another Shell Program Within Another		
		shell program, Recursion, User defined functions		
		Unit 3 *nix operations V		
23	Linux	Install Ubuntu Linux, learn different installs, basic		
		commands, install packages		
24	Firewalls	Types of firewall, install and configure Ubuntu		
		Firewalls		
25	Load balancers	Types of Load balancers, install and configure		
		open source load balancers		
26	Webservers	Types of webservers, install and configure Apache		
		webservers. Learn to install certificates and run an	30%	80%
		https web servers	30%	
27	Application	Types of application servers, install and configure		
	Servers	Django and foreword to develop apps		
28	Database	Types of database servers, install and configure		
	Servers	mysqli. Learn to make database calls from the app		
		server and webserver		
29	Intrusion	Types of intrusion detection servers (IDS), install		
	Detection	and configure open source IDS		
		Unit 4: Trouble Shooting *nix V		
30	Trouble	Learn different commands, tcpdump, traceroute,		
	shooting	basic booting of a server. Learn top 10 trouble	10%	90%
	Techniques	shooting techniques and commands with *nix		
		Unit 5: Setting up your own internet company V		
31	Setting up your	Internet applications - Basics of an internet		
	own internet	application and working, learn to install		
	company	webserver with app server and database server.		
		Learn to run and track app with packets traversing	10%	100%
		across all the different tiers. Web Hosting	10 /0	
		services, choosing a technology service provider,		
		performance metrics. Basics of Finance and misc		
		basic Legal Matters		

#### Literature

Book Type	Code	Title & Author	Publication Information		
			Edition	Publisher	Year
Text Book	Т	UNIX Concepts and Applications,	4	McGraw Hill	2012
		Sumitabha Das		Education	
	R1	Classic Shell Scripting, Arnold Robbins	1	O'Reilly Media;	2005
Reference		and Nelson H. F. Beebe			



Book				
Videos	V1	Links will be shared in the class through	YouTube and	
		an LMS (learning management system) Platform	other	