ECEN 4613/5613 | **Embedded System Design** | **Fall 2014**

Lab #4 Signoff Sheet

You will need to obtain the signature of your TA on the following items in order to receive credit for your lab assignment. Signatures are due by **Friday, November 14, 2014 (Required Elements) and Wednesday, November 19, 2014 (Supplemental Elements)**. Labs completed late will receive grade reductions.

Print your name below, sign the honor code pledge, circle your course number, and then demonstrate your working hardware & firmware in order to obtain the necessary signatures. All items must be completed to get a signature, but partial credit is given for incomplete labs. Receiving a signature on this signoff sheet does not mean that your work is eligible for any particular grade; it merely indicates that you have completed the work at an acceptable level.

Student Name: ANIKET KUMAR LATA

Honor Code Pledge: "On my honor, as a University of Colorado student, I have neither given nor received unauthorized assistance on this work. I have clearly acknowledged work that is not my own."

Student Signature: _____

Signoff Checklist

Required Elements

- ☑ Pins and signals labeled and decoupling capacitors present on board
- ☑ LCD functional, C code for basic LCD routines functional — check comments
- ☑ LCD control signal timing meets specifications (diagram) — Provide timing spec in submission ↳ theoritical & pratical
- ☑ Serial EEPROM functional, contents present after power cycle
- ☑ C code for EEPROM functional, I²C timing correct
- ☐ LCD Display and hex dump of EEPROM — Works only for page blocks

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TA/Instructor signature and date

Supplemental Elements (Qualifies student for higher grade.)

- ☑ Elapsed time display (accurate 1 second resolution) — 6sec → overhead for 1 min
- ☑ Elapsed time stop, restart, reset to "00:00.0":
- ☑ Support for custom LCD characters, fun logo — Not hardcoded → fun logo
- ☑ Good integration with previous code, all functions work with no irregularities

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Supplemental Elements (Qualifies student for higher grade.)

- ☑ PCF8574 I²C I/O Expander — Cannot set the pins bitwise
- ☑ EEPROM `eereset()` and WDT functional and correct

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FOR TA/INSTRUCTOR USE ONLY Required Elements	Not Applicable	Poor/Not Complete	Meets Requirements	Exceeds Requirements	Outstanding
Schematics, SPLD code	☐	☐	☐ ✓	☐	☐
Hardware physical implementation	☐	☐	☑	☐	☐
Required Elements functionality	☐	☐ ✓	☐	☐	☐
Sign-off done without excessive retries		☐	☑		
Student understanding and skills	☐	☐	☐ ✓	☐	☐
Overall Demo Quality	☐	☐	☑	☐	☐

FOR TA/INSTRUCTOR USE ONLY Supplemental Elements	Not Applicable	Below Expectation	Meets Requirements	Exceeds Requirements	Outstanding
Supplemental Elements functionality	☐	☐	☑	☐	☐
Sign-off done without excessive retries		☐	☑		
Student understanding and skills	☐	☐	☐	☑	☐
Overall Demo Quality	☐	☐	☐ ✓	☐	☐

TA/Instructor Comments ☐☐☐ NOTE: This signoff sheet should be the top sheet of your submission.

- ☐ Optional Challenge: Measure LCD DDRAM search performance
- ☑ Optional Challenge: Measure EEPROM byte/page write times — Page write → 332ms Byte write = 972ms
- ☑ Optional Challenge: Measure EEPROM Block Fill performance — Block fill → 17ms → provide screenshots

- Schematics - value of the POT?
- decoupling for the LCD
- Hard coded functions for testing the LCD drivers-no

P.T.O

- Wrapping using lcd putstr not proper goes to line 3 from line 1 ~~after~~ instead of line 2.
- No Error handling for ~~4~~ line 4 - not on LCD
* Use a variable to hold the previous state of the port pins.
- Page write for ~~not m~~ not handled for ~~not~~ non-multiples of 16.