You will need to obtain the signature of your instructor or TA on the following items in order to receive credit for your lab assignment. This assignment is due by Friday, October 17, 2014 (Required Elements) and Wednesday, October 22, 2014 (Supplemental Elements). Labs completed after the due date will receive grade reductions.

Print your name below, sign the honor code pledge, circle your course number, and then demonstrate your working hardware & firmware in order to obtain the necessary signatures. All items must be completed to get a signature, but partial credit is given for incomplete labs. Receiving a signature on this signoff sheet does not mean that your work is eligible for any particular grade; it merely indicates that you have completed the work at an acceptable level.

Honor Code Pledge: "On my honor, as a unauthorized assistance on this work. I h	a University of Colora ave clearly acknowled	do student, I have n ged work that is no	neither given not my own."	or received	
	Student Signatur	e:	nos		
Signoff Checklist					
Required Elements					
Schematic of acceptable quality (all	components shown)				
Pins and signals labeled, decoupling		3-pin wire wrap soc	kets present or	a board	
Very good knowledge of a terminal Demonstrates all 32KB of XRAM is		stianal including m	itaa blaab G	II	
Using PAULMON2, demonstrates h	nighest hand rate as:	9600 Los	ionitor block ii	ii command	
Knows how to use SDCC [Code::Bl					
Knows how to analyze output files (	.RST, .MEM, .MAP) f	or correct addresses	2/1		
C serial program and virtual debug	port functional and cod	e commented (	Pale!	10/17/11.	
Hex display of buffer contents	lighty incorre		Your	19/17/14	
Supplemental Elements (Qualifies stude	nts for higher grade)	TA signature	e and date		
PWM control works correctly	nto for migner grade)				
Correctly enters Idle mode and exits	via external interrupt	l			
Correctly enters Power Down mode					
All other PCA software menu items	function correctly		1.	1-1-010-11	
Good understanding of PCA modes	u to was	N	ante	10/22/2014	(.
Good user interface; program is eas	y to use		1		
Instructor/TA Comments: □ □ □		TA signature	e and'date		
FOR INSTRUCTOR USE ONLY	Not Bel	ow Meets	Exceeds		
Required Elements	Applicable Expec	tation Requirement	ts Requirement	nts Outstanding	
Schematics, SPLD code			/ 1		
Hardware physical implementation Required Elements functionality			\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \	8	
Sign-off done without excessive retries			1		
Student understanding and skills					
Overall Demo Quality		J U ,	V U		
FOR INSTRUCTOR USE ONLY	Not Bel	ow Meets	Exceeds		
Supplemental Elements	Applicable Expec	tation Requiremen	ts Requireme	nts Outstanding	
Supplemental Elements functionality				V 0	
Sign-off done without excessive retries				10	
	i i			7 1	
Student understanding and skills  Overall Demo Quality  Comments:  Optional Challenge: PAULMON2 R Optional Challenge: C and Assembly Optional Challenge: Serial ISR Optional Challenge: Floating point a  Schematics  Paulmon  Wing paulm	UN command y interfacing (1) when	1 + 8 - bit in	puls	/ -	27

to buffer 1 The hex dump prints more memory addresses than usuale -> Multiple PCA modes implemented - Software time , PNM. watchdog, high speed output cfune (2,6) (257,256,256,256) Paul Mon man Trus 2 1, NS 10