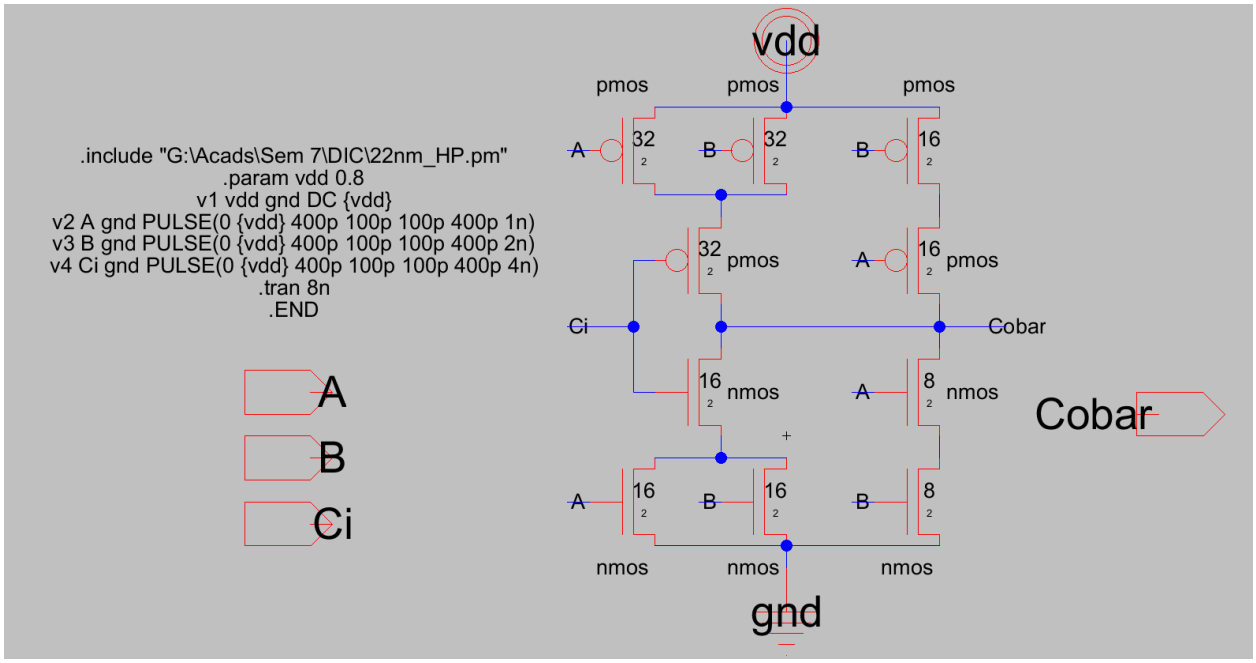
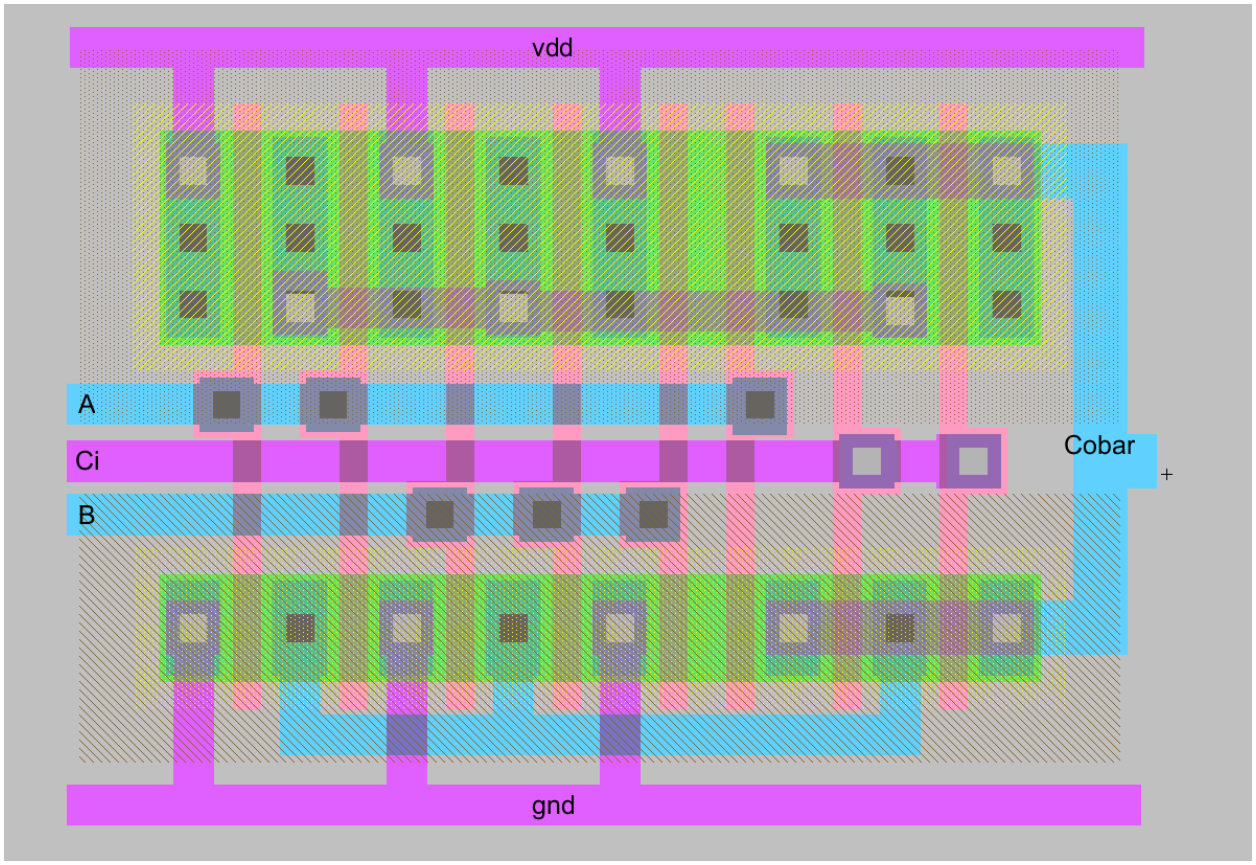
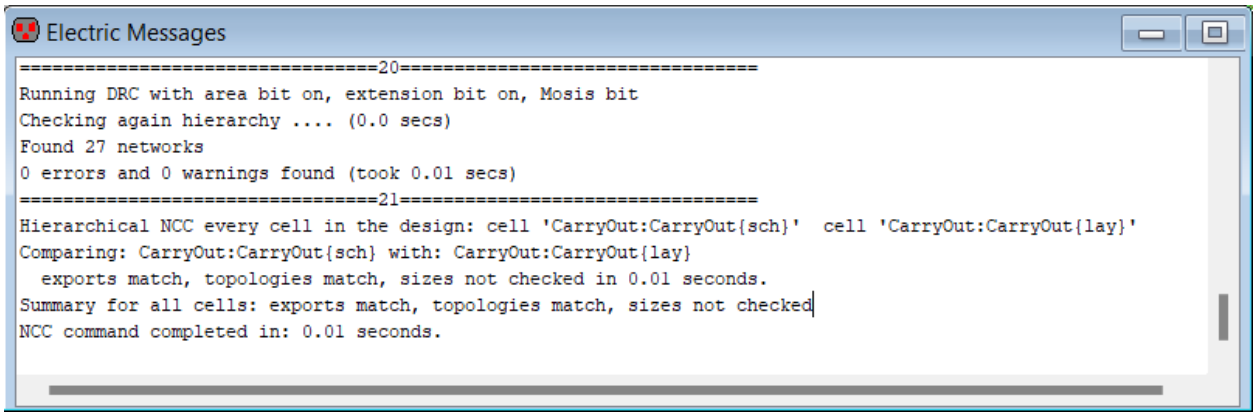
# Schematic of 2X CARRY OUT circuit



# Layout of 2X CARRY OUT circuit




# DRC and LVS results



# Simulation in SPICE to show correct functionality across all input combinations