

WEEK 1
Embedded System

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Section A

Roll no-58

Reg no-190905466

1.

AREA RESET, DATA, READONLY

EXPORT __Vectors

__Vectors

DCD 0X10001000

DCD Reset_Handler

ALIGN

AREA mycode, CODE, READONLY

ENTRY

EXPORT Reset_Handler

Reset_Handler

LDR R0, =SRC

LDR R1, [R0]

STOP

B STOP

SRC DCD 8

END

The screenshot displays a debugger interface with the following components:

- Registers Window:** Shows the state of various registers. R0 is 0x00000010, R1 is 0x00000008, and R15 (PC) is 0x0000000C. Other registers like R2 through R14 and xPSR are at 0x00000000.
- Disassembly Window:** Shows assembly instructions at memory addresses 4802, 4801, and E7FE. The instructions are:
 - 4802: LDR r0, [pc, #8] ; @0x00000014
 - 4801: LDR r1, [r0, #0x00]
 - E7FE: B 0x0000000C
- Source Code Window:** Displays the assembly code for a reset handler:

```
1
2 AREA RESET, DATA, READONLY
3 EXPORT __Vectors
4
5 __Vectors
6
7 DCD 0x10001000
8 DCD Reset_Handler
9 ALIGN
10 AREA mycode, CODE, READONLY
11 ENTRY
12 EXPORT Reset_Handler
13
14 Reset_Handler
15
16 LDR R0, =SRC
17 LDR R1, [R0]
18
```
- Command Window:** Shows the command "Load" and the path "C:\\Users\\aniket\\Desktop\\desktop\\sem4\\es\\labs solved\\week1\\Objects\\v".
- Call Stack - Locals Window:** Shows a single entry with Name "0x00000000", Location/Value, and Type.

2.

AREA RESET, DATA, READONLY

EXPORT __Vectors

__Vectors

DCD 0X10001000

DCD Reset_Handler

ALIGN

AREA mycode, CODE, READONLY

ENTRY

EXPORT Reset_Handler

Reset_Handler

LDR R0, =SRC

LDR R1, =DST

LDR R3, [R0]

STR R3, [R1]

STOP

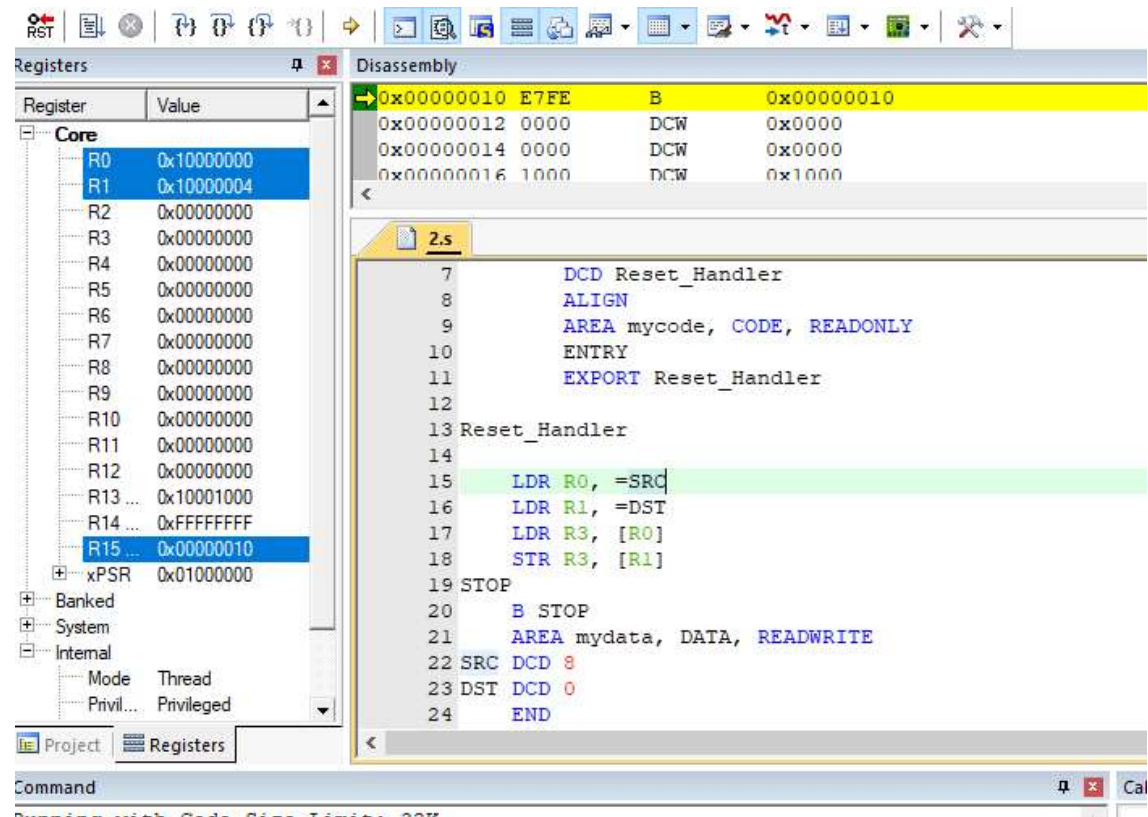
B STOP

AREA mydata, DATA, READWRITE

SRC DCD 8

DST DCD 0

END



3.

AREA RESET, DATA, READONLY

EXPORT __Vectors

__Vectors

DCD 0X10001000

DCD Reset_Handler

ALIGN

AREA mycode, CODE, READONLY

ENTRY

EXPORT Reset_Handler

Reset_Handler

LDR R0, =SRC

LDR R1, =DST

MOV R3, #10

AGAIN LDR R4, [R0], #4

STR R4, [R1], #4

SUBS R3, #1

BNE AGAIN

STOP

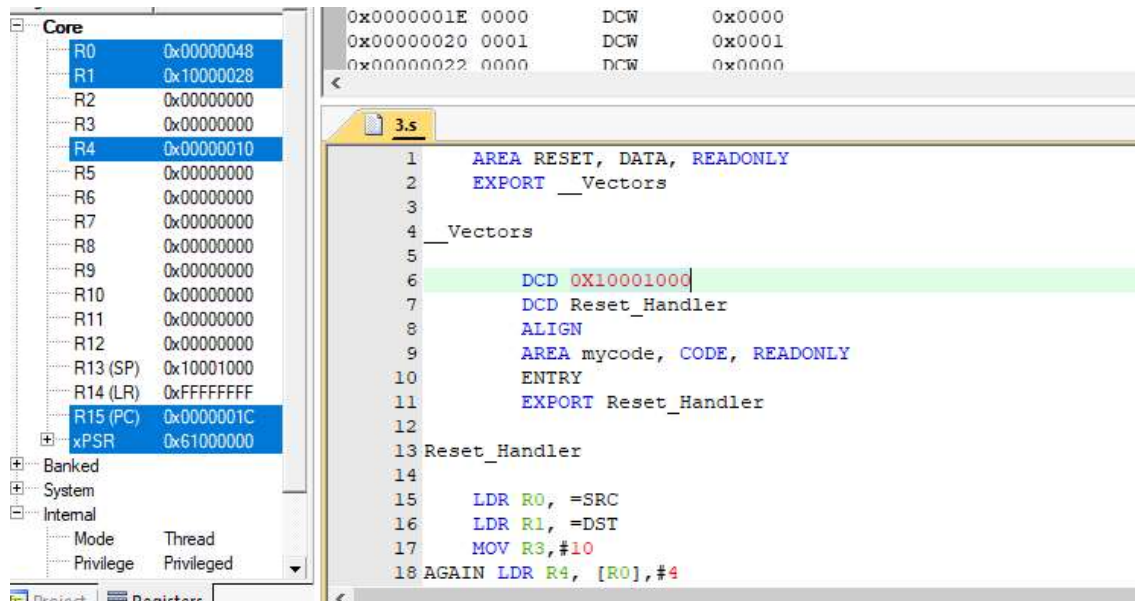
B STOP

SRC DCD 0x01,0x02,0x03,0x04,0x05,0x06,0x07,0x08,0x09,0x10

AREA mydata, DATA, READWRITE

DST DCD 0

END



4.

AREA RESET, DATA, READONLY

EXPORT __Vectors

__Vectors

DCD 0x10001000

DCD Reset_Handler

ALIGN

AREA mycode, CODE, READONLY

ENTRY

EXPORT Reset_Handler

Reset_Handler

LDR R0,=SRC

LDR R1,=SRC

ADD R1,#36;shift the R1 to the last location i.e $8*9=36$

MOV R2, #5;total 5 iterations

AGAIN LDR R3,[R0]

LDR R4,[R1]

STR R3,[R1], #-4

STR R4,[R0], #4

SUBS R2, #1

BNE AGAIN

STOP

B STOP

AREA mydata, DATA, READWRITE

SRC DCD 0x01,0x02,0x03,0x04,0x05,0x06,0x07,0x08,0x09,0x10

END

Registers

Register	Value
Core	
R0	0x10000014
R1	0x10000010
R2	0x00000000
R3	0x00000000
R4	0x00000000
R5	0x00000000
R6	0x00000000
R7	0x00000000
R8	0x00000000
R9	0x00000000
R10	0x00000000
R11	0x00000000
R12	0x00000000
R13 (SP)	0x10001000
R14 (LR)	0xFFFFFFFF
R15 (PC)	0x00000024
xPSR	0x61000000
Banked	
System	
Internal	
Mode	Thread
Privilege	Privileged

Disassembly

0x00000024 E7FE B 0x00000024

0x00000026 0000 DCW 0x0000

0x00000028 0000 DCW 0x0000

0x0000002A 1000 DCW 0x1000

4.s

7 DCD Reset_Handler

8 ALIGN

9 AREA mycode, CODE, READONLY

10 ENTRY

11 EXPORT Reset_Handler

12

13 Reset_Handler

14

15 LDR R0,=SRC

16 LDR R1,=SRC

17 ADD R1,#36;shift the R1 to the last location i.e 8*9=36

18 MOV R2, #5;total 5 iterations

19 AGAIN LDR R3,[R0]

20 LDR R4,[R1]

21 STR R3,[R1], #-4

22 STR R4,[R0], #4

23 SUBS R2, #1

24 BNE AGAIN