```
1   ;EXPERIMENT 3:WRITE AN ALP TO INTERFACE DAC AND GENERATE SINE
    WAVEFORM
2   ;NAME:AAADESH PISE
3   ;EXPERIMENT 3 TO GENERATE SINE WAVE USING DAC
4   ;ROLL N0:312041
5   ;BATCH:B2
6   ;DATE OF PERFORMANCE:1/8/2018
7
8   ORG 000H
9   CLR A                    ;clr accumulator
10  L1:MOV DPTR,#SINEWAVE    ;taking sine wave content in dptr
11  MOV R0,#72
12  CLR P0.7                 ;enable the dac wr line
13  L2:MOVC A,@A+DPTR        ;load content in accumulator
14  MOV P1,A                 ;load data in port 1
15  CLR A
16  INC DPTR                 ;taking next content
17  DJNZ R0,L2
18  SJMP L1                  ;continuous waveform
19
20  ORG 0100H
21  SINEWAVE:
22  DB
    128,138,150,160,170,180,191,201,209,217,224,231,238,242,246,250,253,
    254
23  DB
    255,254,253,250,246,242,238,231,224,217,209,201,191,181,170,160,150,
    115
24  DB 128,117,106,96,84,74,64,55,46,38,31,25,18,13,9,5,2,1,0
25  DB 0,1,2,5,9,13,18,25,31,38,46,55,64,74,84,96,106,117,126
26          END
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