Aniraj Kesavan

Curriculum Vitae

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Education

2015–2017 Master of Science, University of Utah, U.S.A, GPA – 3.6.

Computer Science

2008–2012 Bachelor of Technology, Cochin Univ. Science and Technology, India, 66%.

Computer Science and Engineering

Masters Thesis

Title Making Large Transfers Fast For In-Memory Databases In Modern Networks

Advisor Prof.Ryan Stutsman

Description This thesis explored how we can leverage scatter gather DMA technology from modern NICs

to accelerate performance of large transfers for In-Memory Databases.

Experience

2015-Present Graduate Research Assistant, UTAH SCALABLE COMPUTER SYSTEMS LAB, Salt Lake City, Utah, U.S.A.

- Advised by Prof.Ryan Stutsman.
- o Research focusses on how modern networking hardware influence performance of large transfers of In-Memory Data.
- Published Work:
 - To Copy or Not to Copy: Making In-Memory Databases Fast on Modern NICs A.Kesavan, R.Stutsman, R.Ricci - Proceedings of the Fourth International Workshop on In-Memory Data Management and Analytics (IMDM) 2016 at VLDB 2016, New Delhi, India.
- Also worked for Flux Research Group under Prof.Robert Ricci developing profiles for the Apt Cluster in Cloudlab.

Summer 2016 Site Reliability Engineering Intern, LINKEDIN, Mountain View, California, U.S.A.

Interned with the Monitoring Infrastructure team. Developed an out of band monitoring solution and some state driven automation.

2014–2015 **R&D Engineer - Pipeline**, DREAMWORKS ANIMATION, Bengaluru, India.

- Developed and re-engineered production pipeline for the Animation Studio.
- Work based on flow based programming and graph theory.
- Credited as R&D Engineer for three Hollywood productions IMDB profile.

2012–2014 Associate Software Engineer, Zynga, Bengaluru, India.

o Developed and maintained zBase, the flagship NoSQL database in use at Zynga at that time.

Positions of Responsibility

2015–2016 Membership Chair, ACM Student Chapter, University of Utah.

2015 Chairman & Founder, Quiz Club, DREAMWORKS ANIMATION INDIA.

2011–2012 Student Coordinator, Placement Cell, Model Engineering College, India.