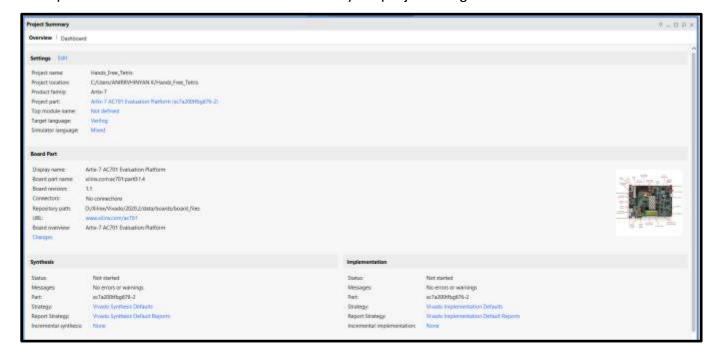
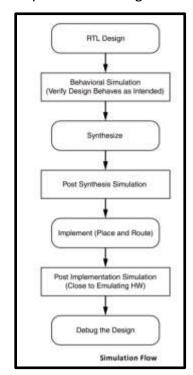
## **IMAGES**

The specifications of Artix 7 FPGA and summary of project are given below:



### **BITSTREAM GENERATION:**

The flowchart below represents steps of simulating the design.



### **MACHINE LEARNING:**

# The model summary:

Model: "sequential"			
nouel: sequential			
Layer (type)	Output	Shape	Param #
conv2d (Conv2D)	(None,	254, 254, 32)	320
activation (Activation)	(None,	254, 254, 32)	0
conv2d_1 (Conv2D)	(None,	252, 252, 32)	9248
activation_1 (Activation)	(None,	252, 252, 32)	0
max_pooling2d (MaxPooling2D)	(None,	126, 126, 32)	0
dropout (Dropout)	(None,	126, 126, 32)	0
conv2d_2 (Conv2D)	(None,	124, 124, 64)	18496
activation_2 (Activation)	(None,	124, 124, 64)	0
max_pooling2d_1 (MaxPooling2	(None,	62, 62, 64)	θ
dropout_1 (Dropout)	(None,	62, 62, 64)	0
flatten (Flatten)	(None,	246016)	0
dense (Dense)	(None,	128)	31490176
dropout_2 (Dropout)	(None,	128)	0
dense_1 (Dense)	(None,	64)	8256

```
dense_1 (Dense)
            (None, 64)
                        8256
dense_2 (Dense)
            (None, 4)
                        260
Total params: 31,526,756
Trainable params: 31,526,756
Non-trainable params: 0
42/42 [************************ - 54s 1s/step - loss: 0.7635 - accuracy: 0.7441 - val_loss: 0.0074 - val_accuracy: 0.998
42/42 [-----
       Epoch 3/5
42/42 [---
       Epoch 4/5
42/42 [-
         0.9913
Epoch 5/5
42/42 [--
        0.9930
```

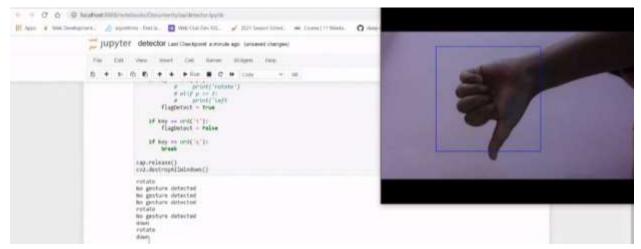
# **BITSTREAM GENERATION:**

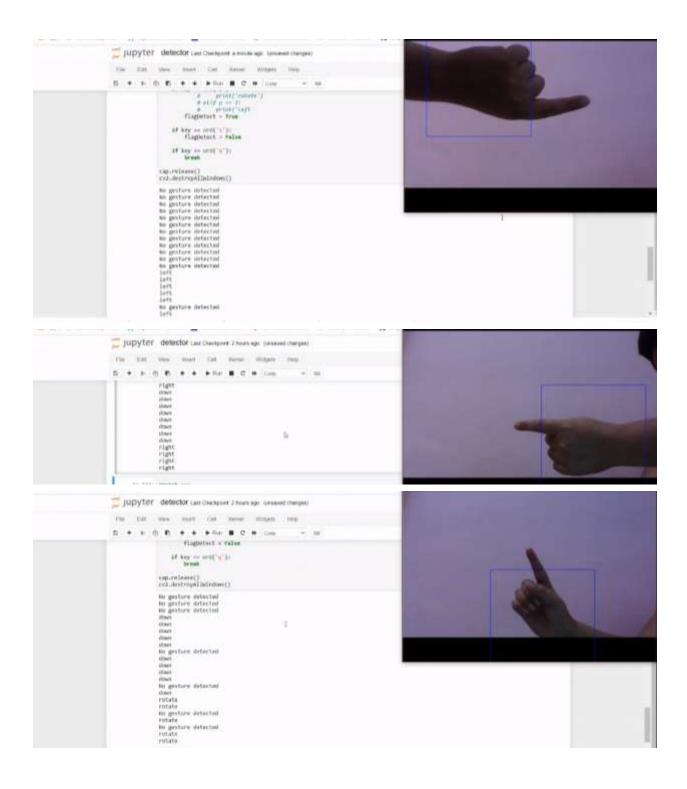
The Bit Stream generated by Vivado is given below:



# **GESTURE DETECTION:**

The given inputs are correctly detected. The simulation results are shown below:





# FINAL OUTPUT:

