SSN College of Engineering
Department of Computer Science and Engineering
III year - UCS1512 – Microprocessors Lab

Academic Year: 2020 -2021   Batch: 2018-2022
Semester: V

Experiment No. 7: BCD Addition and Subtraction

7 a) BCD Addition

Input: 2 BCD numbers (8 bit)
Output: Sum in one location, carry in another location

Hint:
Use DAA instruction to adjust to decimal.

7 b) BCD Subtraction

Input: 2 BCD numbers (8 bit)
Output: Difference in one location (in absolute form), Sign indication in another location.

Hints:
Use DAS instruction.
If carry, find 10's complement of the result (99-result+01) and decimal adjust it.