## Abstractions for programming line-rate routers

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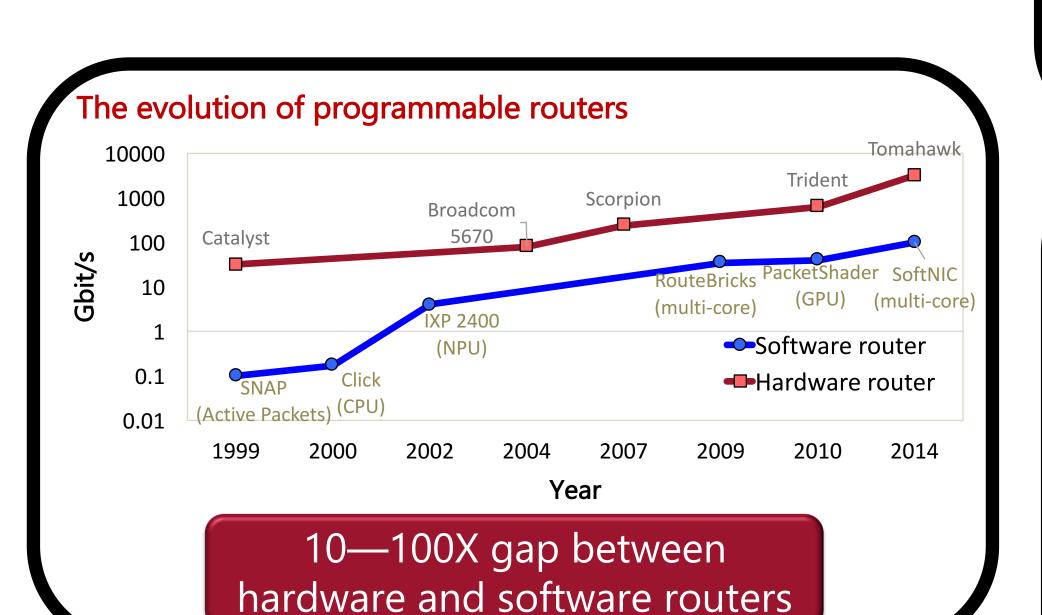




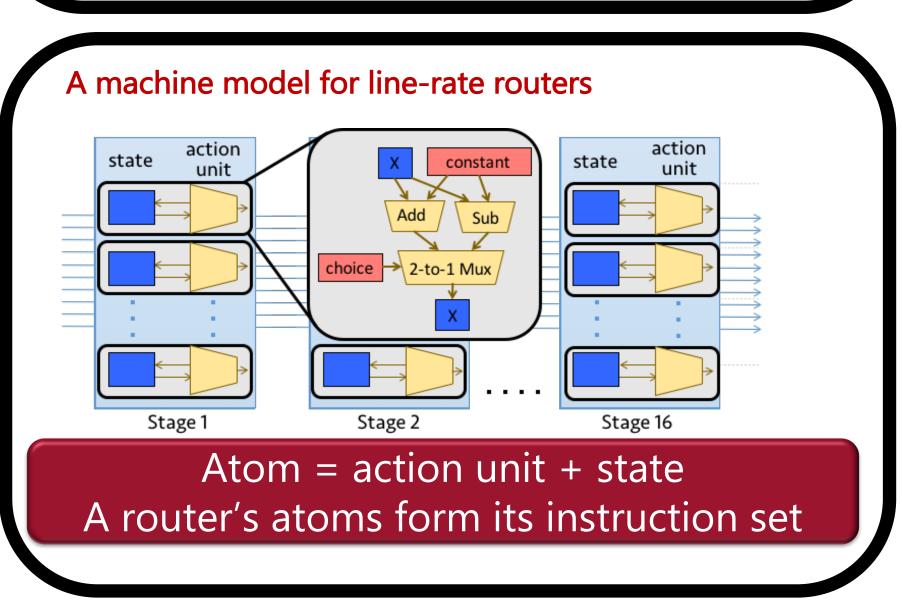


### Programming router data planes at line rate

- Programming: Can we implement a new data-plane algorithm?
  - AQM
  - Scheduling
  - Congestion control
  - Load balancing
- Line rate: Highest speed supported by dedicated hardware



## Packet transactions: high-level data-plane programming Data-plane algorithm For each packet Calculate average queue size if min < avg < max calculate probability p mark packet with probability p else if avg > max mark packet Program in C-like DSL, compile to run at line-rate



# Same performance as fixed-function chips, Some programmability Parser Ingress pipeline Queues/Scheduler Egress pipeline Deparser Scheduler Stage 1 Stage 2 Stage 16 Stage 1 Stage 1 Stage 16 Parser Ingress pipeline Scheduler Egress pipeline Out Stage 1 Stage 16 Parser Ingress pipeline Scheduler Egress pipeline Out Stage 1 Stage 1 Stage 16 Parser Ingress pipeline Stage 1 Stage 2 Stage 16 Parser Ingress pipeline Ingre

### A family of atoms Atom Description Area overhead Least Read or write state 0.04% R/W Expressive RAW 0.07% Read, add, and write back 0.13% PRAW Predicated version of RAW 0.16% **IfElseRAW** 2 RAWs, one each when a predicate is true or false Sub IfElseRAW with a stateful 0.24% subtraction capability Nested 4-way predication (nests 2 0.58% Most IfElseRAWs) Expressive Update a pair of state 0.96% variables

Algorithm	LOC	Stages	Max. atoms/stag e	Min. Atom Required
Bloom filter	29	4	3	R/W
Heavy hitter detection	35	10	9	RAW
Rate Control Protocol	23	6	2	PRAW
Flowlet switching	37	3	3	PRAW
Sampled NetFlow	18	4	2	IfElseRAW
HULL	26	7	1	Sub
Adaptive Virtual Queue	36	7	3	Nested
CONGA	32	4	2	Pairs
CoDel	57	15	3	Doesn't map

### Programmable scheduling is hard

- Decades of scheduling algorithms, but no consensus on abstractions for scheduling. In contrast to:
  - Parse graphs for parsing
  - Match-action tables for forwarding
- The scheduler has very tight timing requirements

Need an expressive abstraction that can run at line rate

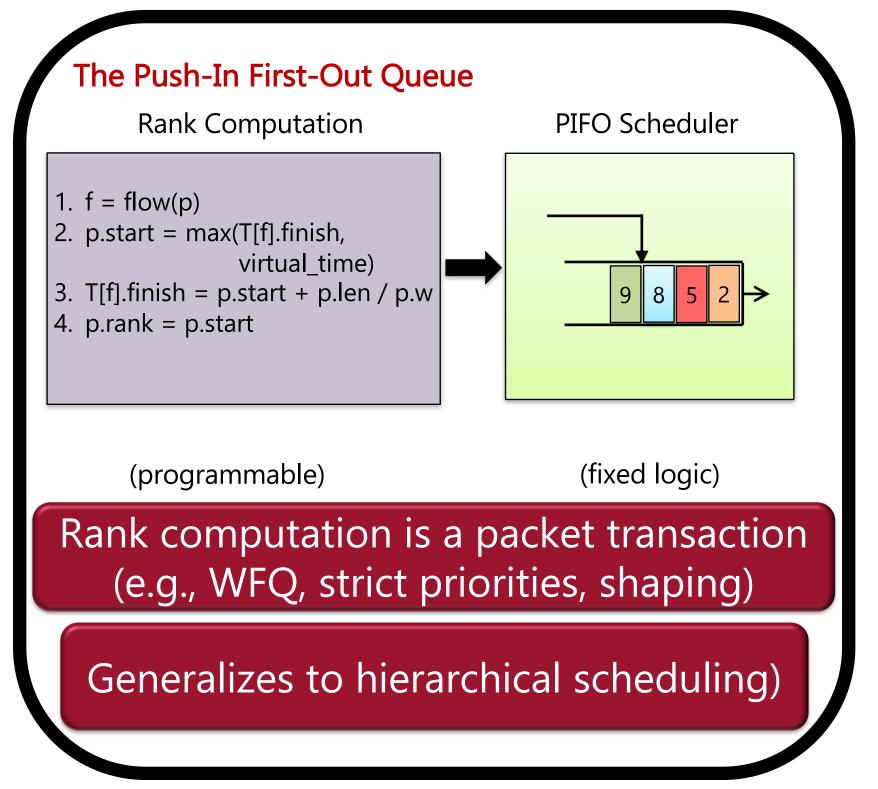
### What does the scheduler do?

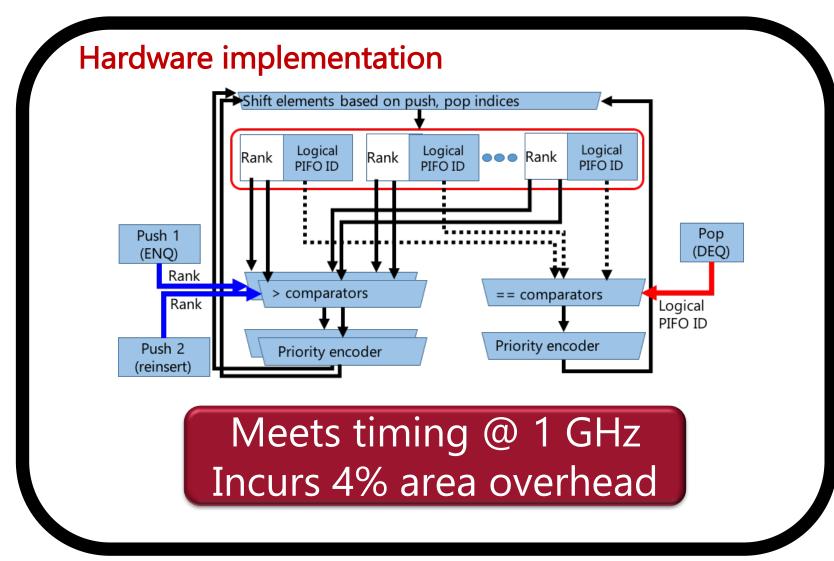
It decides in what order packets are sent

E.g., FCFS, priorities, weighted fair-queueing

### **Key observation**

- In many algorithms, the order can be determined before enqueue
- i.e., relative order of buffered packets does not change





### Two abstractions for line-rate programming

- Packet transactions
- Stateful algorithms
- C-like syntax
- Automatically compile to a router pipeline
- Push-In First-Out (PIFO) queues
- Expresses many scheduling algorithms
- Modest area overhead