Anirudh A S

B.Tech, Electronics and Communications Engineering

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Portfolio: https://anirudhas-2004.github.io/
GitHub: https://github.com/anirudhas-2004

Education

Bachelor of Technology in Electronics and Communications Engineering

PES University 2022 – 2026 (Ongoing)
CGPA: 8.45/10 *(6 of 8 semesters completed)*

Senior Secondary Education (Grade XII)

CBSE Board 2022
Percentage: 90.4%

Secondary Education (Grade X)

CBSE Board 2020
Percentage: 91.6%

Technical Skills

Programming Languages

Python, C, C++

Engineering Software & Tools

Signal Processing & Design: MATLAB (Signal Processing, Control Systems, Antenna & Antenna Array Design Toolbox)
Circuit Design & Simulation: QUCS, Tanner EDA (S-Edit & L-Edit)
FPGA & Hardware Description: Vivado (VHDL, SystemVerilog), Ripes (RISC-V)
Networking & Analysis: GNS3, Wireshark, NetSquid (Quantum Networks)
Electromagnetic Solvers: CST, Ansys Lumerical FDTD
Quantum Computing: Qiskit

Relevant Coursework

- Digital VLSI Design, Analog Circuit Design
- Network Analysis and Synthesis, Digital Signal Processing, Control Systems
- Digital Communications, Computer Communication Networks
- Electromagnetic Field Theory
- Quantum Computing

Projects

FPGA Implementation of Watchdog Timer for FADEC

Aeronautical Development Establishment, Ministry of Defence
(Reconfigurable Logic, Real-Time Digital Systems, Fault-Tolerant Design)
FPGA realization of watchdog timer for Full Authority Digital Engine Control (FADEC).

Simulation of Free Space Quantum Recursive Network Architecture

(Quantum Computing, Quantum Information, Quantum Communication)

Time Server Implementation with NTP Protocol

(IP Protocol Suite, Time Synchronization)
Time server running NTP for accurate timing in embedded applications.

Analog Frequency Divider Design Using FET

(EDA-Based Simulation, Layout Extraction, Jitter/Noise Analysis)
Design and analysis of analog frequency divider circuits using Field Effect Transistors.

On-Site Training/Visits

Gas Turbine Research Establishment (GTRE)

Visited the Kaveri test bed and observed engine reheat runs. Gained insights into sensors used for blade separation detection and data acquisition systems for data collection. Toured various departments including control systems, assembly, calibration, and machining facilities.

U R Rao Satellite Centre (URSC) - ISRO

Toured the clean room facilities, Assembly Integration and Testing (AIT) facility, and Thermovac facility. Gained exposure to satellite manufacturing and testing processes.